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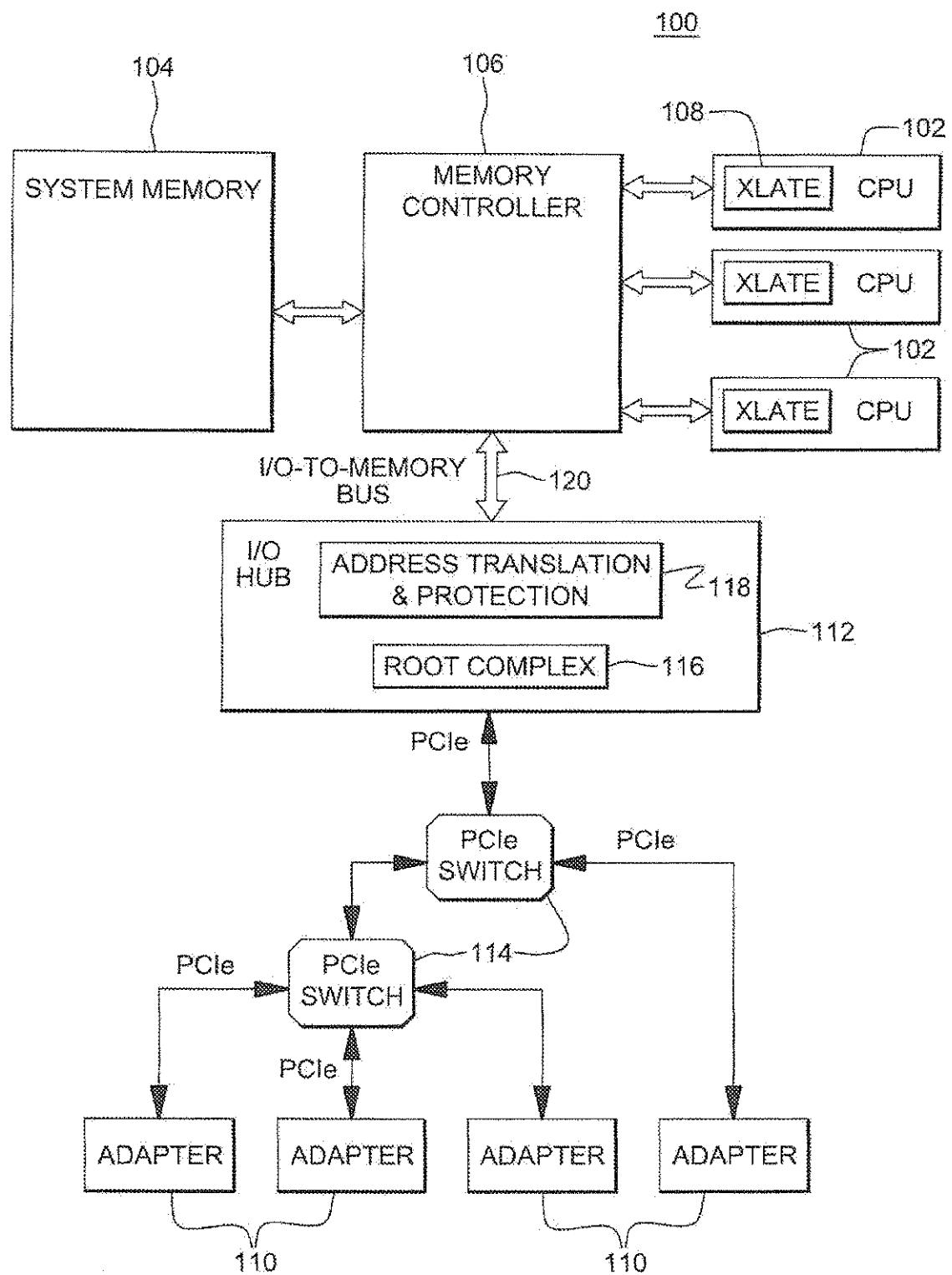


FIG. 1

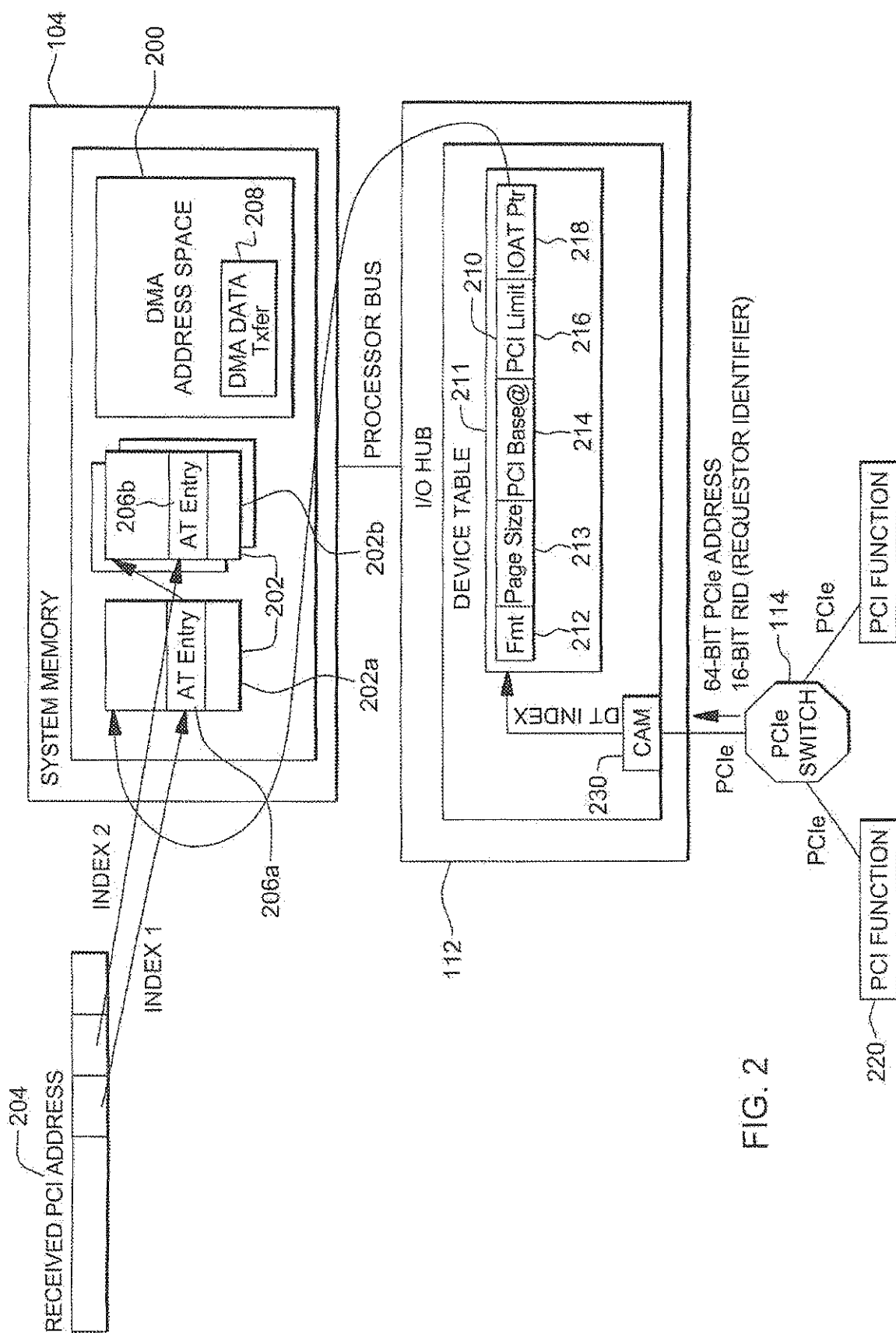


FIG. 2

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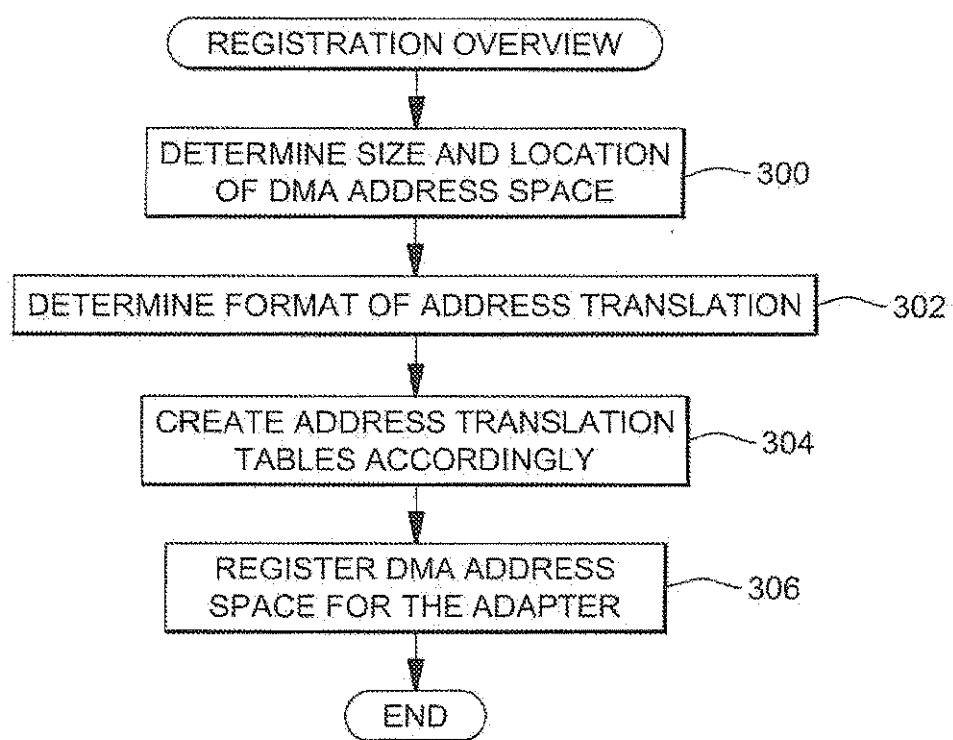


FIG. 3A

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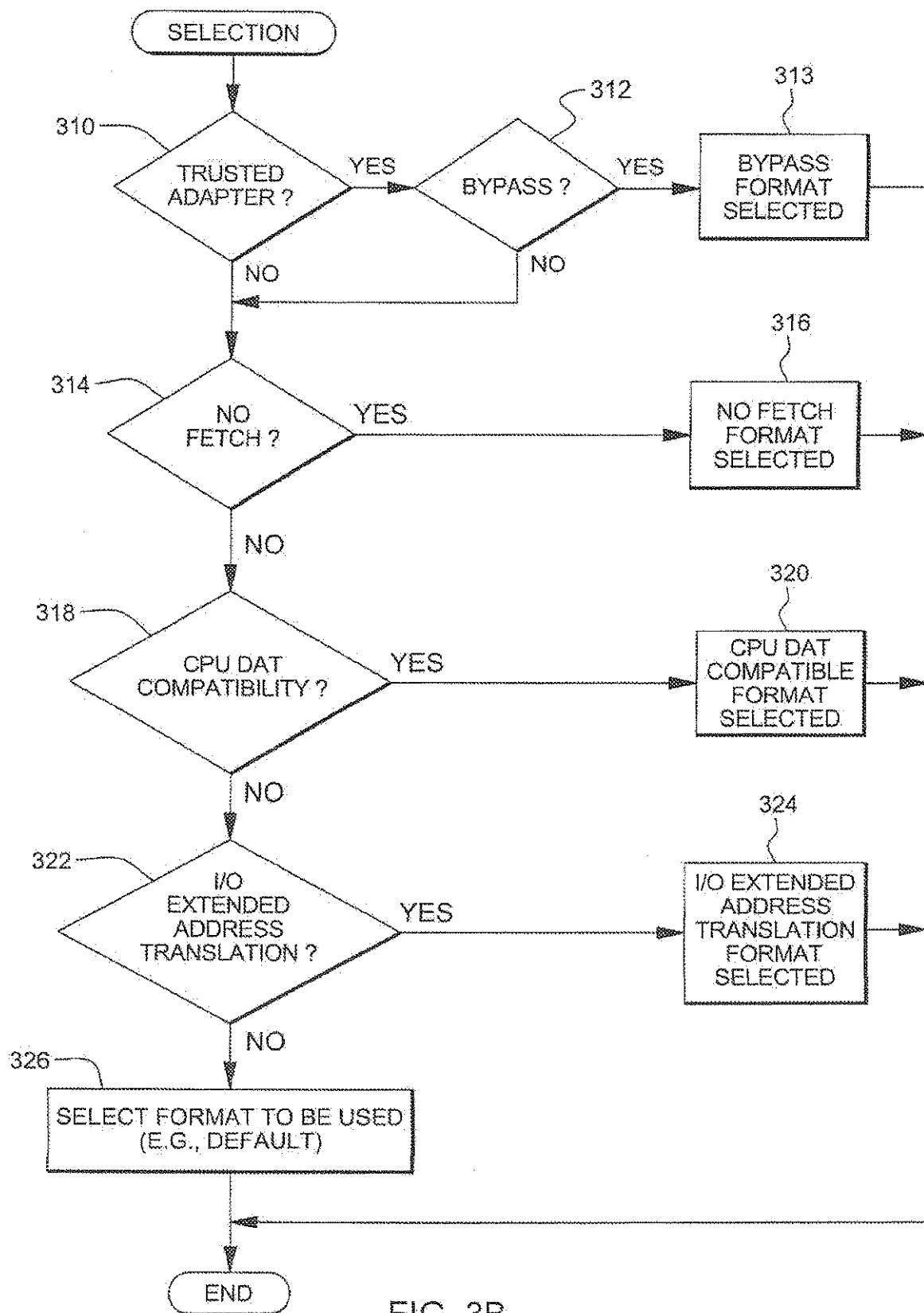


FIG. 3B

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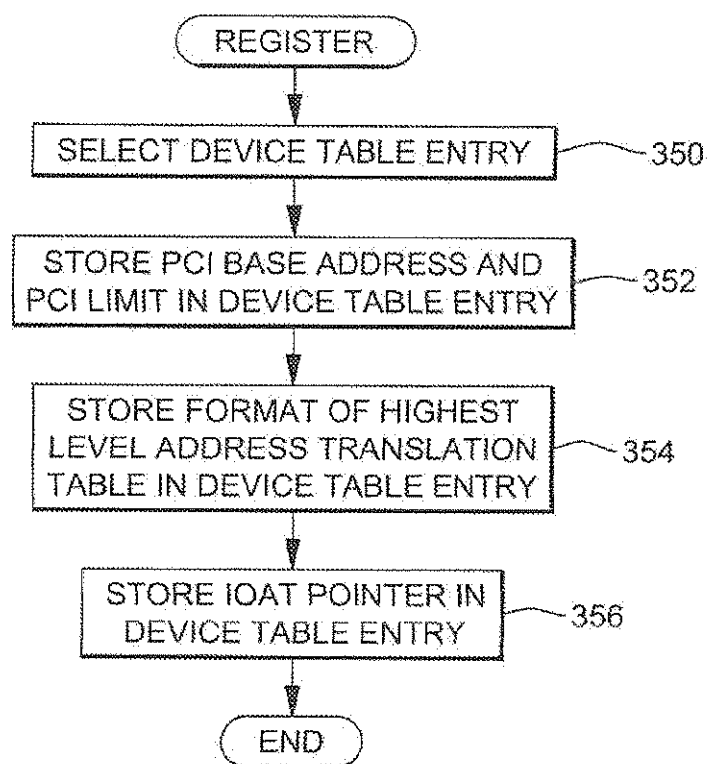


FIG. 3C

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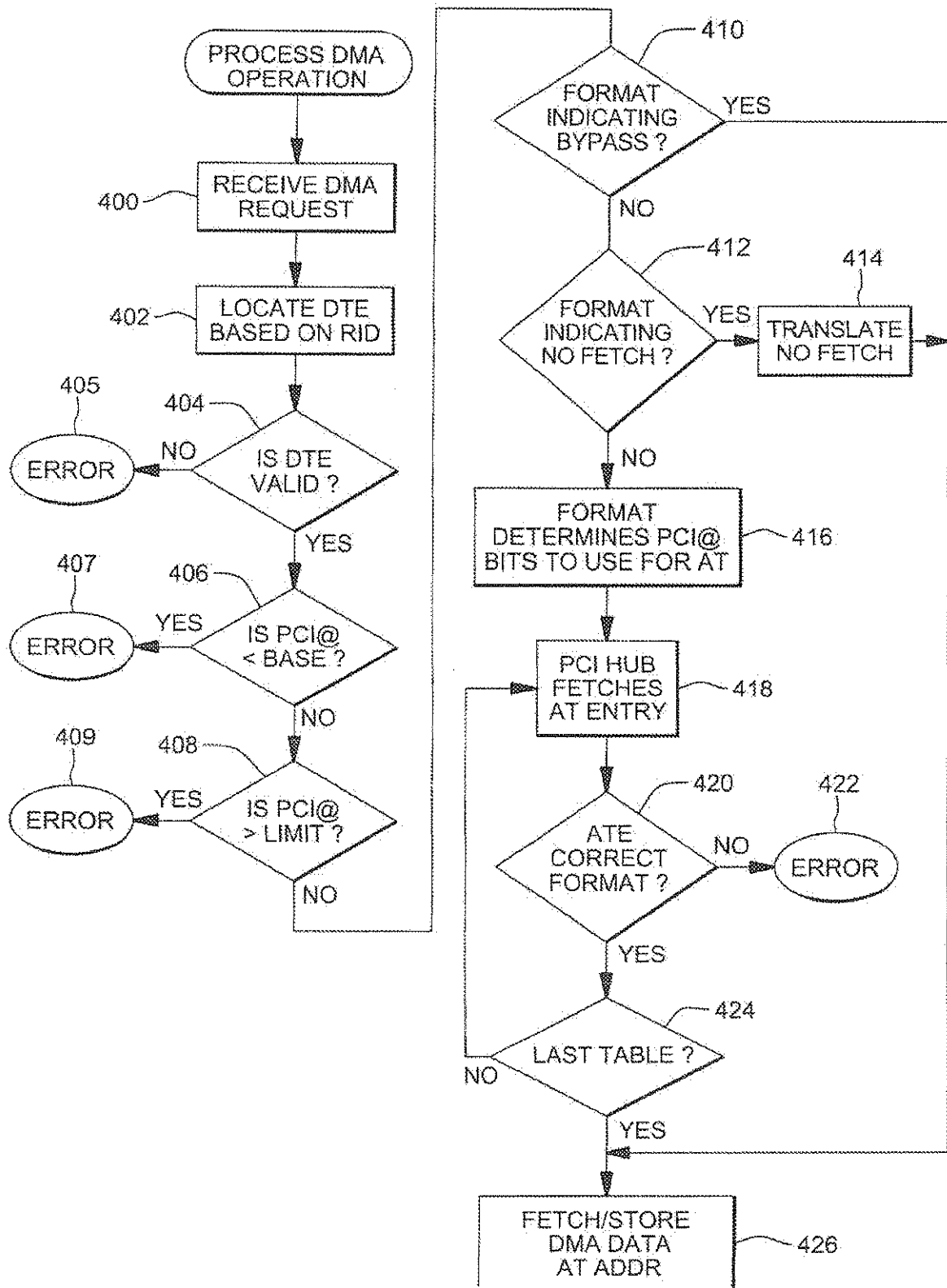


FIG. 4

DMA 6M IN SIZE
REQUIRES 6 LEVELS OF TABLE INCLUDING PAGE TABLES (EACH 4K PAGE CONTAINS 512 8-BYTE ENTRIES)
PCI ADDRESS FOR THE DMA OPERATION: FFFF C000 0009 C600

PCI ADDRESS FOR THE DMA OPERATION: FFFF C000 0009 C600

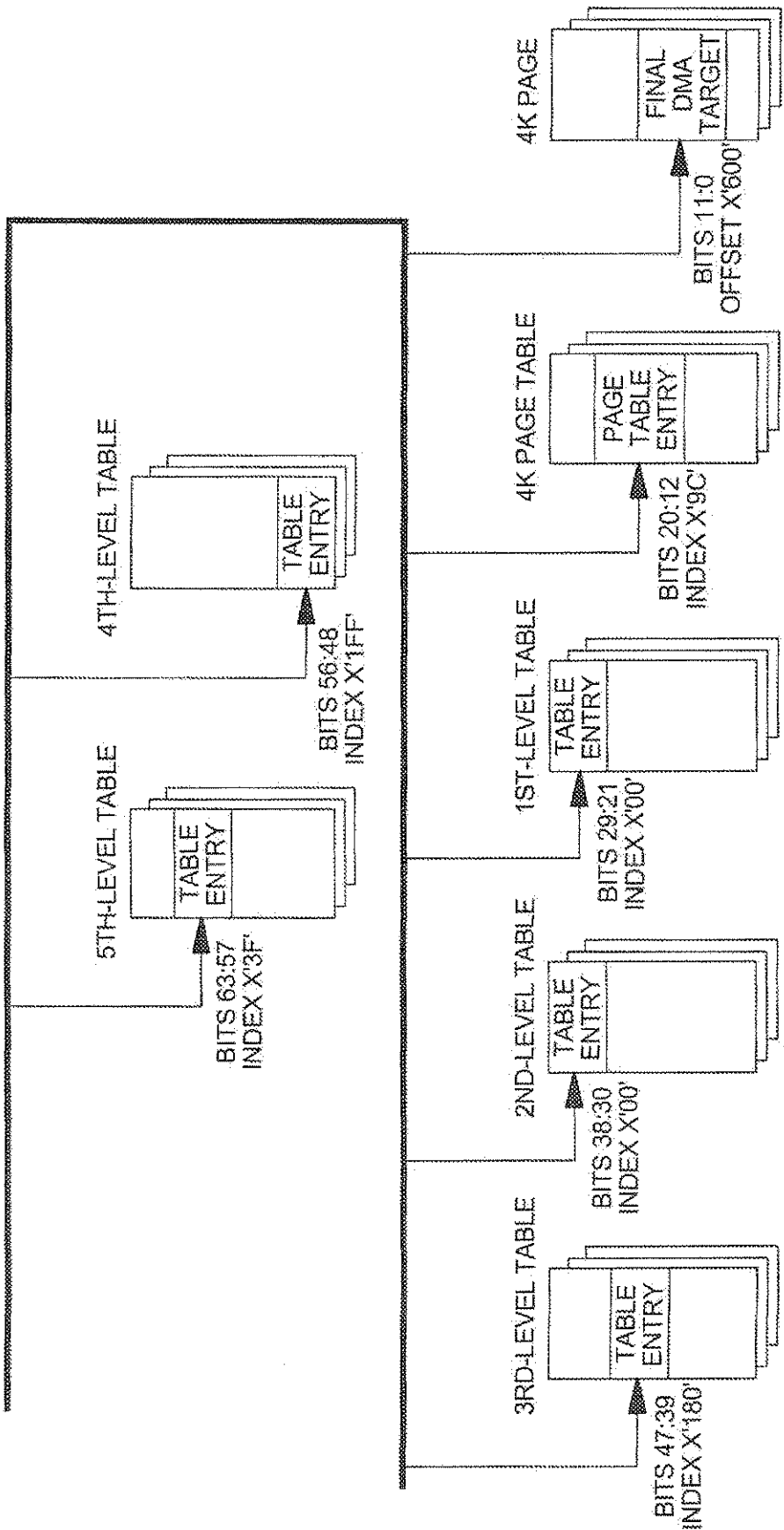


FIG. 5A

DMAAS 6M IN SIZE
REQUIRES 1 4K 1ST LEVEL TABLE AND 3 4K PAGE TABLES (EACH 4K PAGE CONTAINS 512 8-BYTE ENTRIES)
BASE PCI ADDRESS FFFF C000 0000 0000
PCI LIMIT ADDRESS FFFF C000 005F FFFF
PCI ADDRESS FOR THE DMA OPERATION: FFFF C000 0009 C600
THE PCI BASE/LIMIT CHECK ALLOWS PCI ADDRESS BITS 63:30 TO BE IGNORED

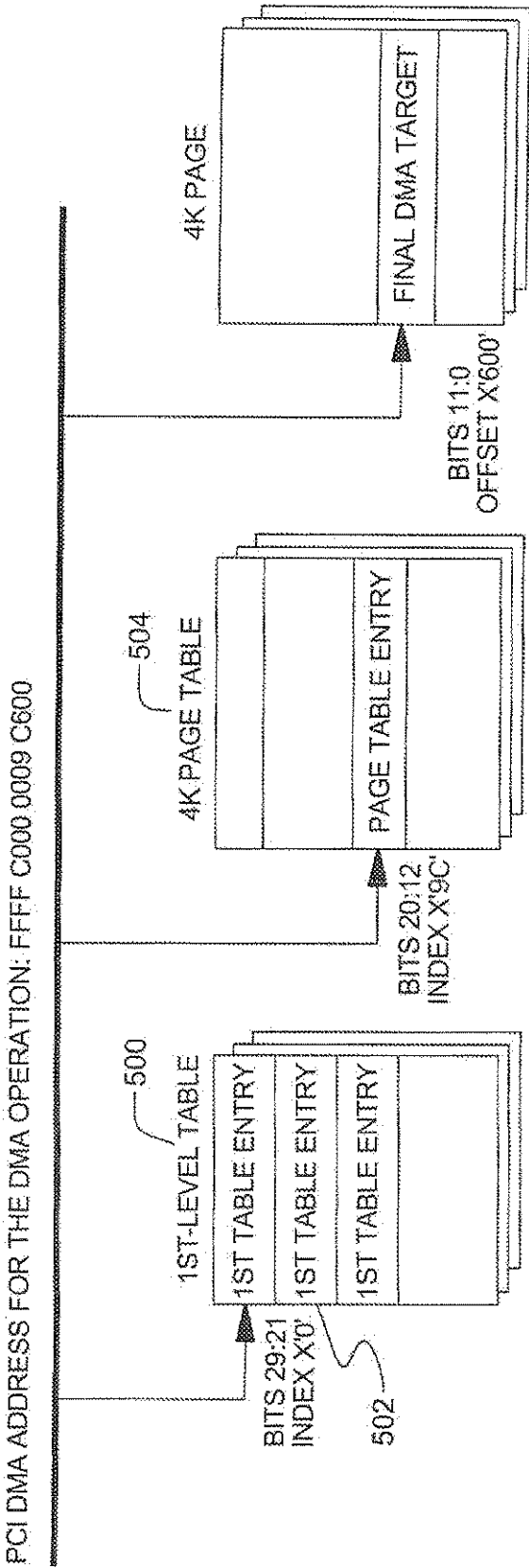


FIG. 5B

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CPU DAT COMPATIBLE FORMATS						
564	562	560	558	556	554	
	11 BITS	11 BITS	11 BITS	8 BITS	12 BITS	
4K PAGE DAT COMP.	REGION FIRST DMAAS 16E	REGION SECOND DMAAS 8P	REGION THIRD DMAAS 4T	SEGMENT TABLE DMAAS 2G	PAGE TABLE DMAAS 1M	BYTE OFFSET 4K
	11 BITS	11 BITS	11 BITS	11 BITS		20 BITS
1M PAGE DAT COMP.	REGION FIRST DMAAS 16E	REGION SECOND DMAAS 8P	REGION THIRD DMAAS 4T	SEGMENT TABLE DMAAS 2G		BYTE OFFSET 1M

CO
LO
G
E

FORMAT	I/O EXTENDED ADDRESS TRANSLATION FORMATS						578	576
	7 BITS	9 BITS	9 BITS	9 BITS	9 BITS	9 BITS	12 BITS	
4K PAGE 4K AT TABLES	5 TH LEVEL DMAAS 16E	4 TH LEVEL DMAAS 128P	3 RD LEVEL DMAAS 256T	2 ND LEVEL DMAAS 512G	1 ST LEVEL DMAAS 1G	IOPT DMAAS 2M	BYTE OFFSET 4K	
	17 BITS			17 BITS		17 BITS	12 BITS	
4K PAGE 1M AT TABLES	2 ND LEVEL DMAAS 8E		1 ST LEVEL DMAAS 64T		IOPT DMAAS 512M		BYTE OFFSET 4K	
	10 BITS	17 BITS		17 BITS		20 BITS		
1M PAGE 1M AT TABLES	2 ND LEVEL DMAAS 16E	1 ST LEVEL DMAAS 16P		IOPT DMAAS 128G		BYTE OFFSET 1M		

100

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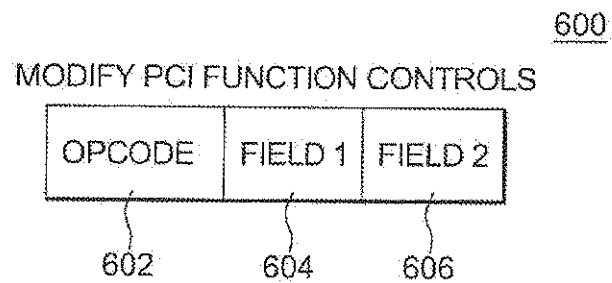


FIG. 6A



FIG. 6B

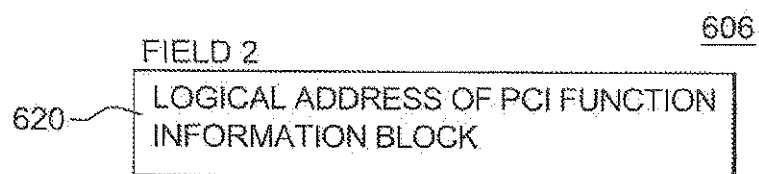


FIG. 6C

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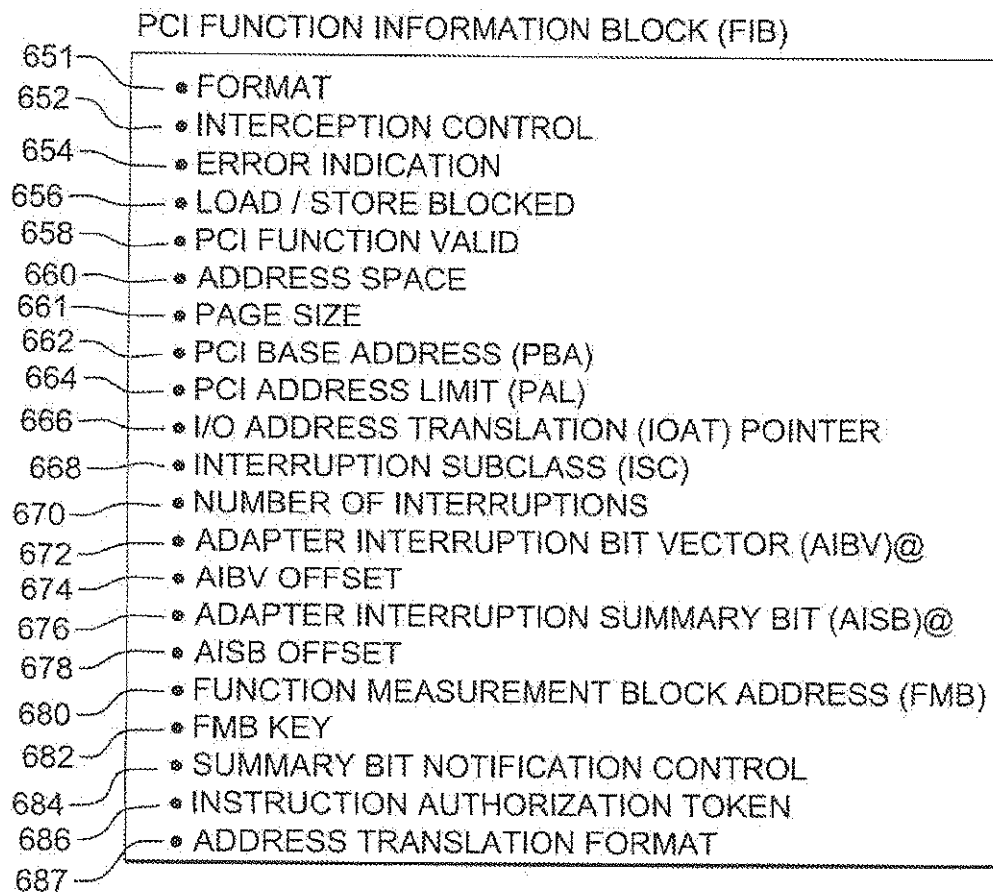
650

FIG. 6D

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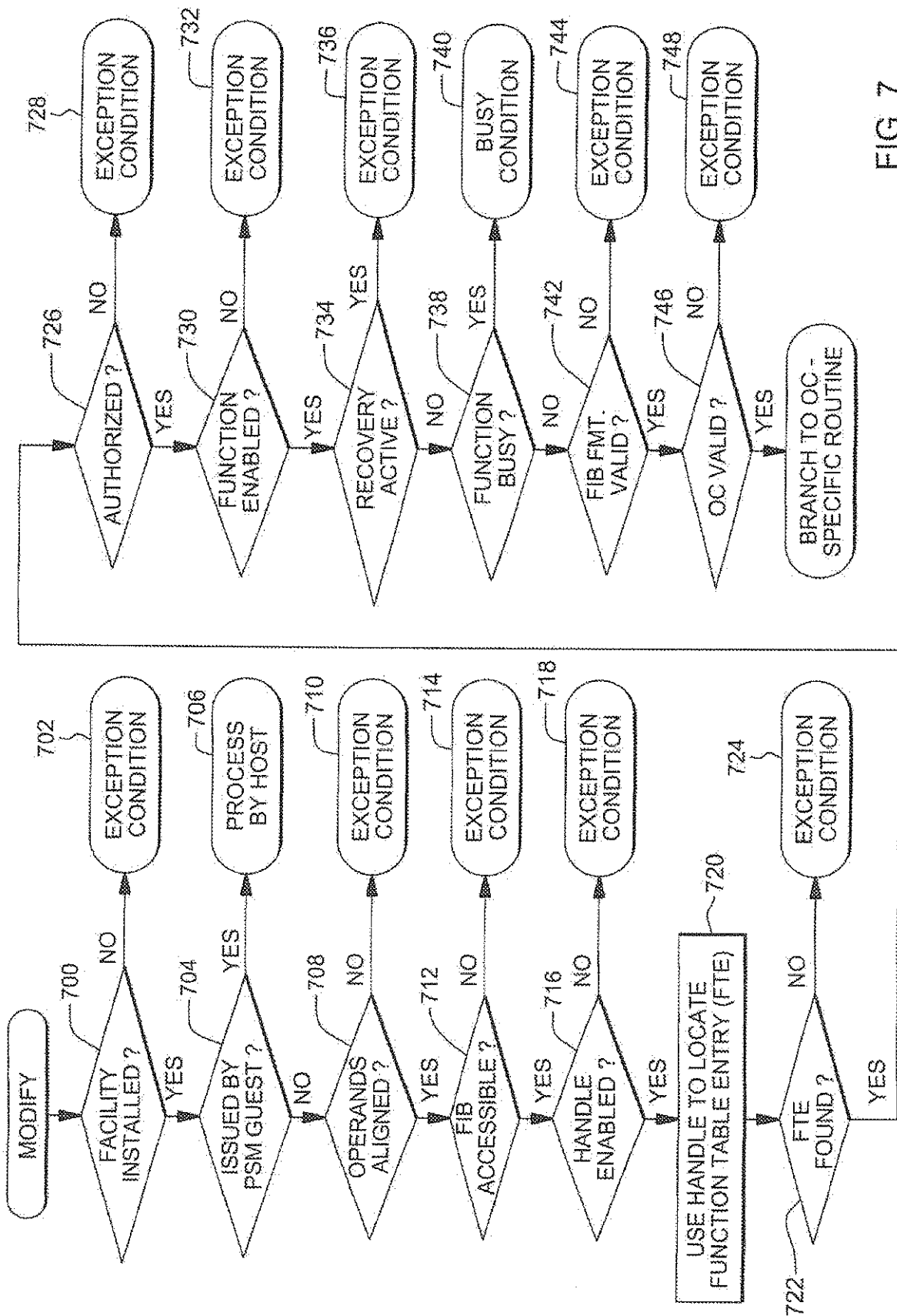


FIG. 7

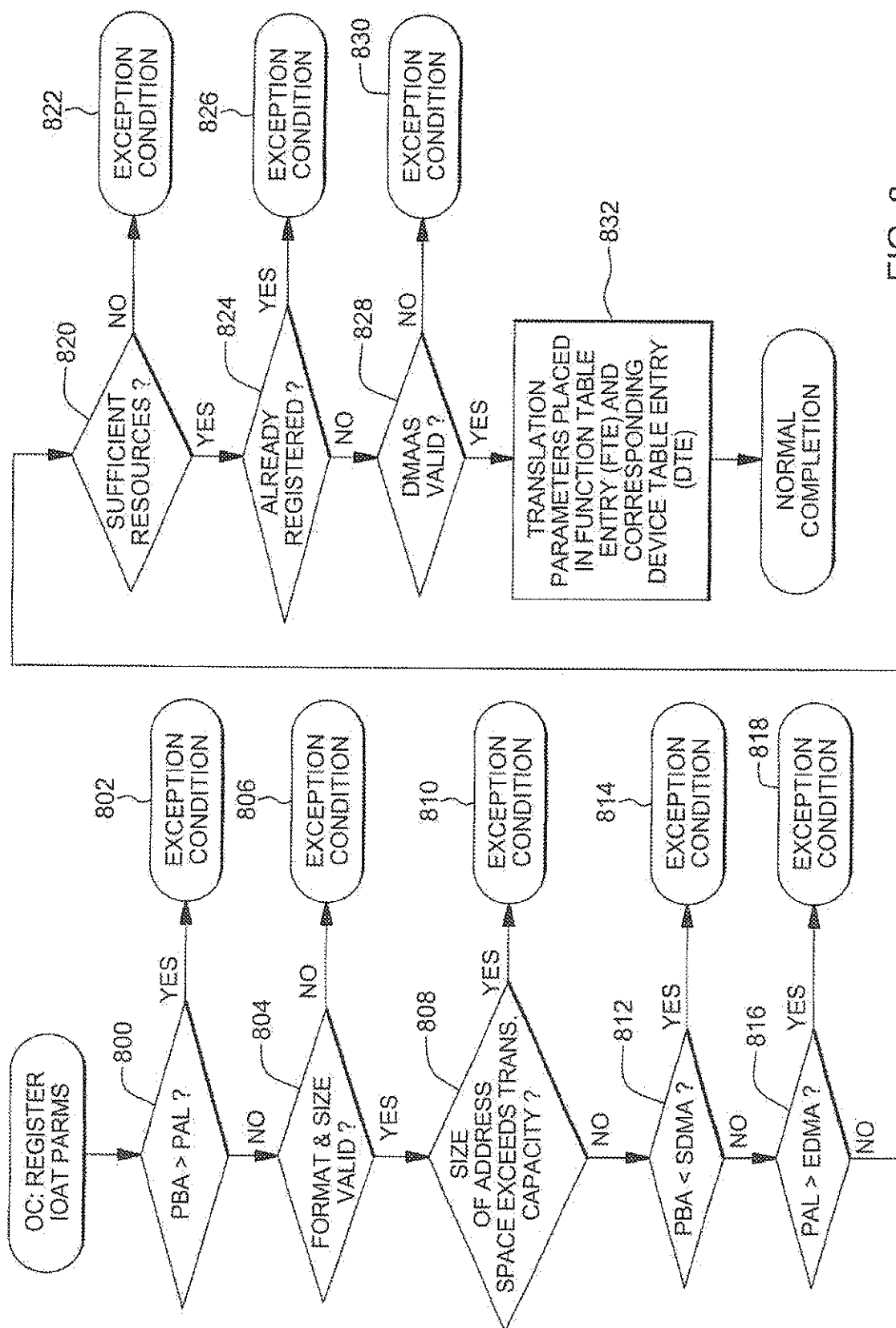


FIG. 8

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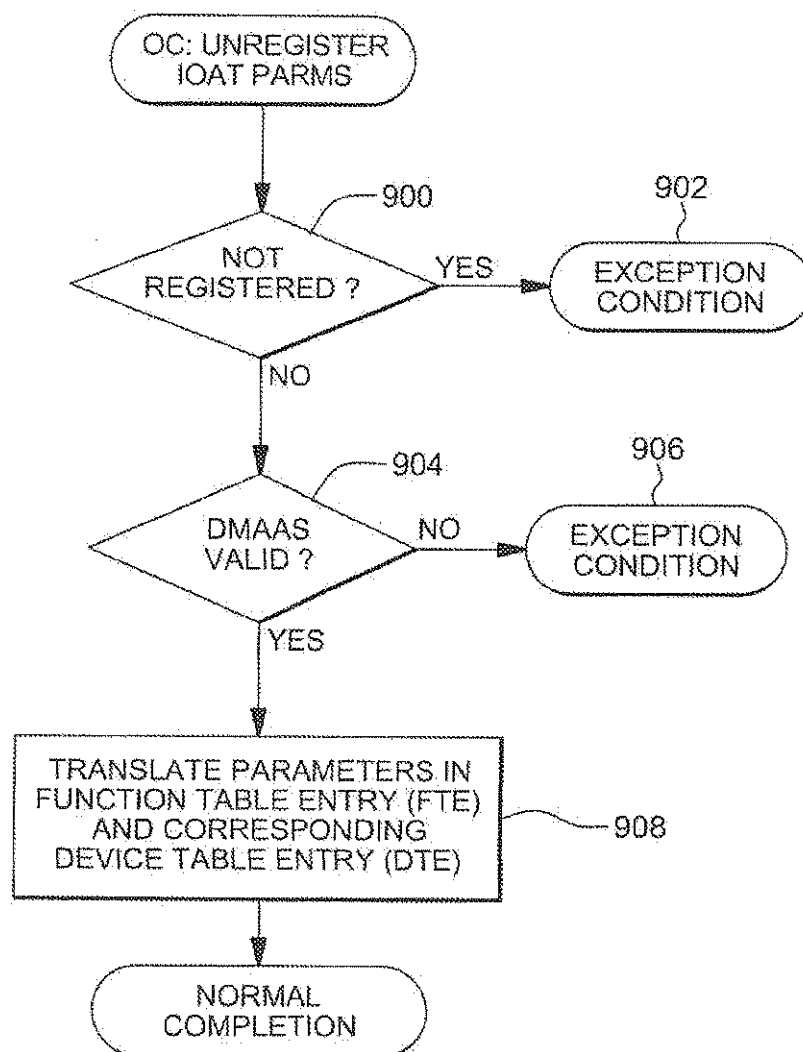


FIG. 9

COMPUTER
PROGRAM
PRODUCT
1000

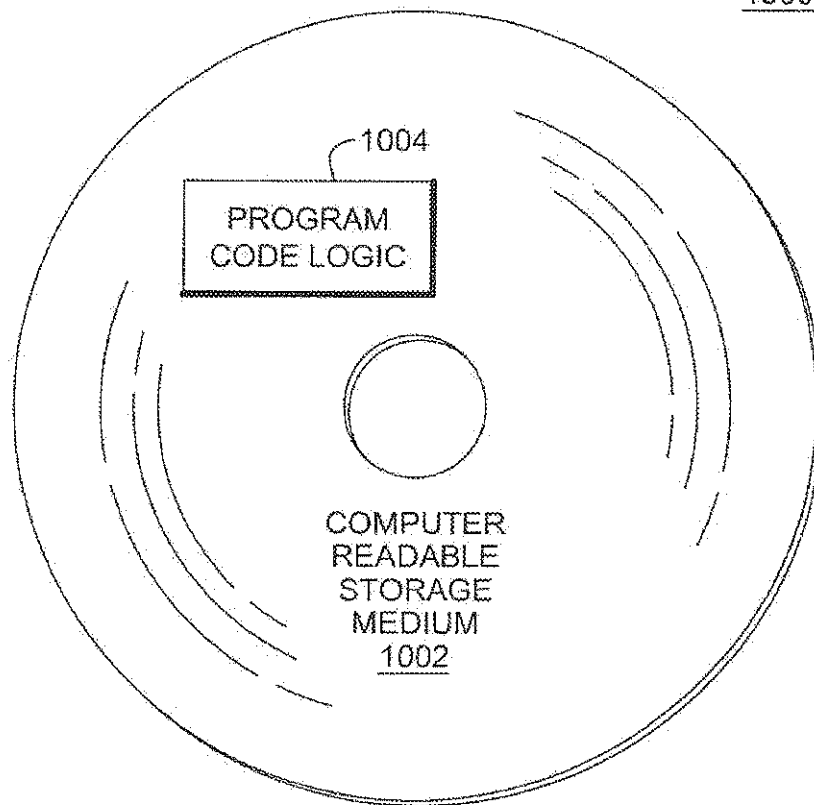


FIG. 10

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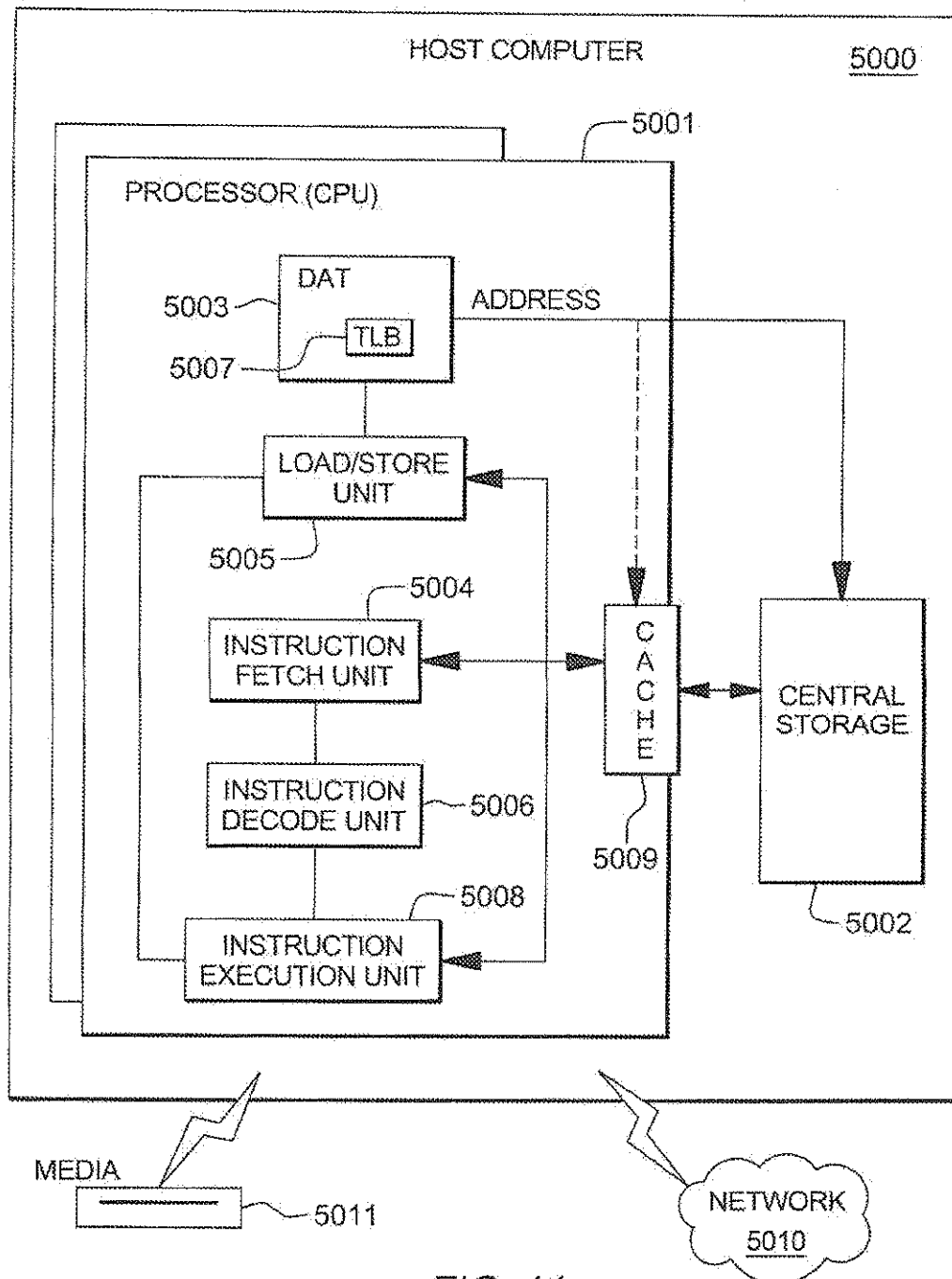


FIG. 11

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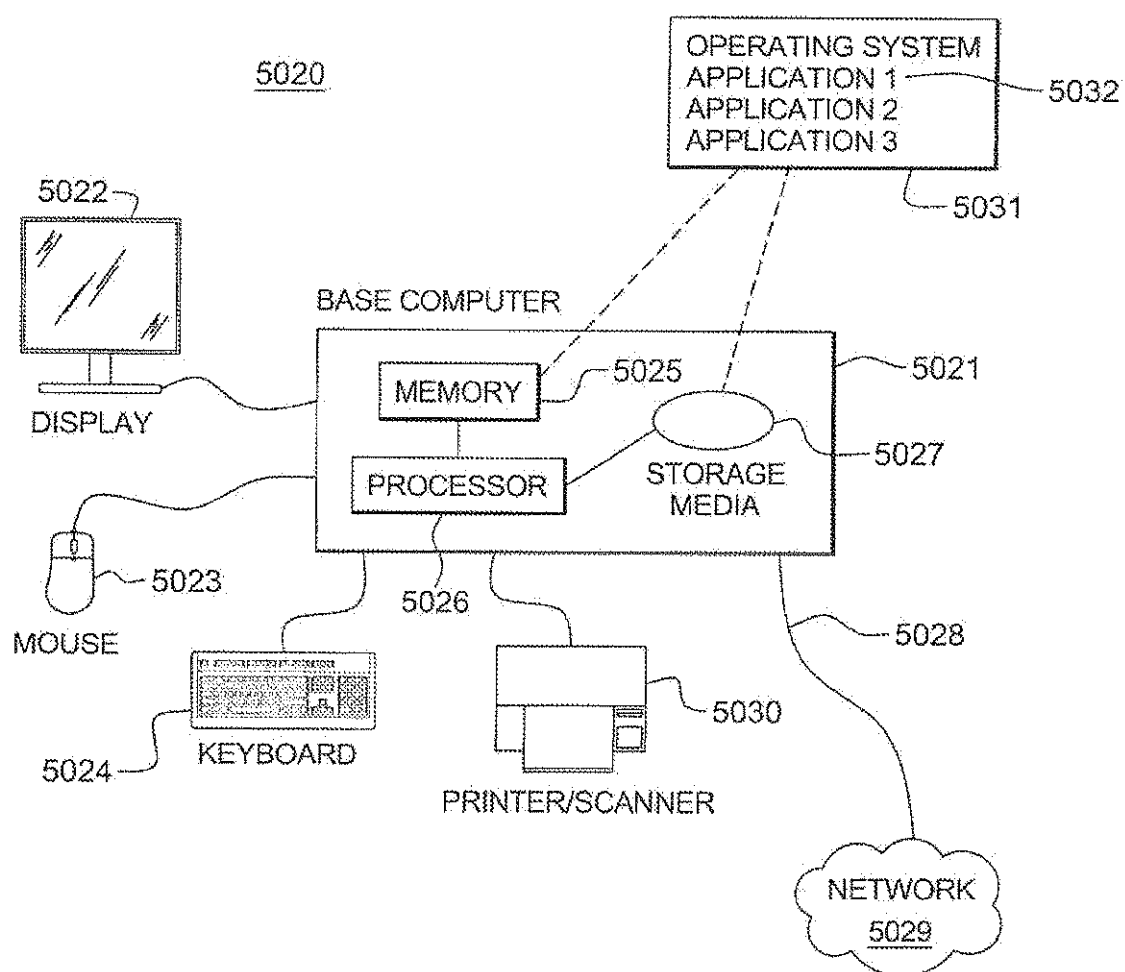


FIG. 12

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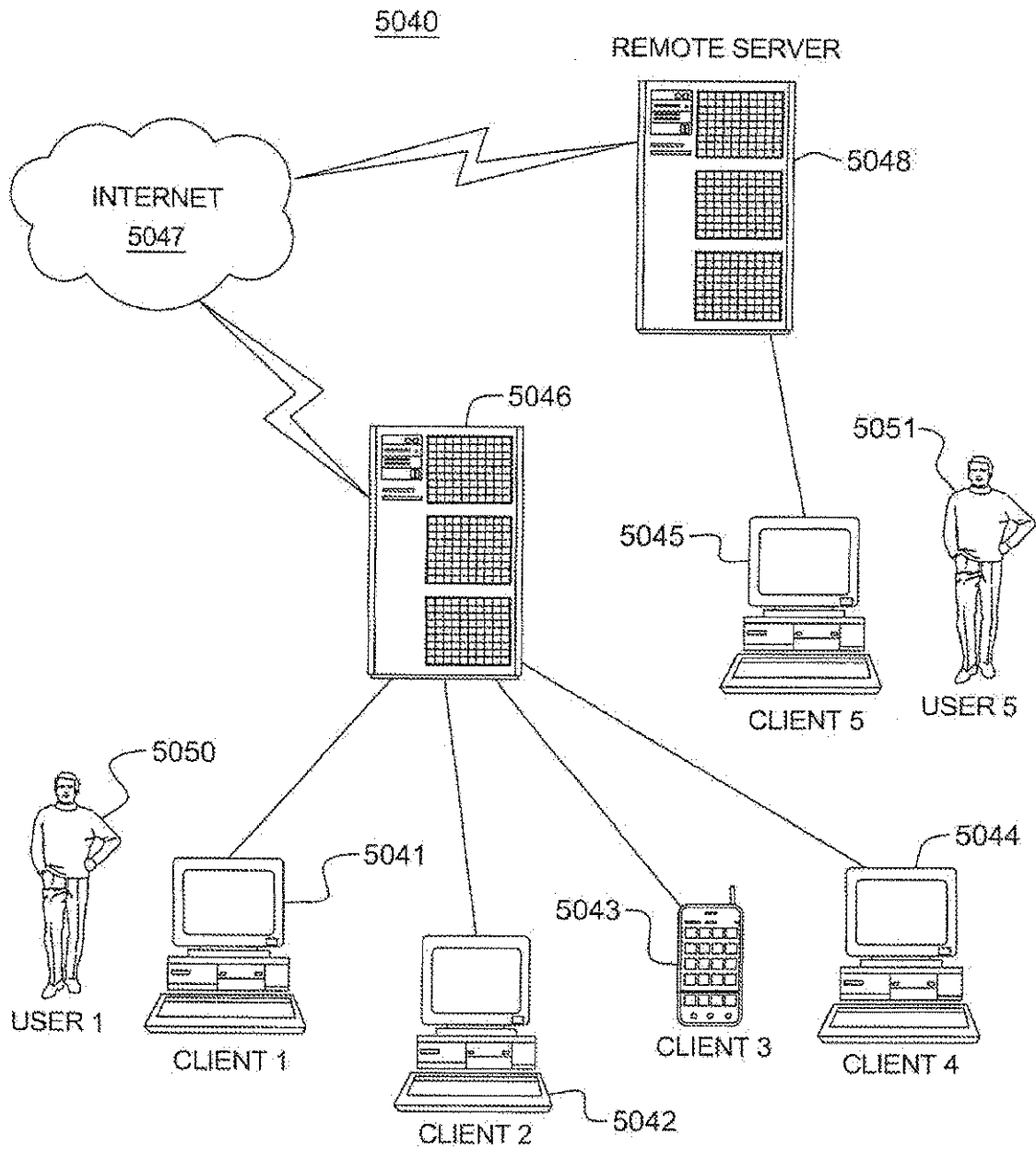


FIG. 13

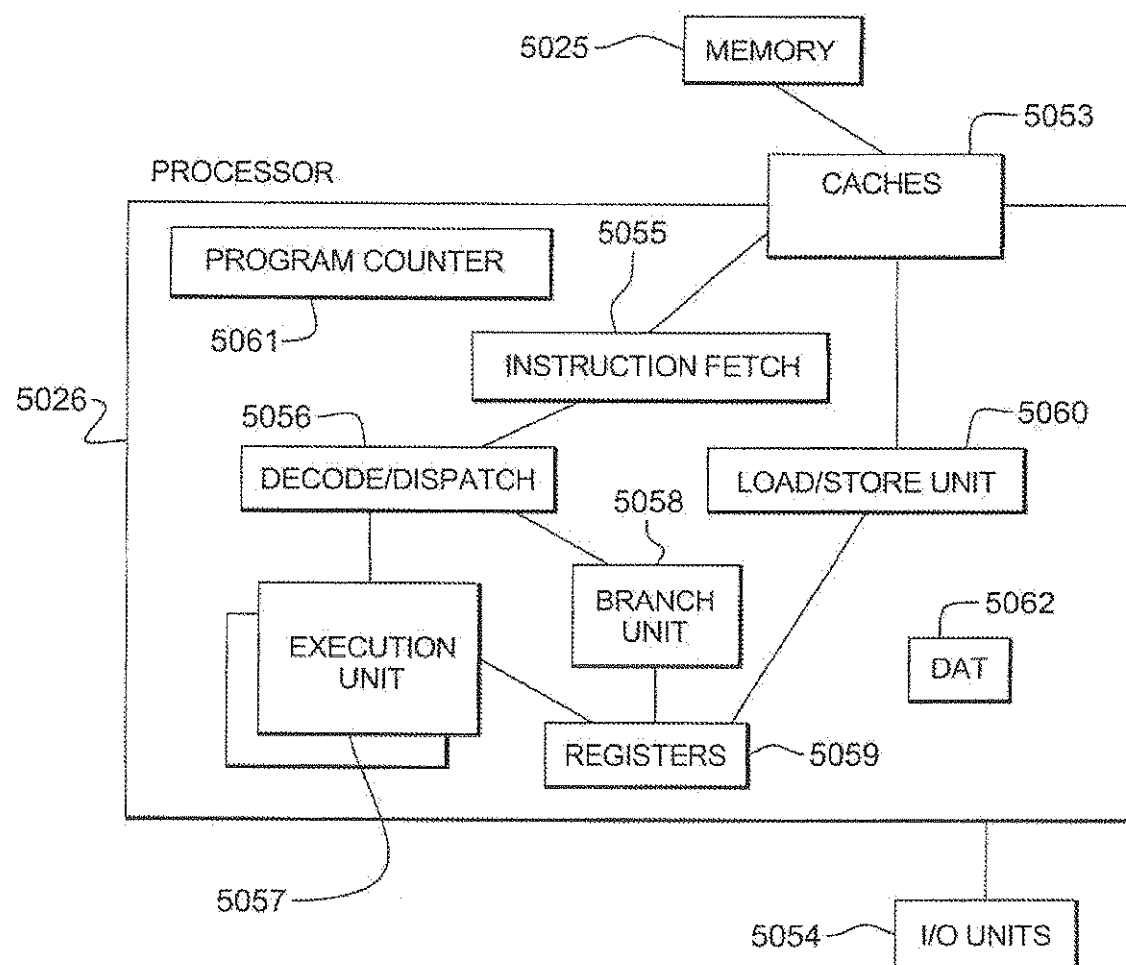


FIG. 14

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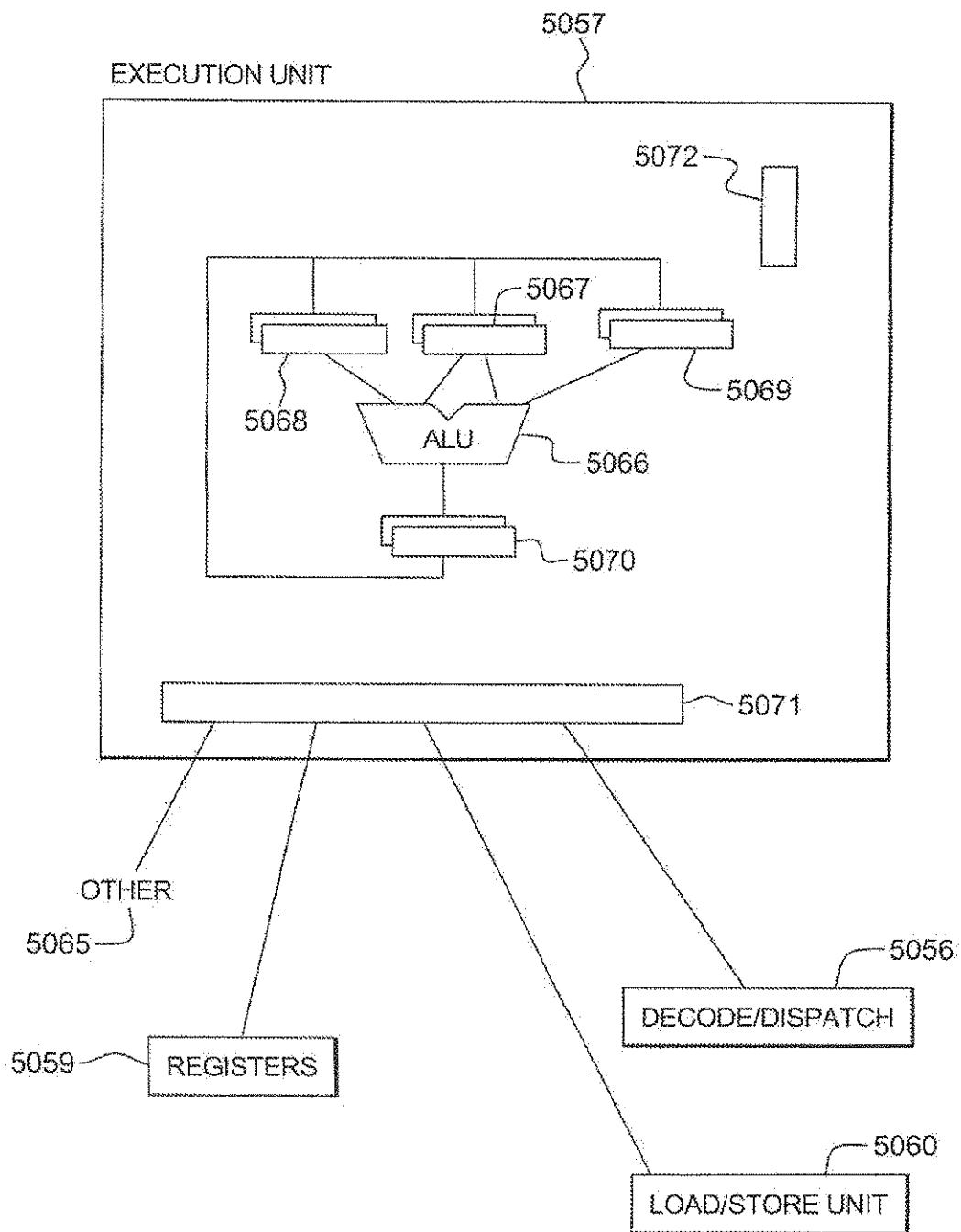


FIG. 15A

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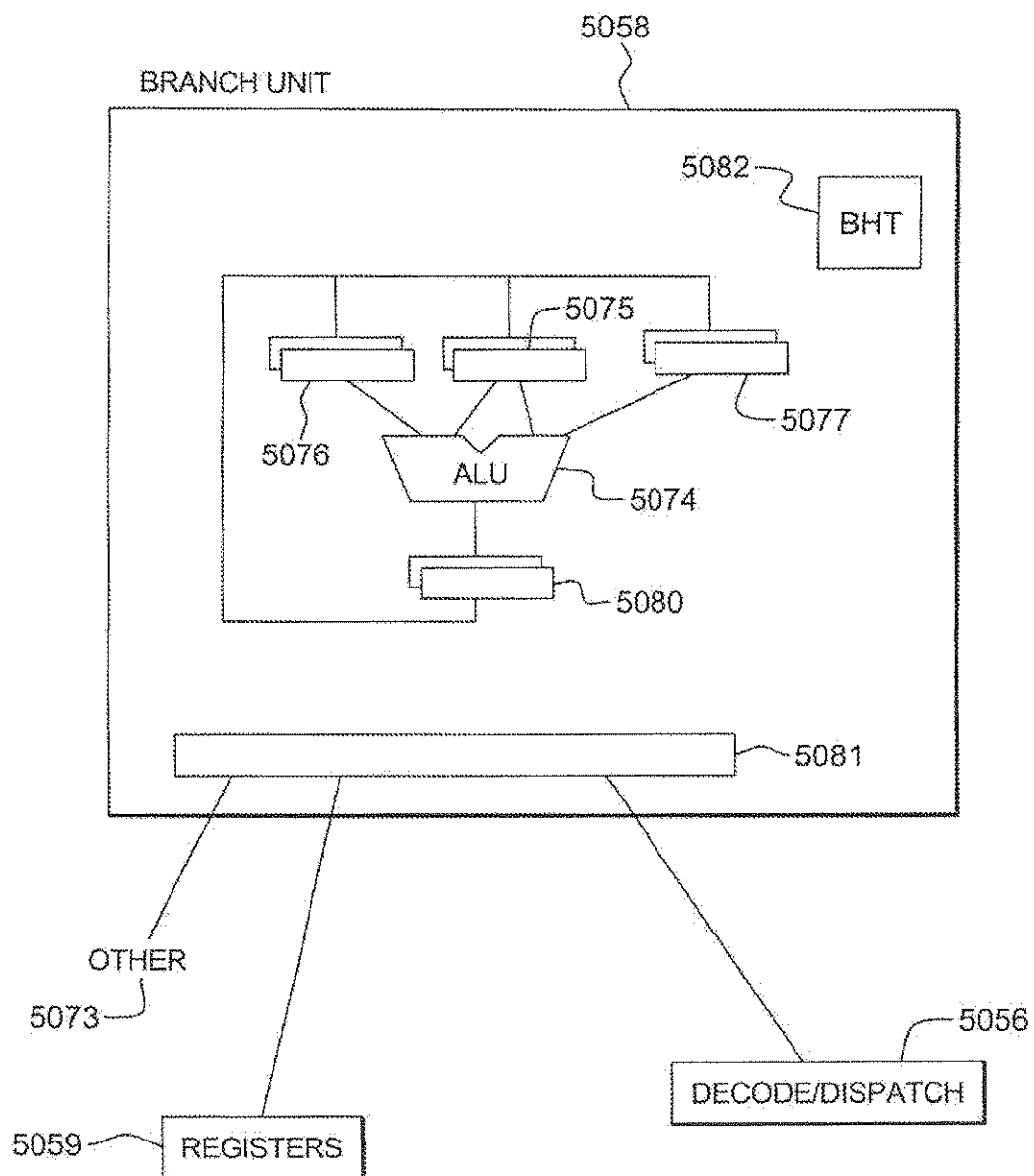


FIG. 15B

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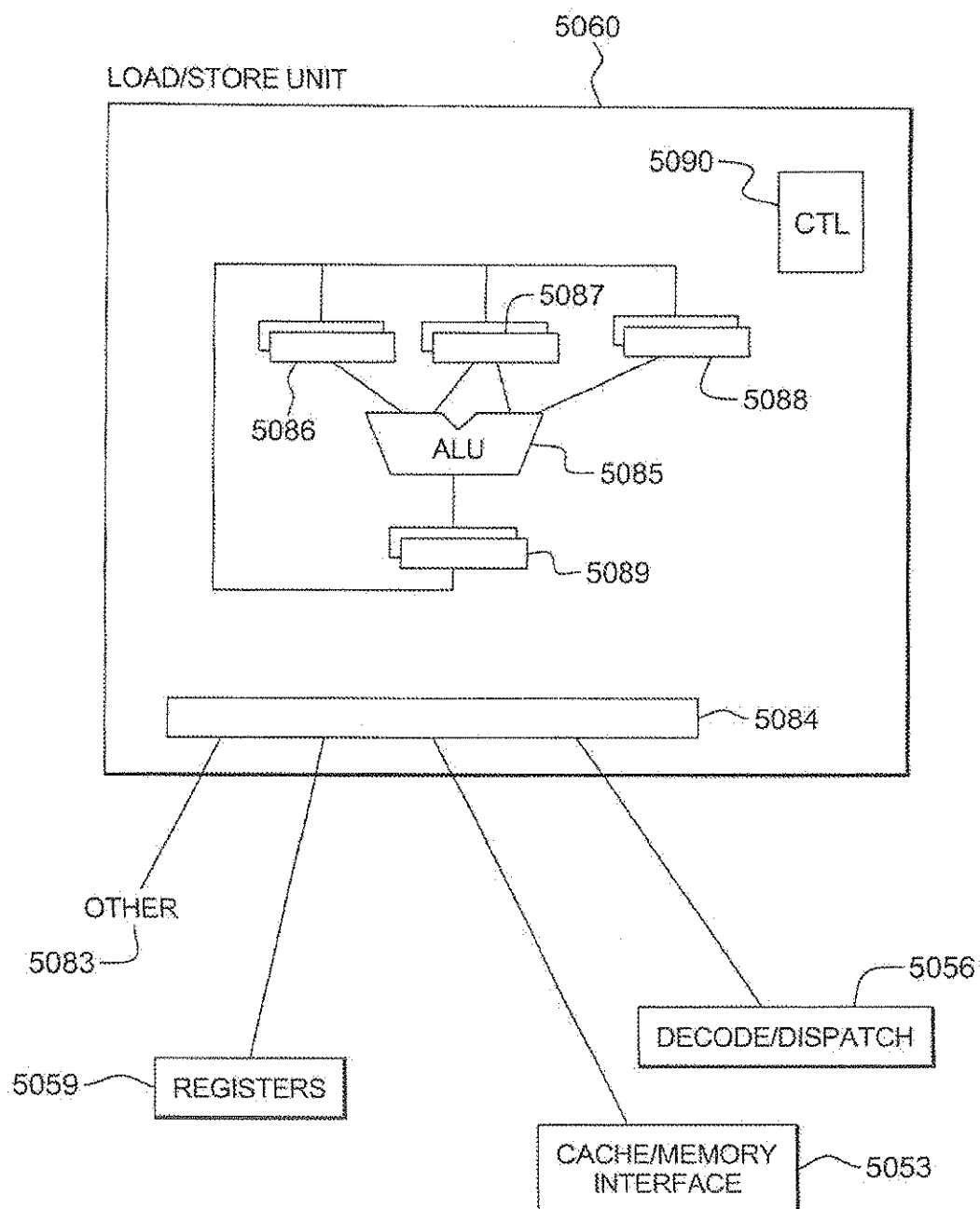


FIG. 15C

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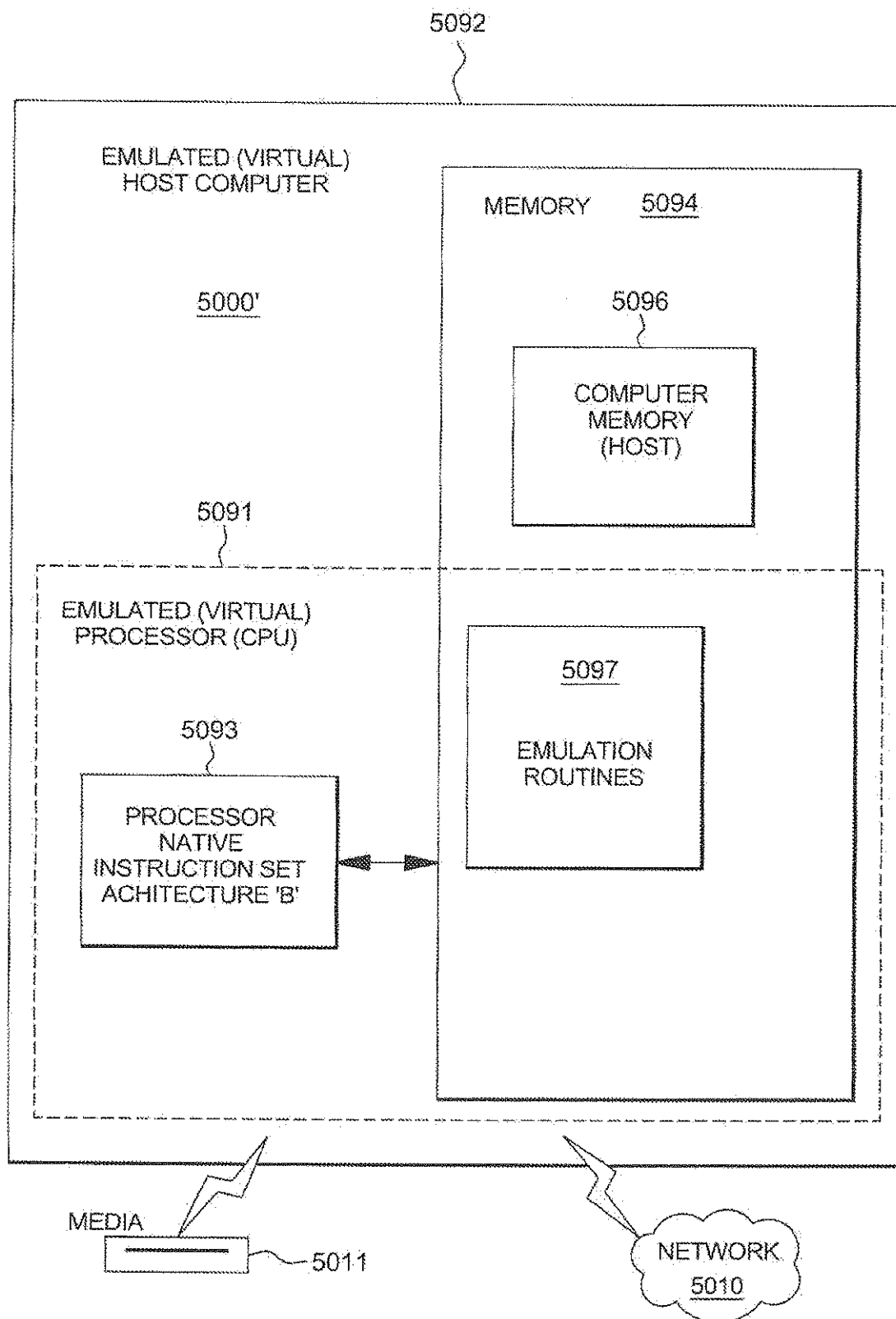


FIG. 16