

1/19

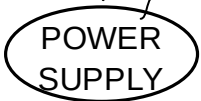


FIG. 2

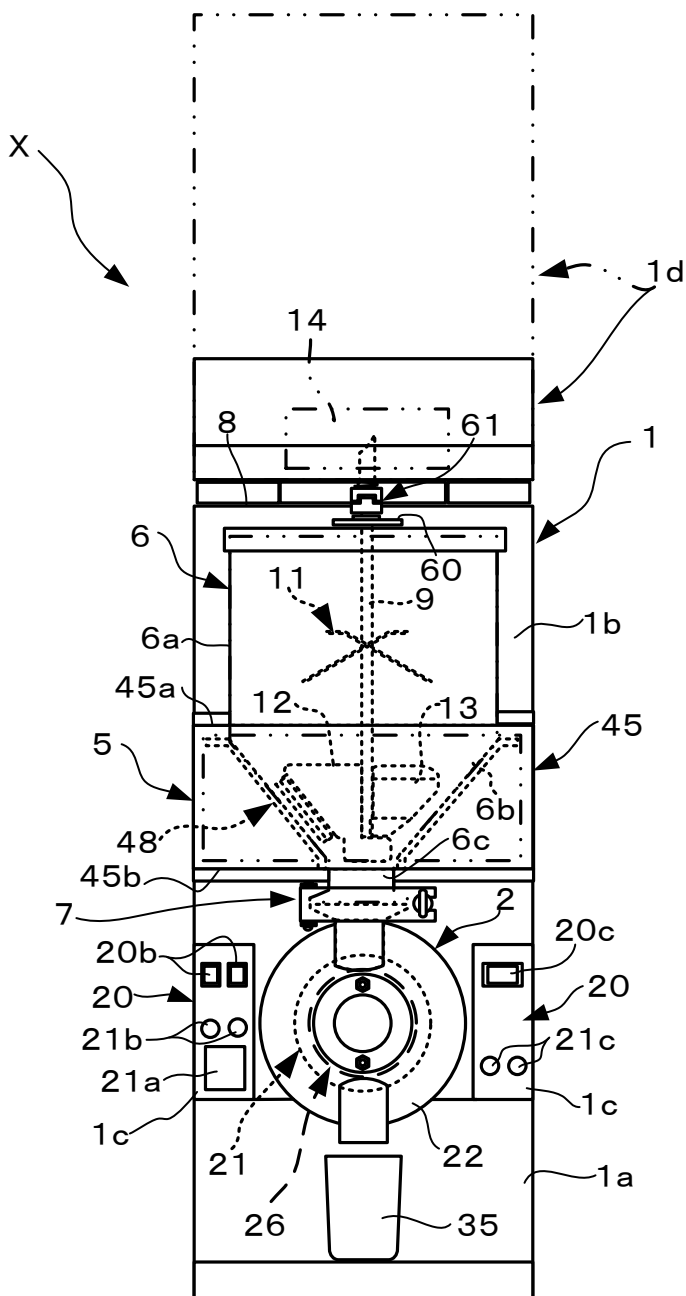


FIG. 3

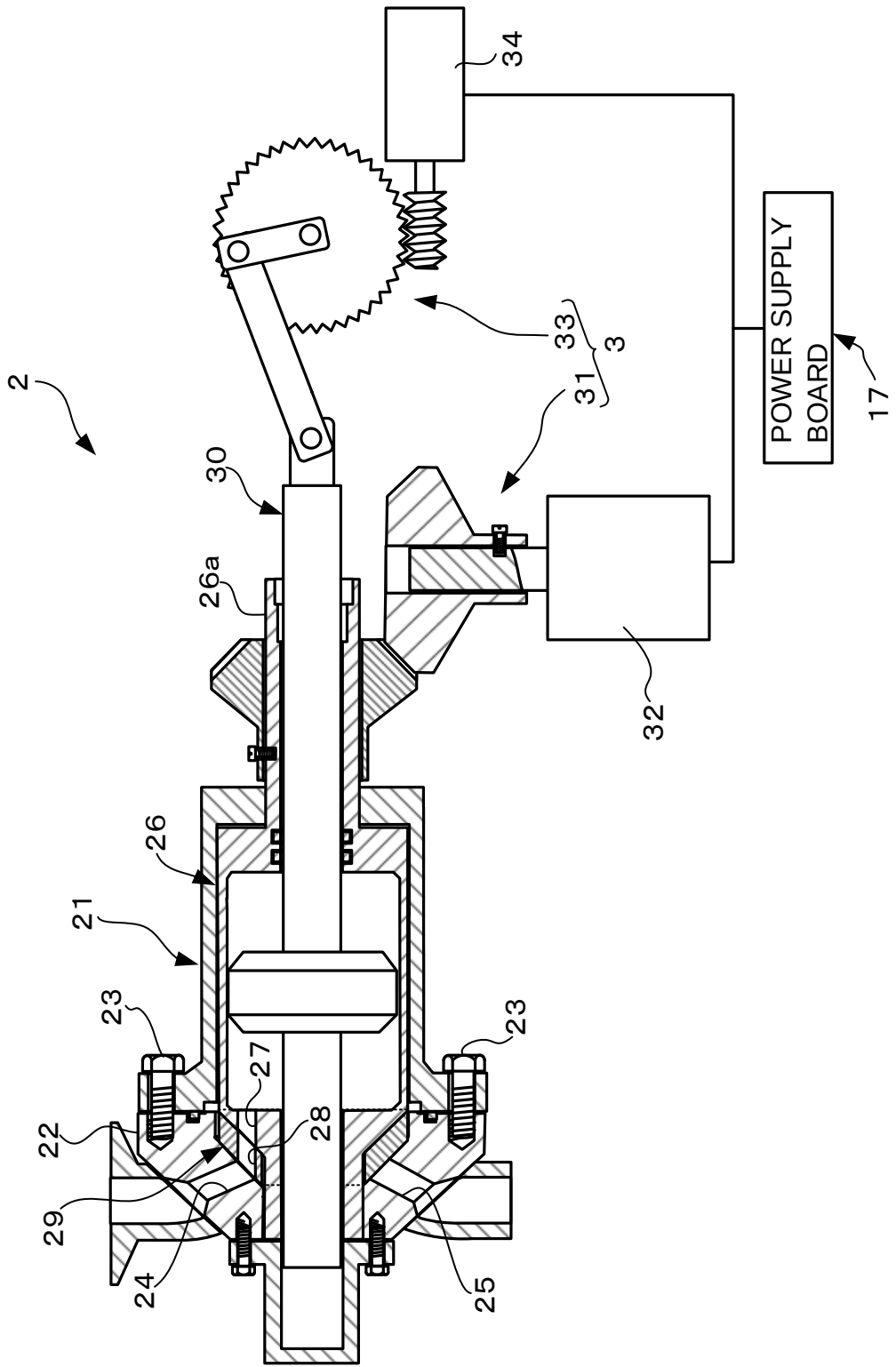


FIG. 4

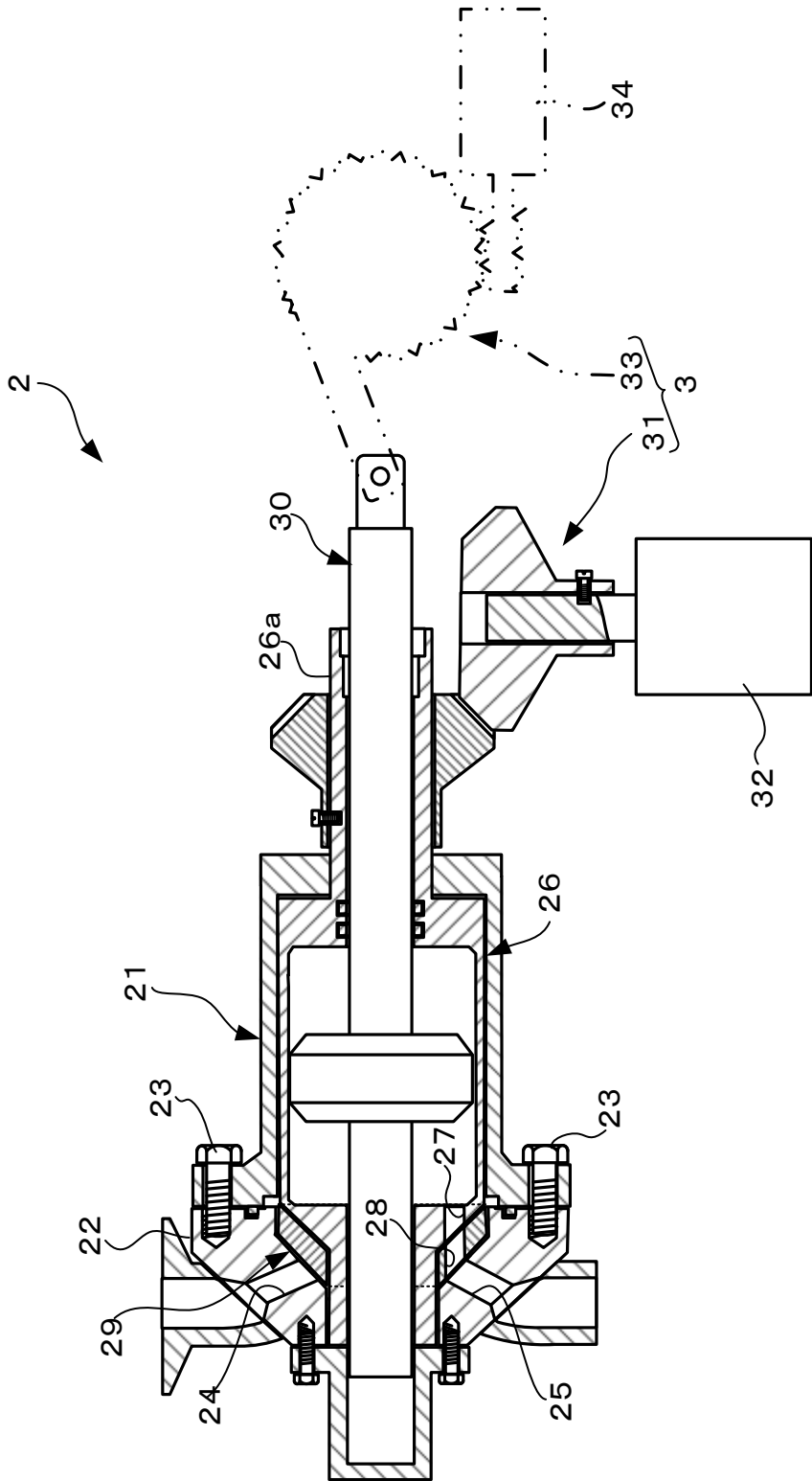


FIG. 5

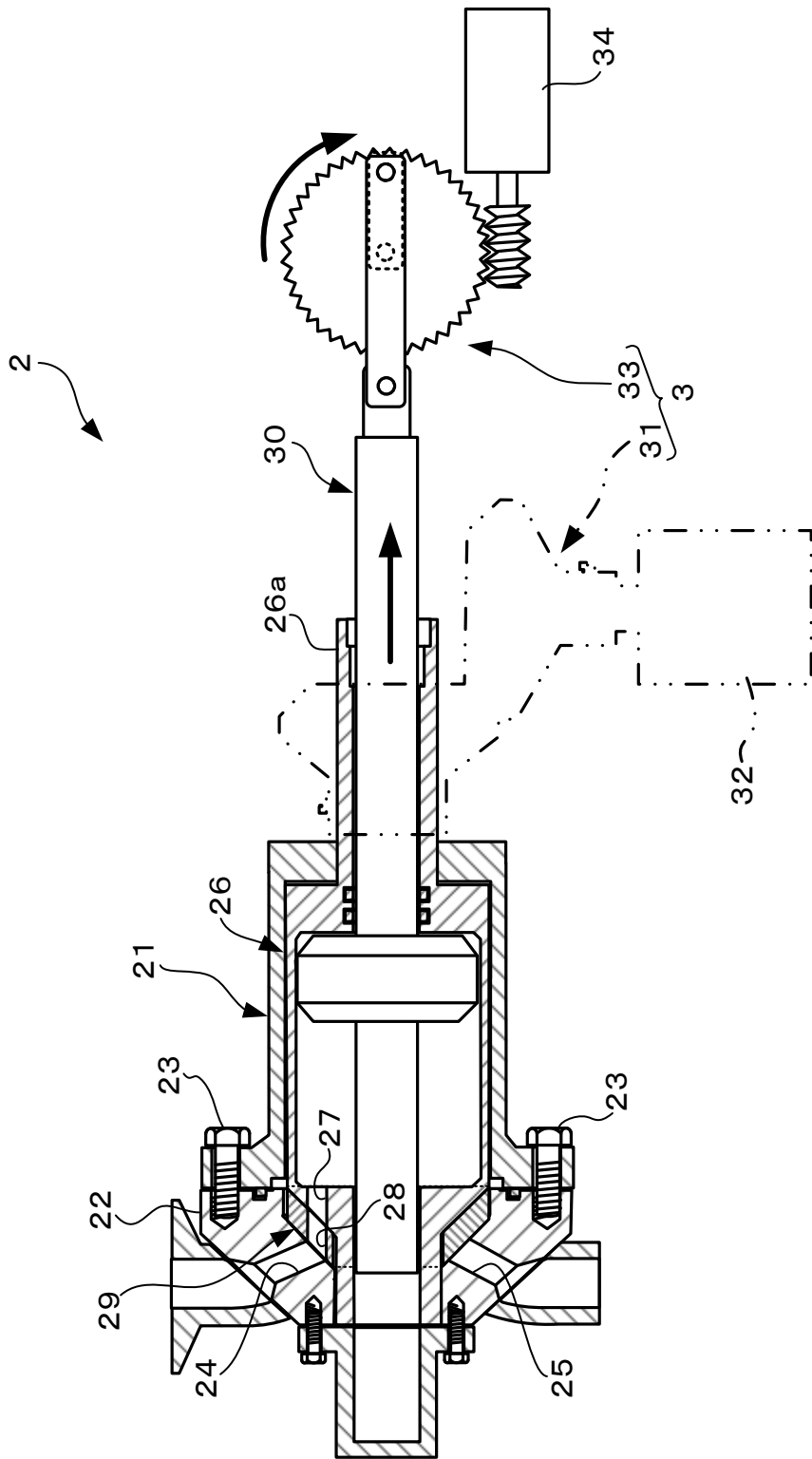
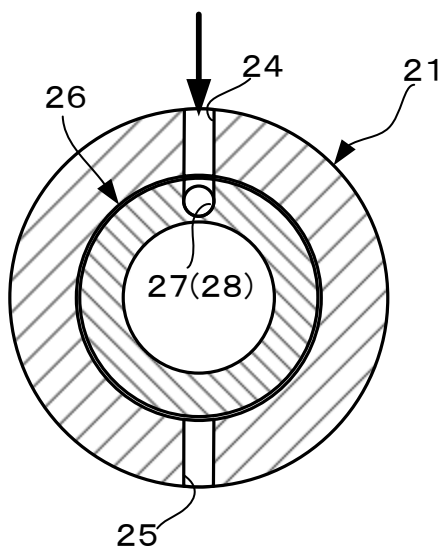


FIG. 6

(a)



(b)

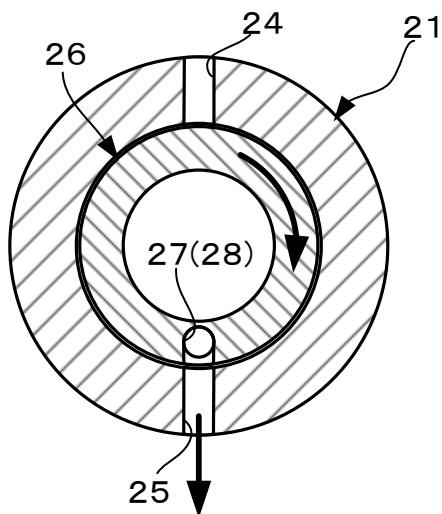


FIG. 7

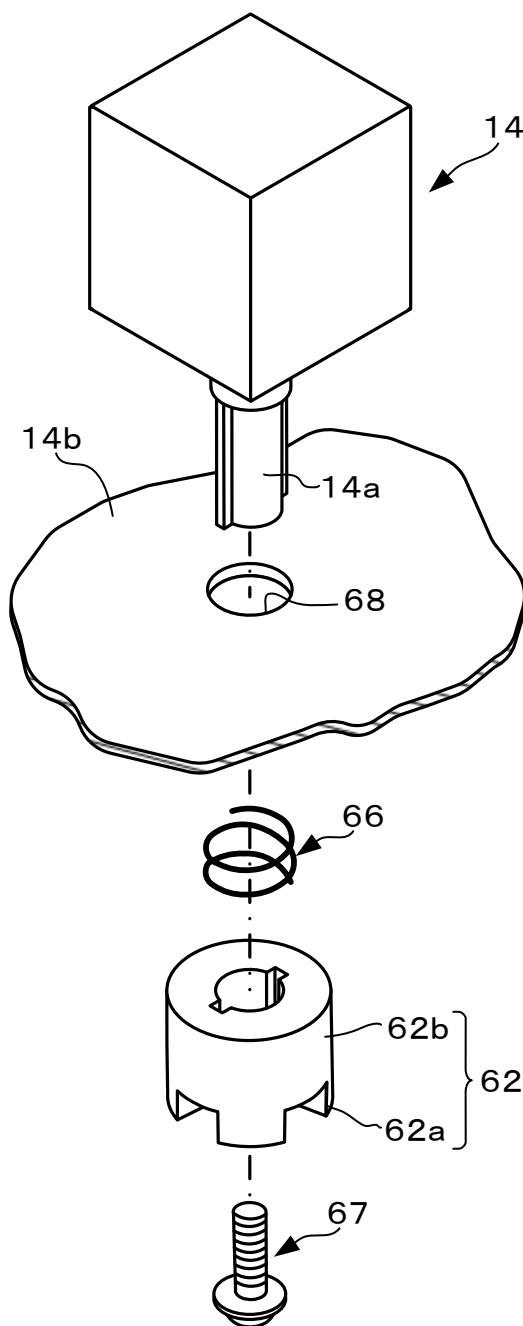


FIG. 8

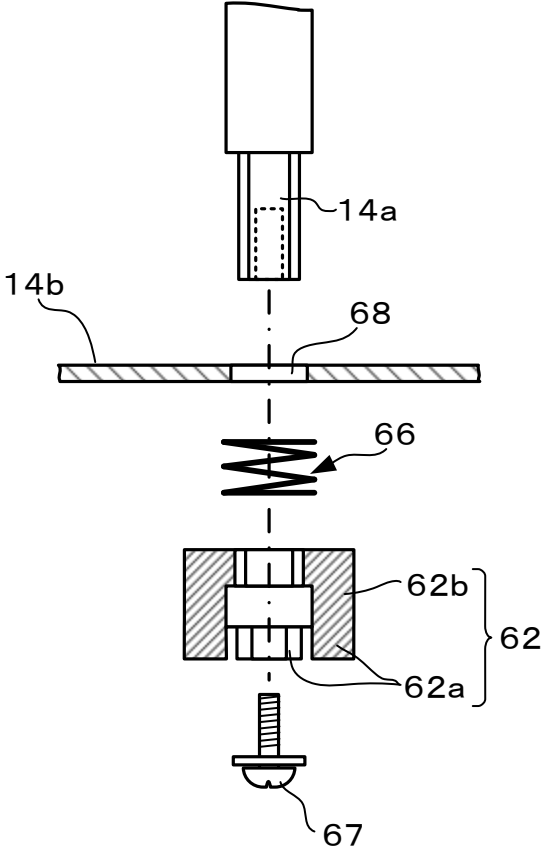


FIG. 9

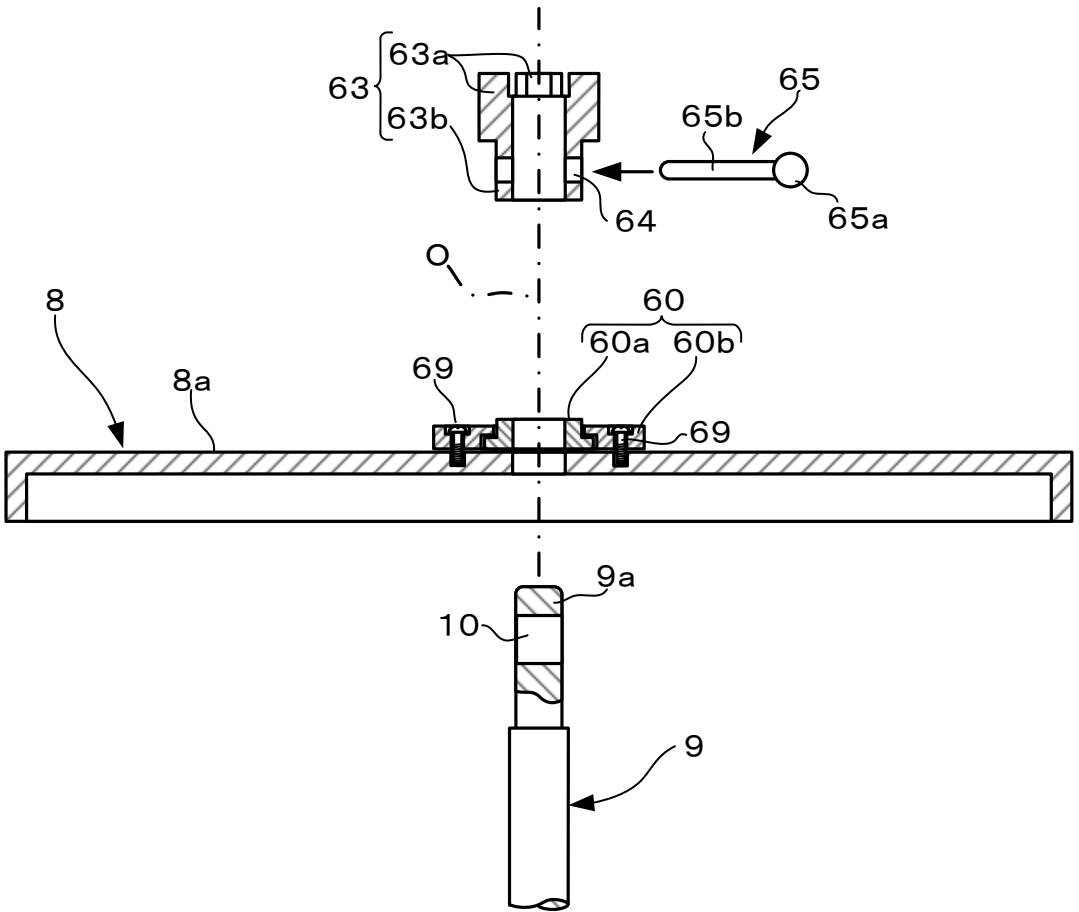


FIG. 10

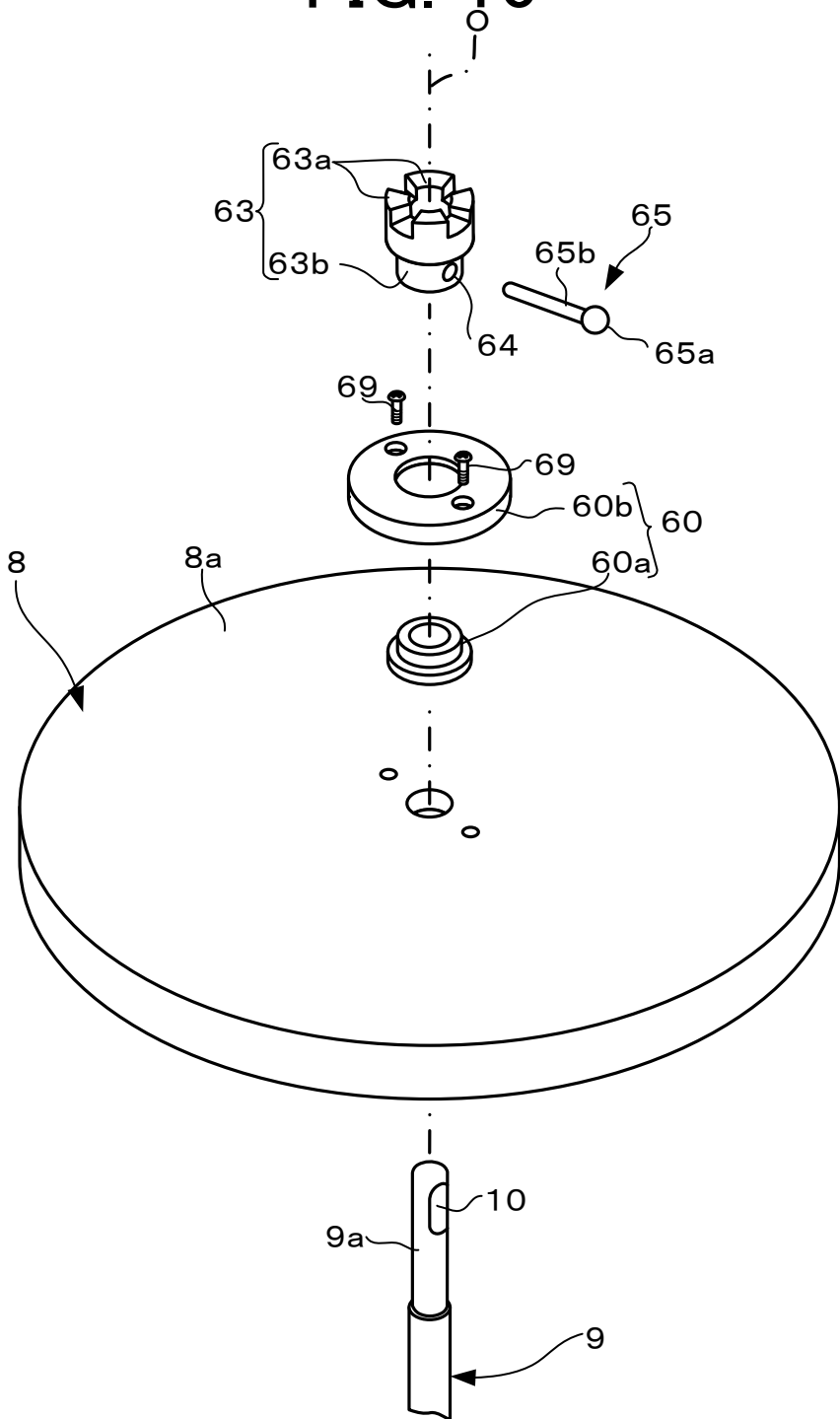


FIG. 11

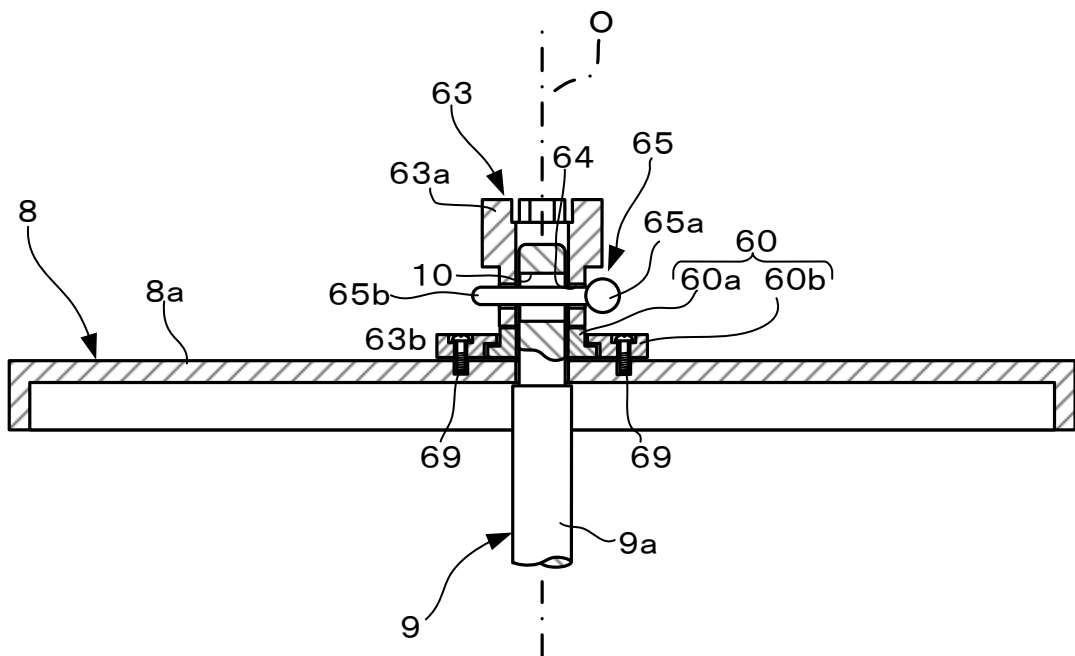


FIG. 12

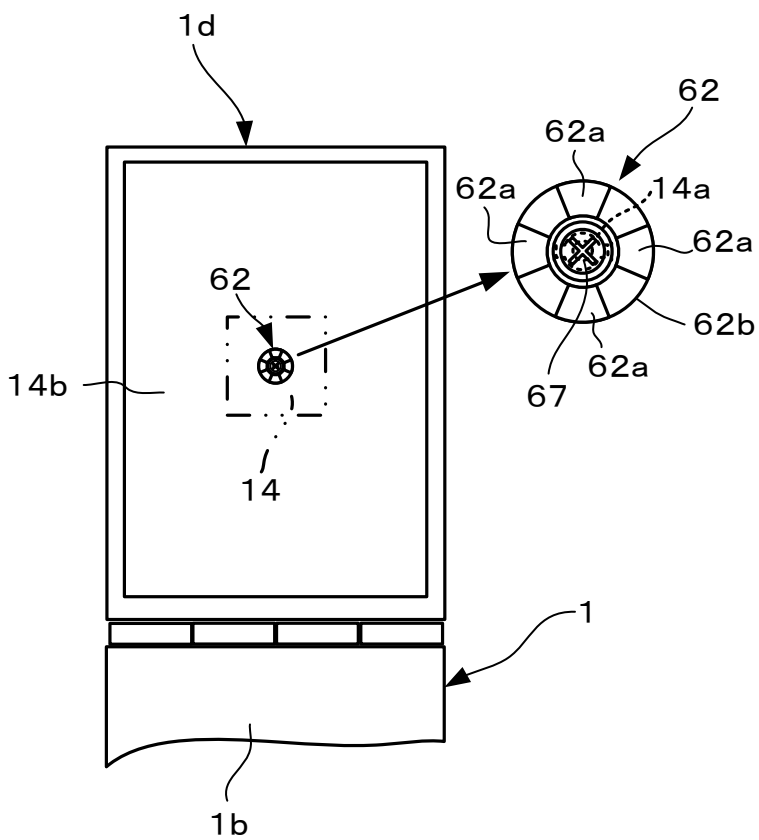
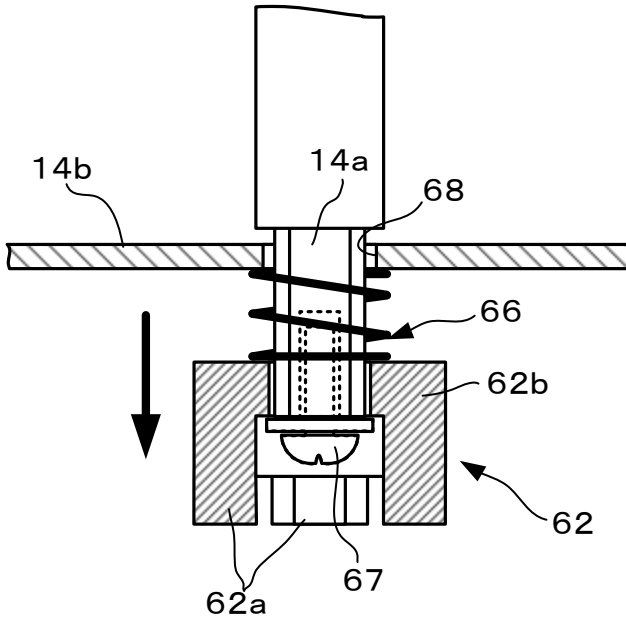


FIG. 13

(a)



(b)

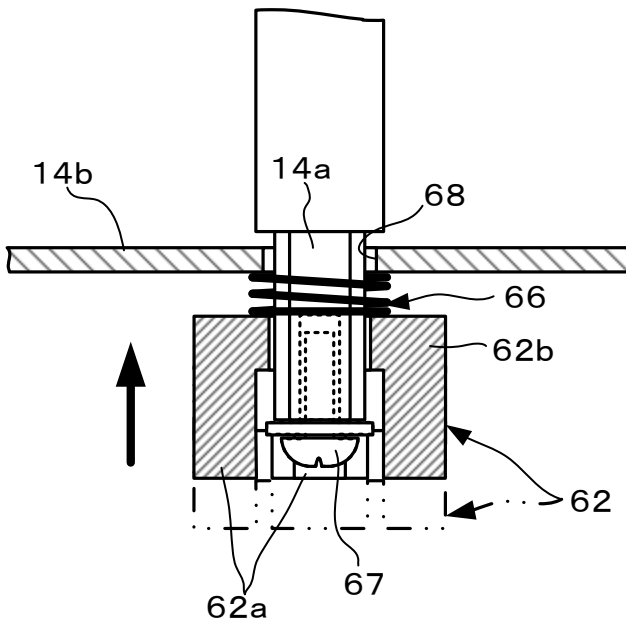


FIG. 14

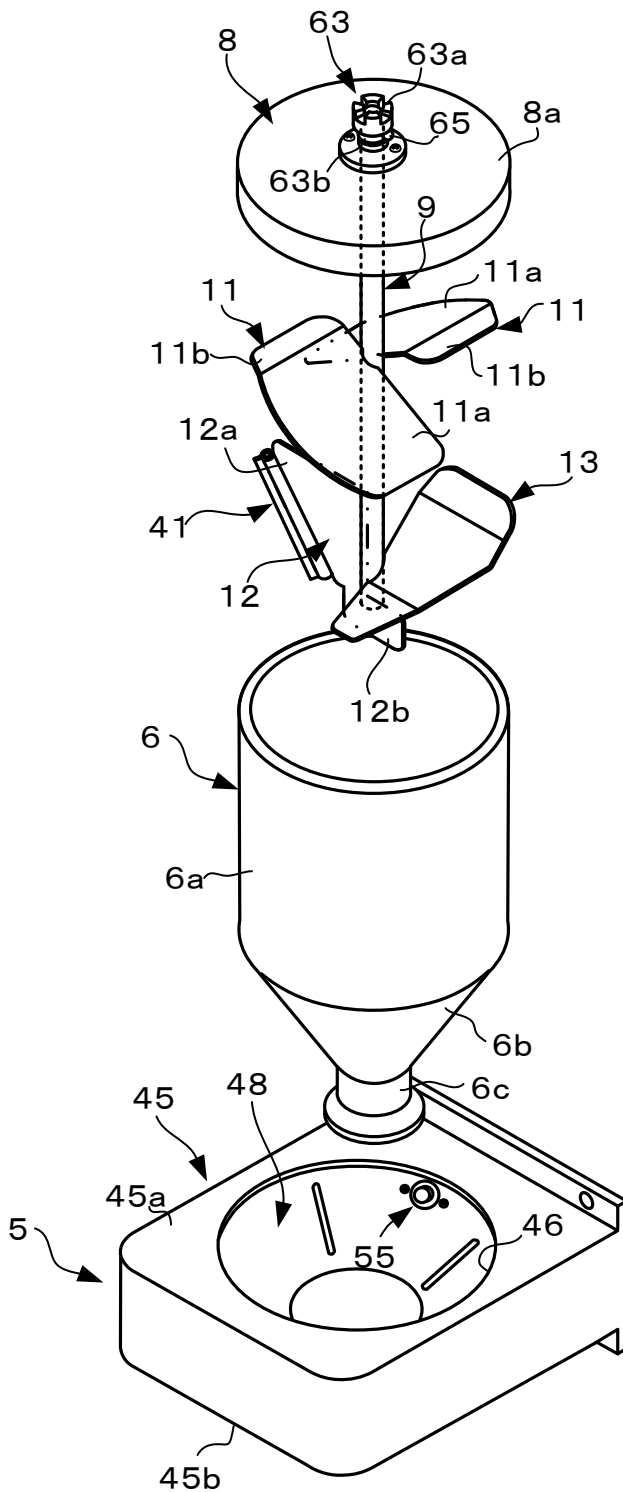


FIG. 15

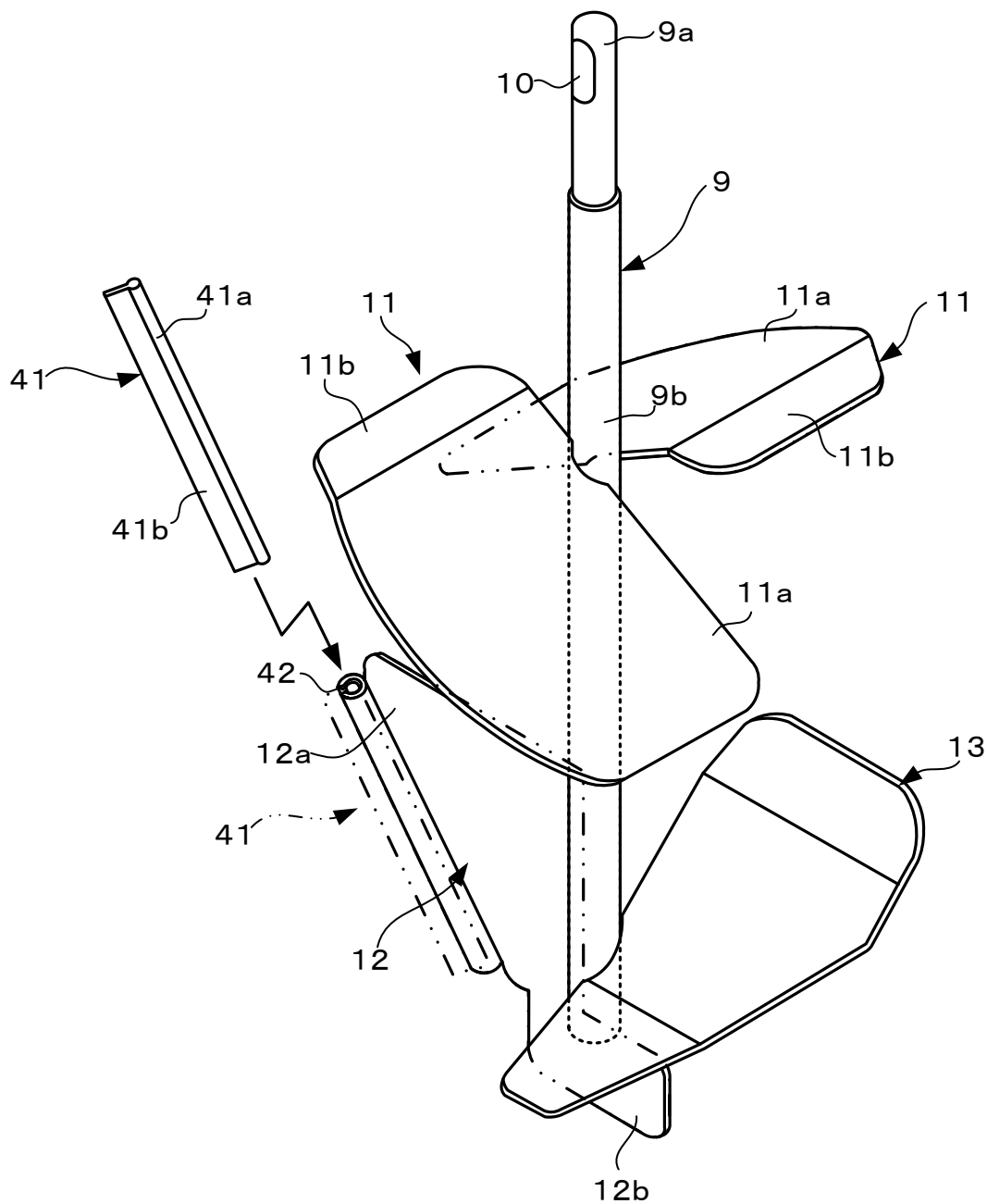


FIG. 16

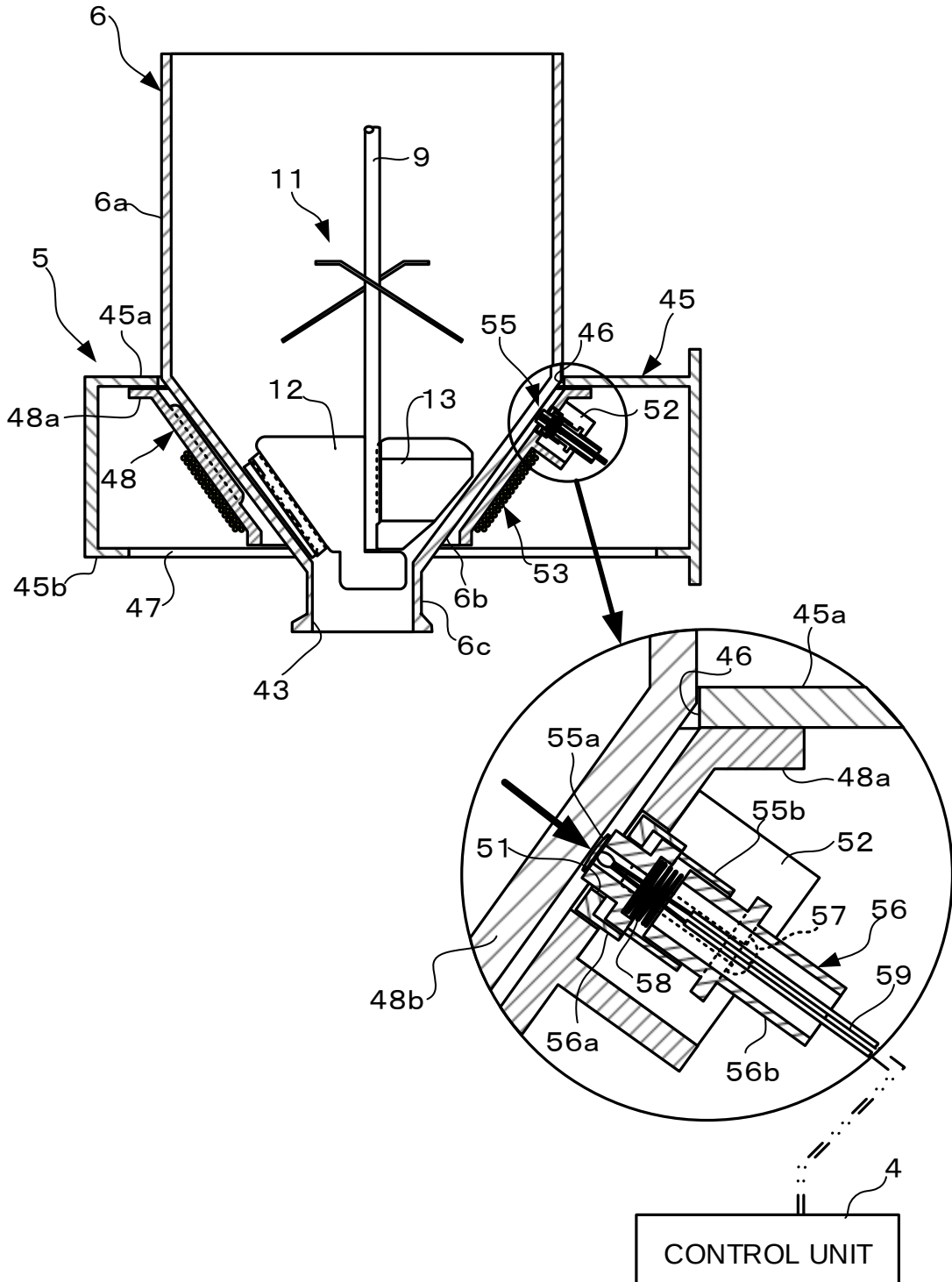


FIG. 17

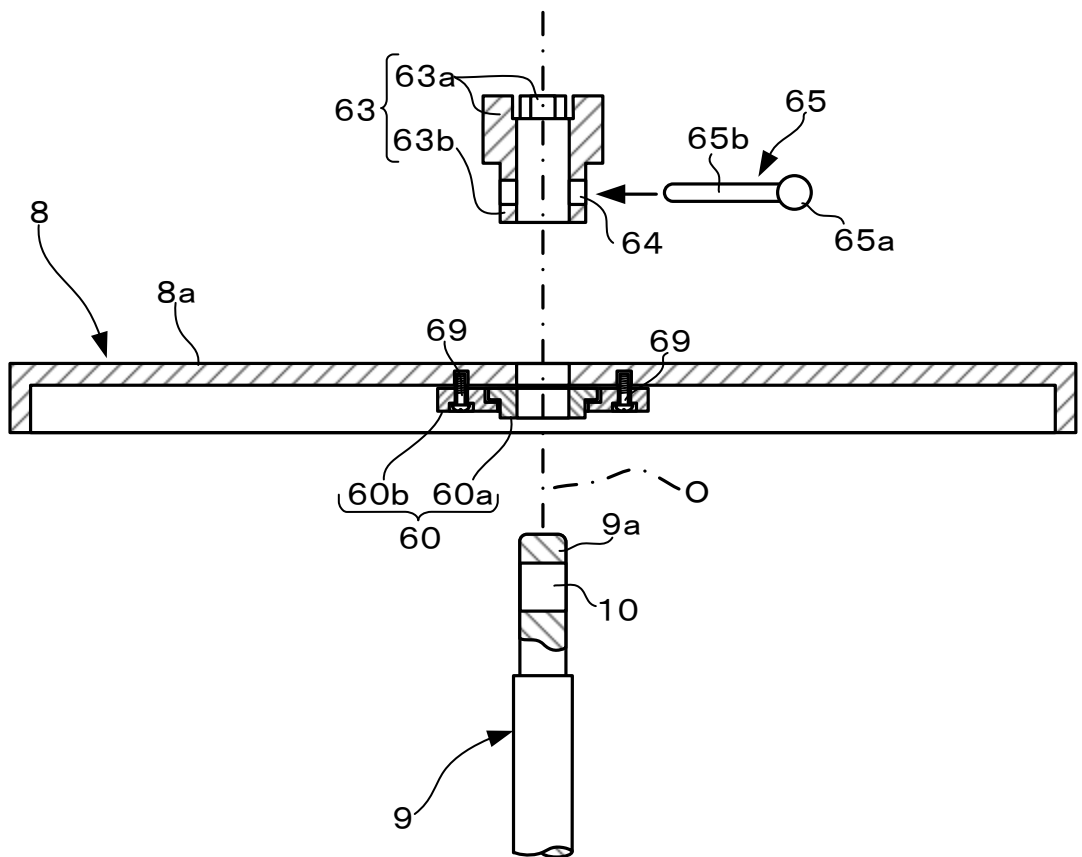
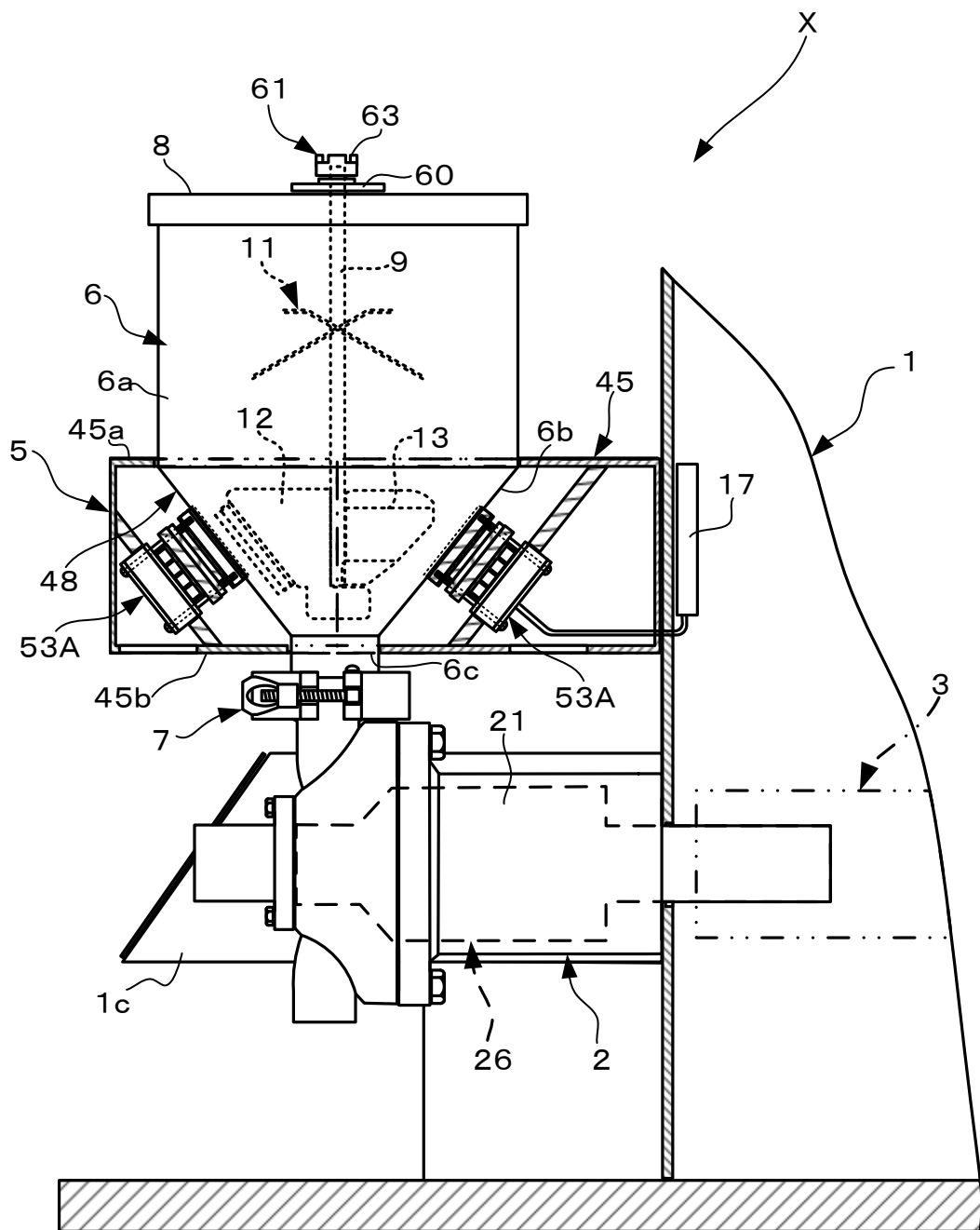


FIG. 18



Block diagram of the input/output section 19, 20. It shows an INPUT UNIT (21b, 21c) and a DISPLAY UNIT (21a) connected by a dashed line.

