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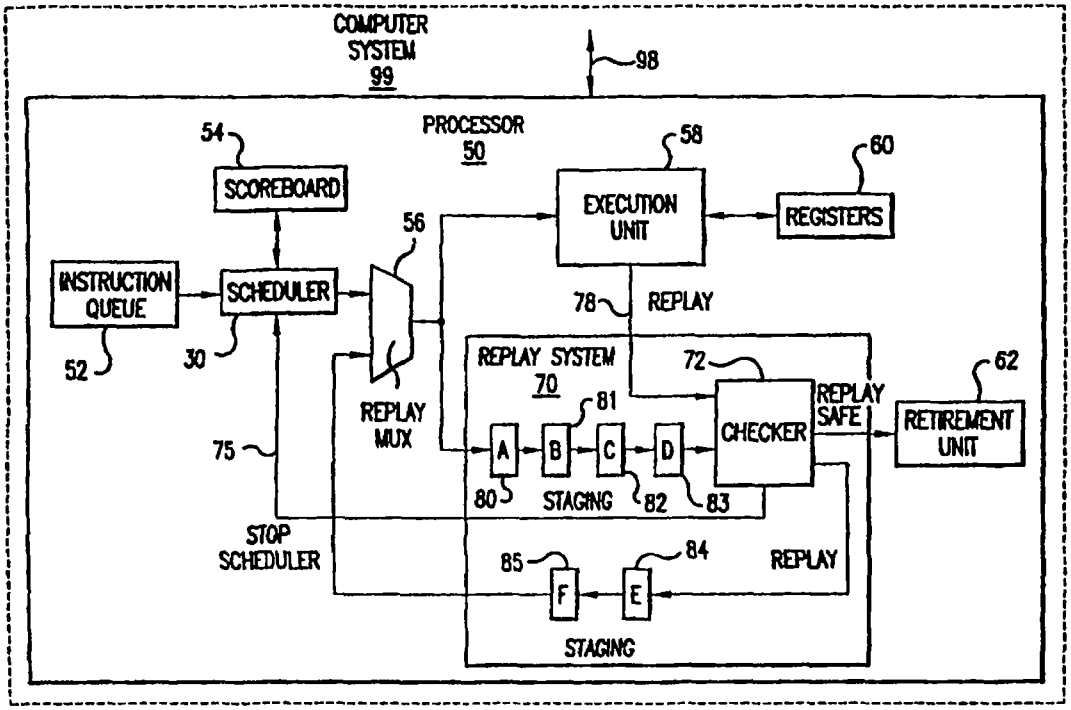
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(54) Title: A COMPUTER PROCESSOR HAVING A REPLAY UNIT



(57) Abstract

A computer processor that has a checker for receiving an instruction. The checker includes a scoreboard, an input for receiving an external replay signal, and decision logic coupled to the scoreboard and the input. The decision logic determines whether the instruction executed correctly based on both the scoreboard and the external replay signal.