

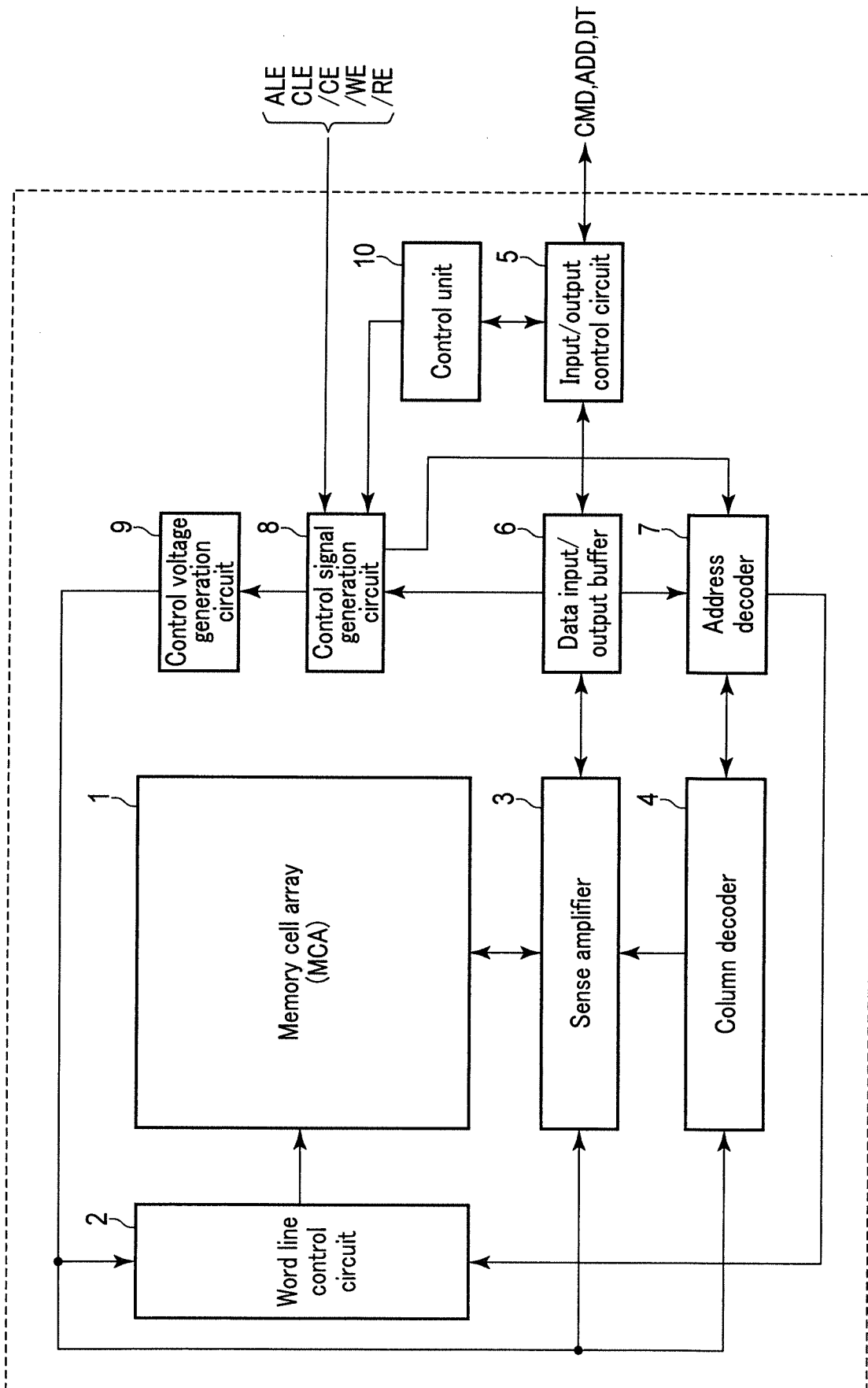


FIG. 1

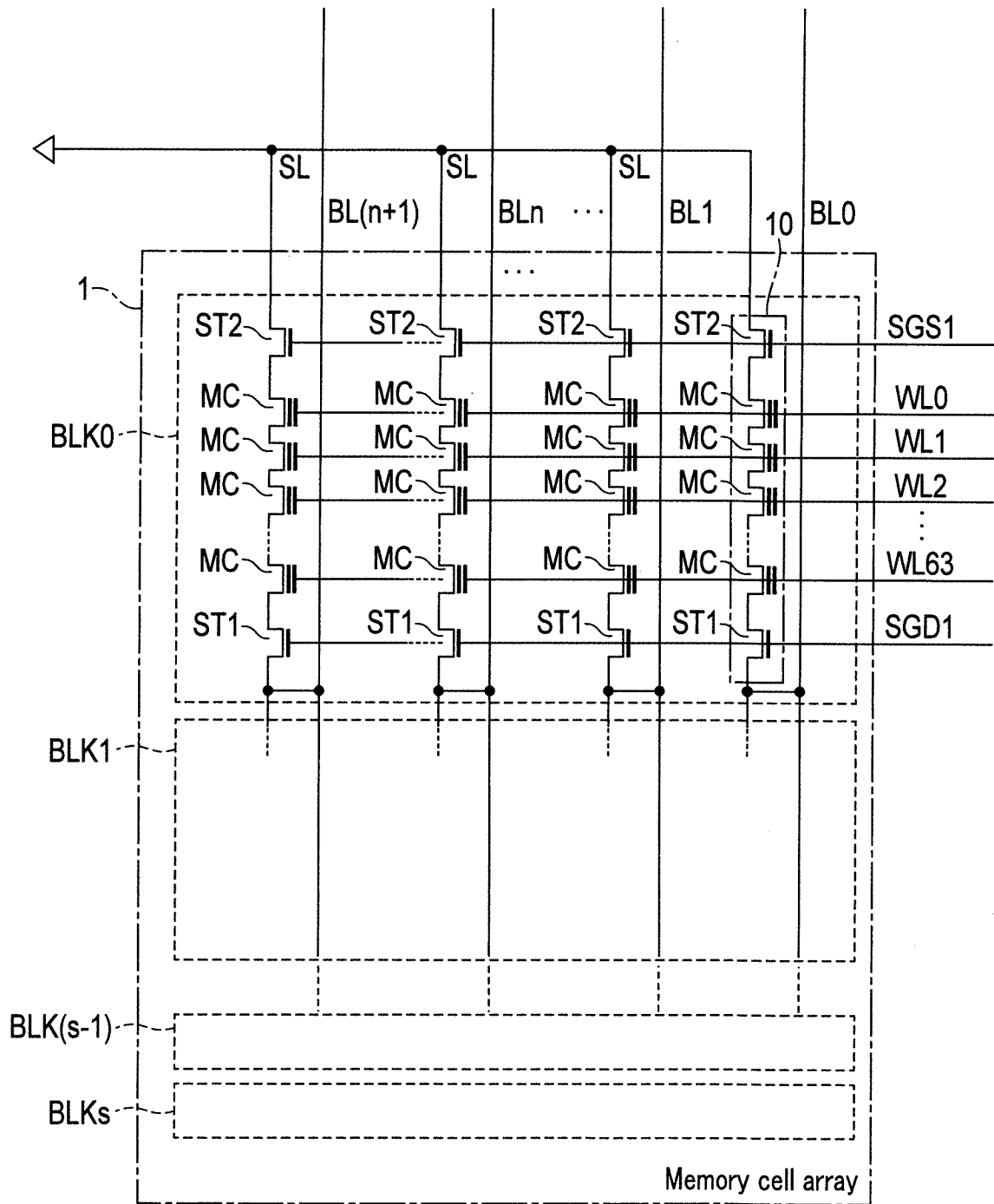


FIG. 2

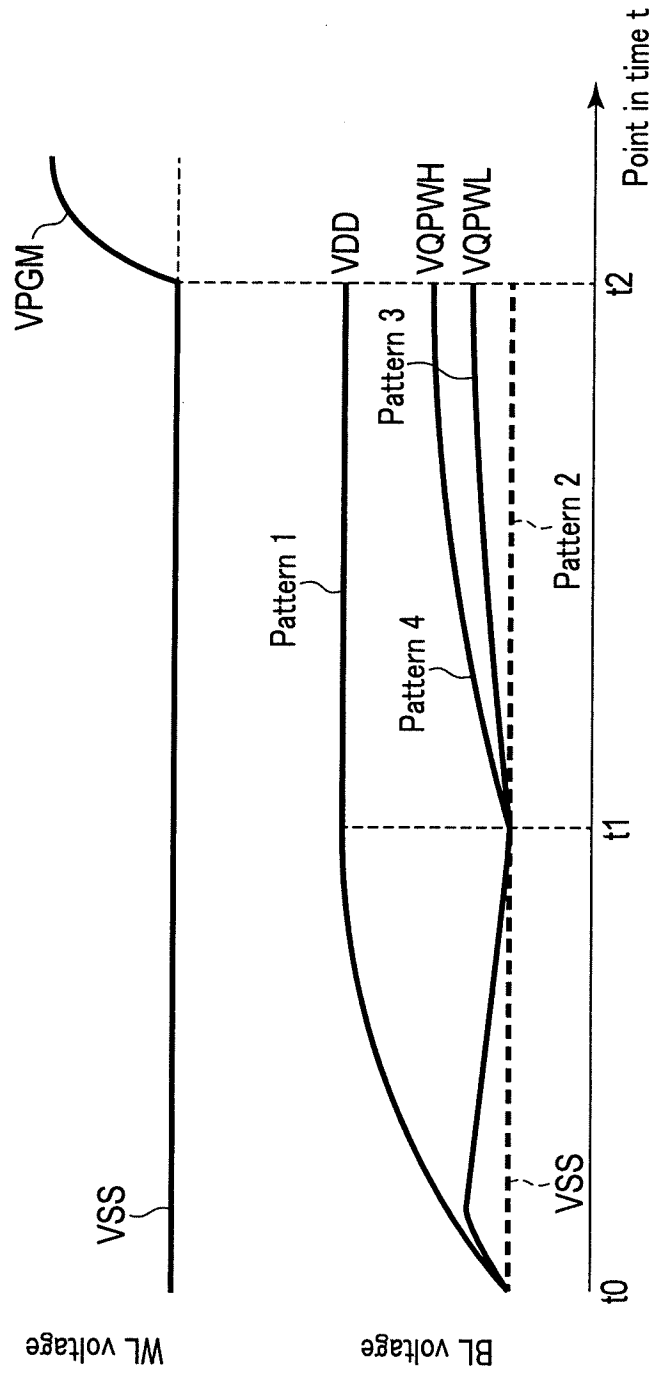


FIG. 4A

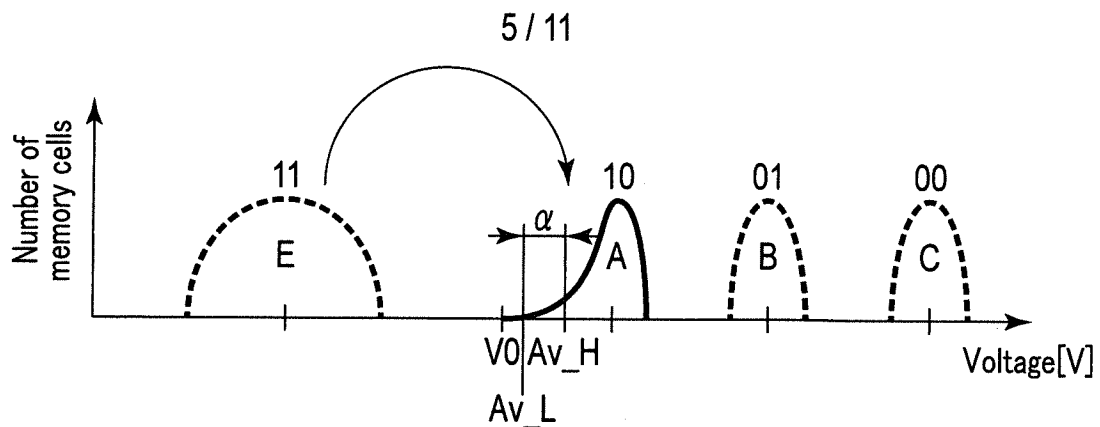


FIG. 4B

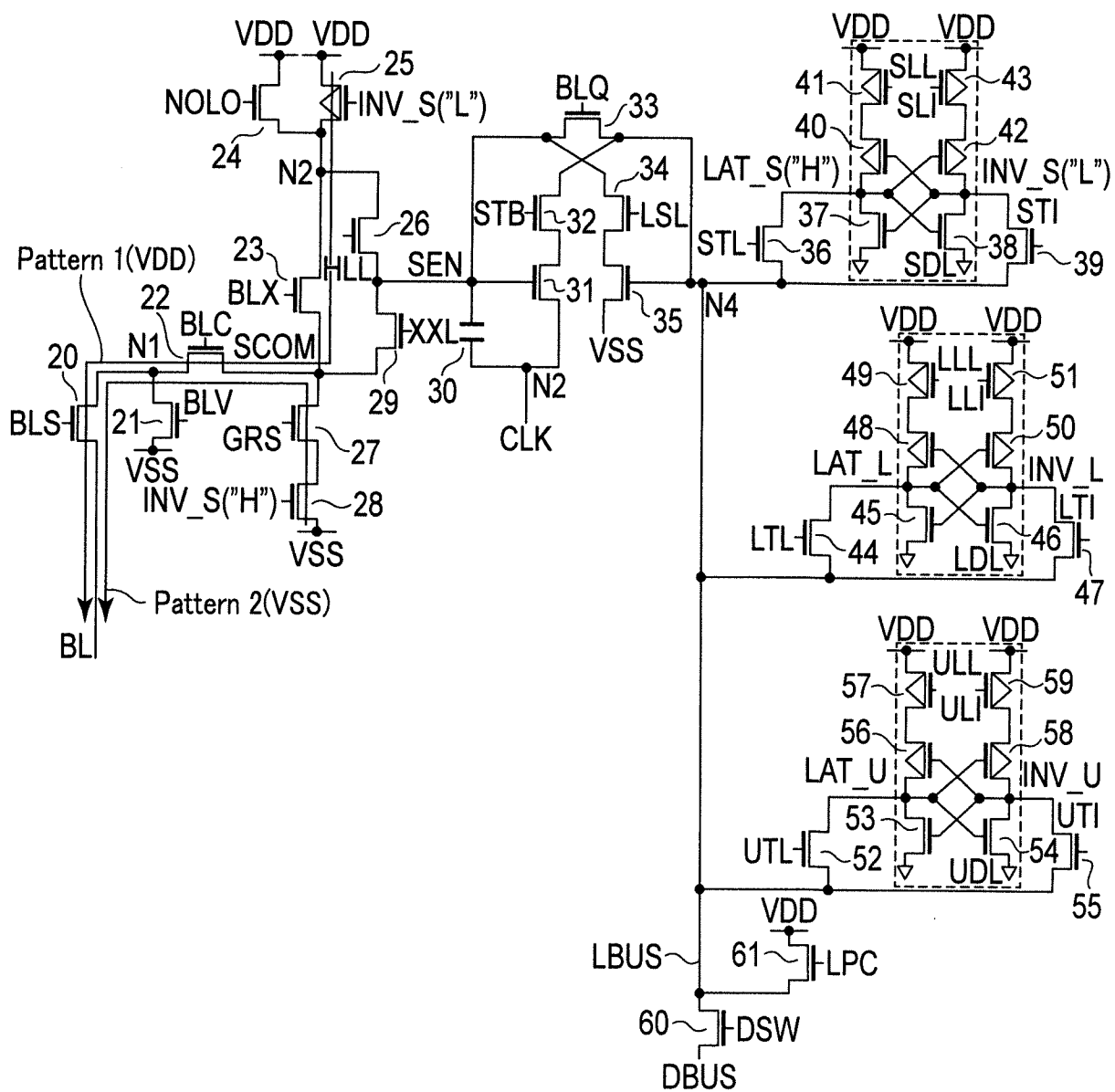


FIG. 5

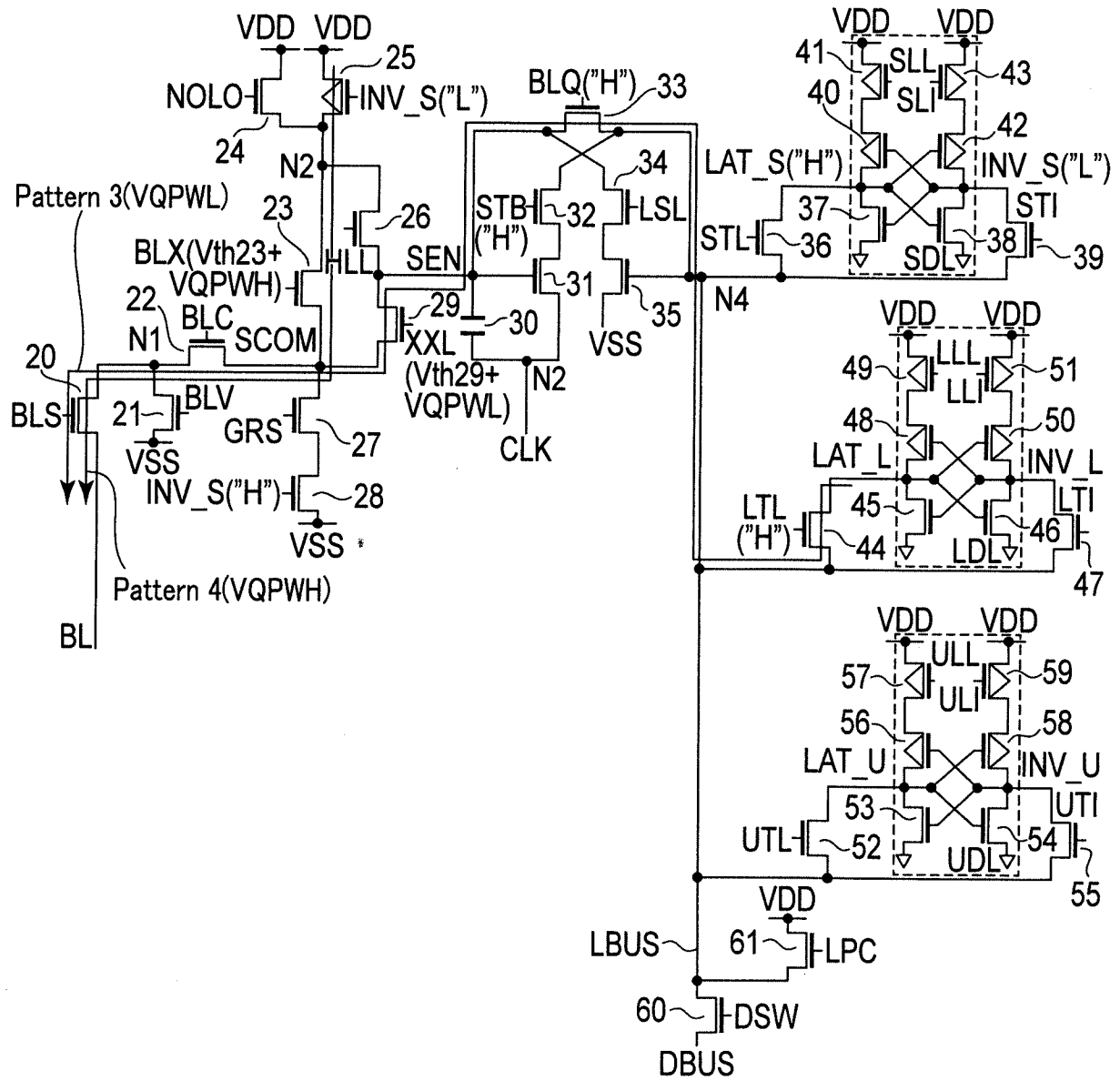


FIG. 6

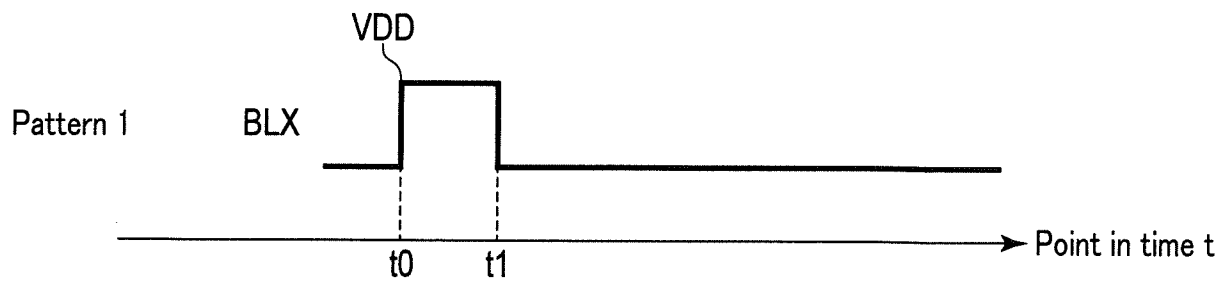


FIG. 7A

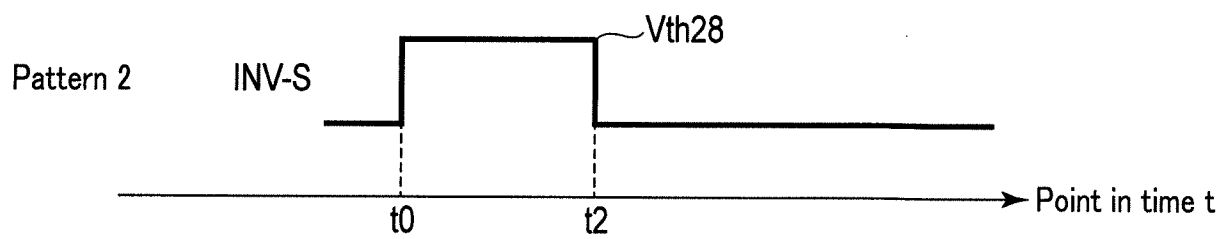


FIG. 7B

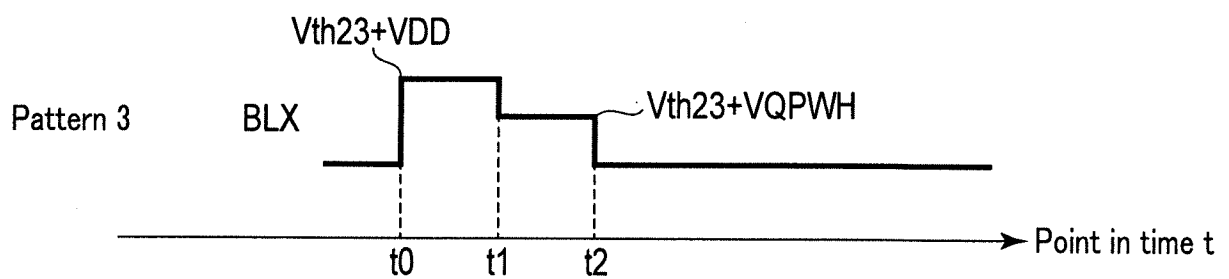


FIG. 7C

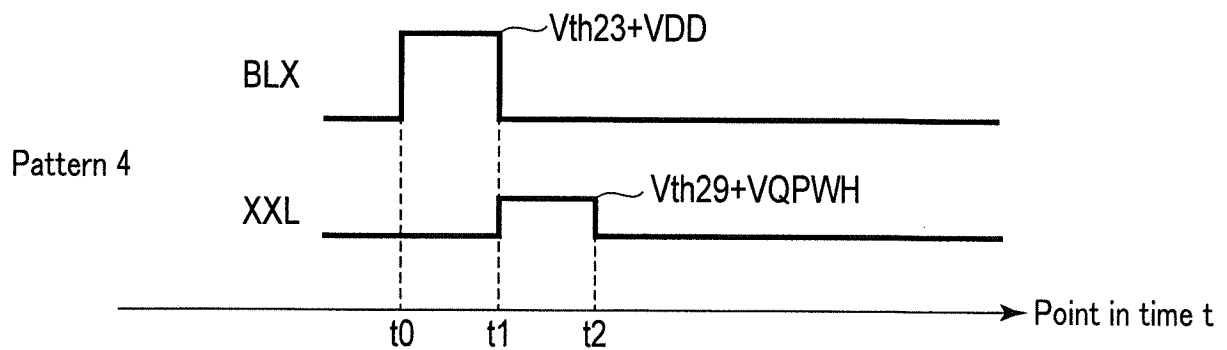


FIG. 7D

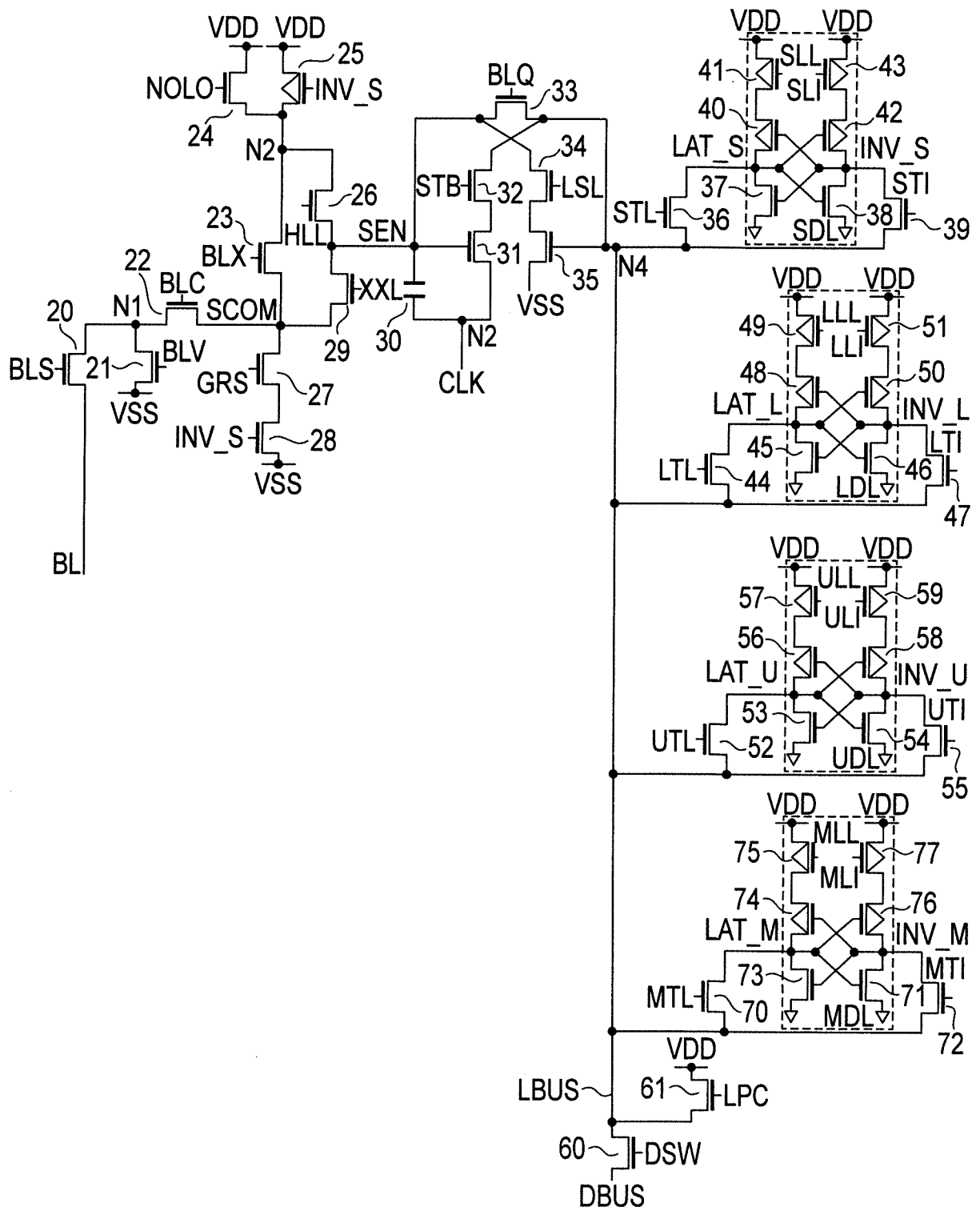


FIG. 8

		UDL	
		LAT_U	INV_U
{	E 111	1	0
	A 011	0	1
	B 001	0	1
	C 101	1	0
{	D 100	1	0
	E 000	0	1
	F 010	0	1
	G 110	1	0

FIG. 9A

MDL		LDL	
LAT_M	INV_M	LAT_L	INV_L
1	0	1	0
1	0	1	0
0	1	1	0
0	1	1	0
0	1	0	1
0	1	0	1
1	0	0	1
1	0	0	1

FIG. 9B

Before passing C level			
	PASS	Fail	First write target
E	VDD	-	-
A	VDD	VQPWL	VPQWH
B	VDD	VQPWL	VPQWH
C	VDD	VQPWL	VPQWH
D	VDD	VSS	VPQWH
E	VDD	VSS	VPQWH
F	VDD	VSS	VPQWH
G	VDD	VSS	VPQWH

F I G. 9C

After passing C level			
	PASS	Fail	First write target
	VDD	-	-
	VDD	-	-
	VDD	-	-
	VDD	-	-
	VDD	VQPWL	VPQWH
	VDD	VQPWL	VPQWH
	VDD	VSS	VPQWH
	VDD	VSS	VPQWH

F I G. 9D

After passing E level			
	PASS	Fail	First write target
E	VDD	-	-
A	VDD	-	-
B	VDD	-	-
C	VDD	-	-
D	VDD	-	-
E	VDD	-	-
F	VDD	VQPWL	VPQWH
G	VDD	VSS	VPQWH

FIG. 9E