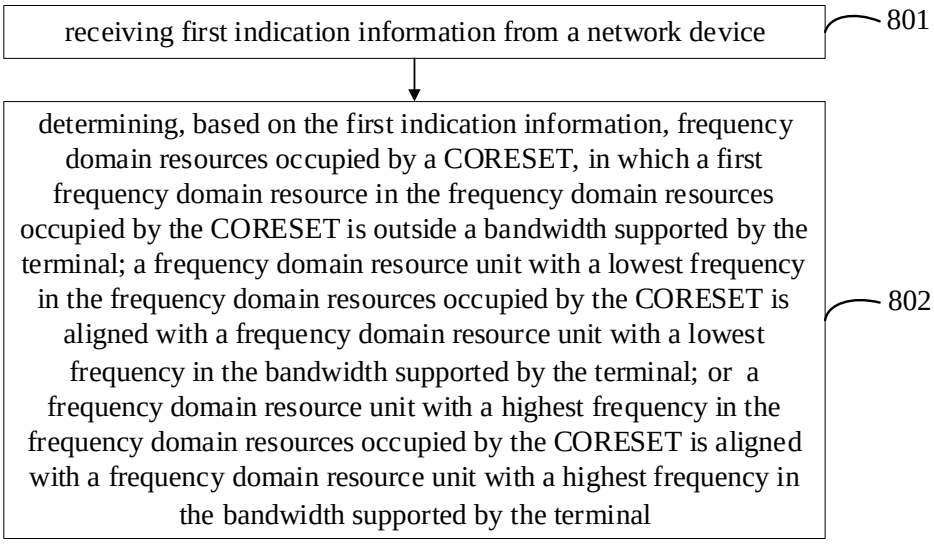


FIG. 8

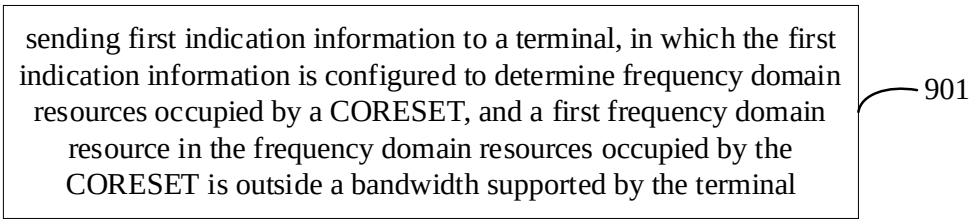


FIG. 9

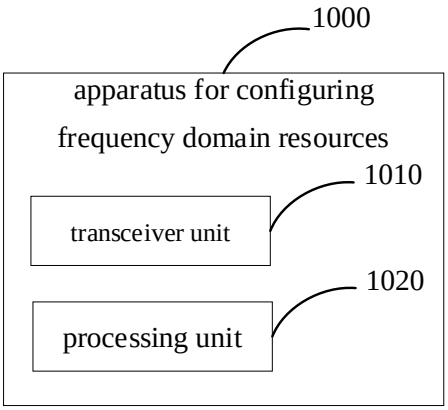


FIG. 10

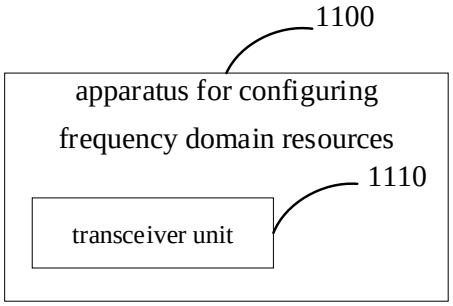


FIG. 11

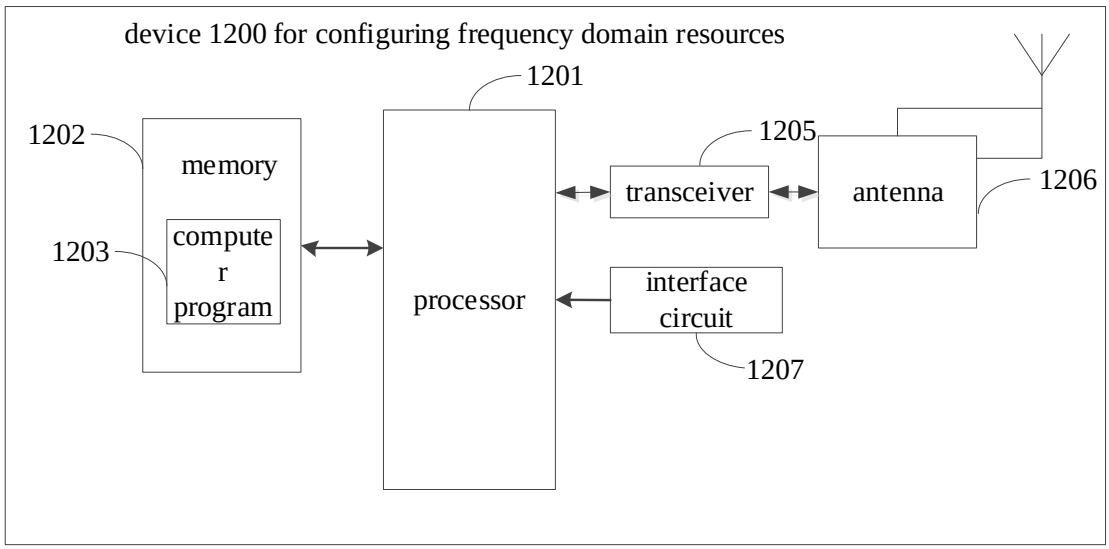

device 1200 for configuring frequency domain resources
1201
1202
memory
1203
compute
r
program
processor
1205
transceiver
antenna
1206
interface
circuit
1207

FIG. 12

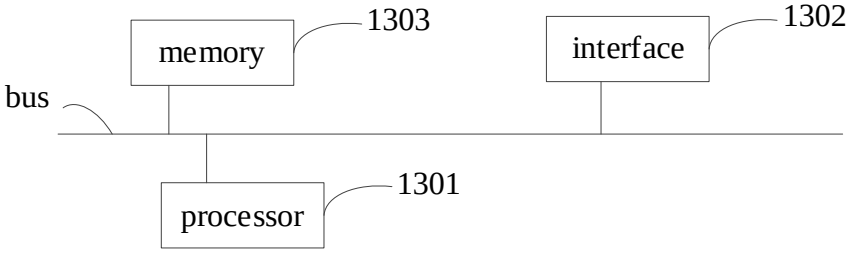

1303
memory
1302
interface
bus
1301
processor

FIG. 13