

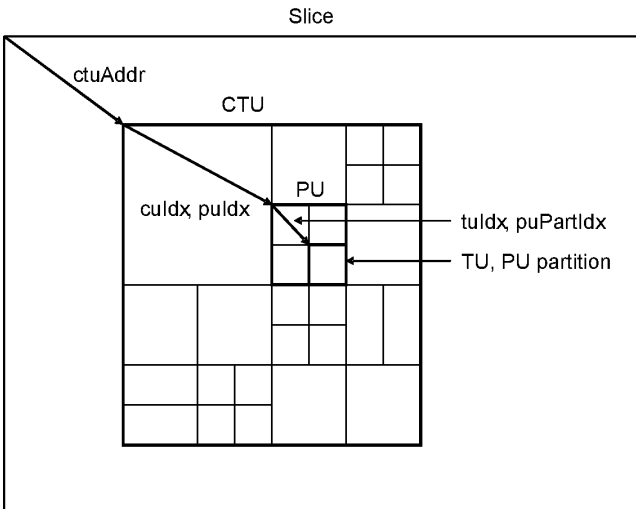


Figure 1

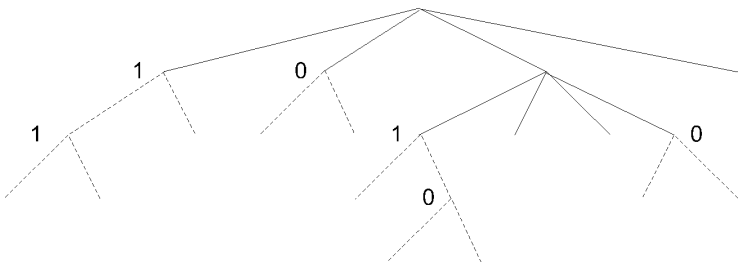
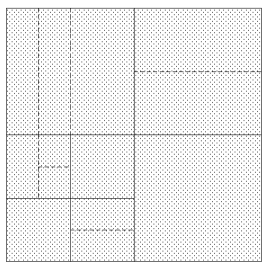


Figure 2

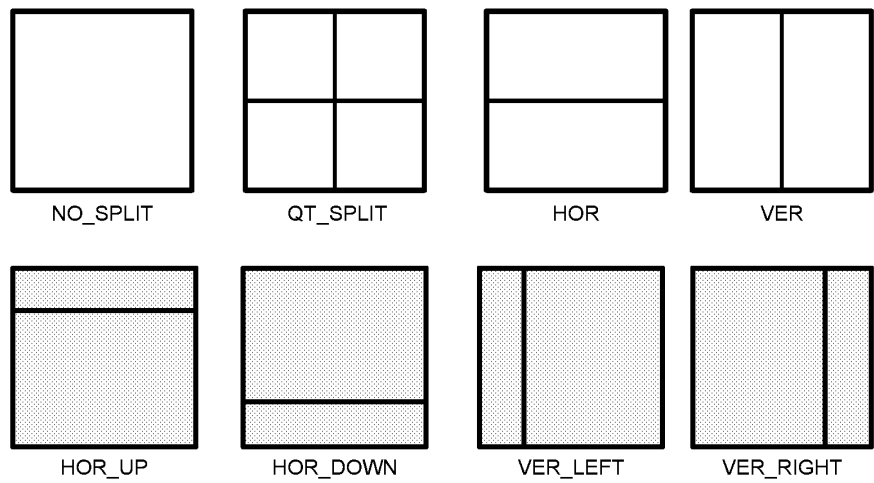


Figure 3

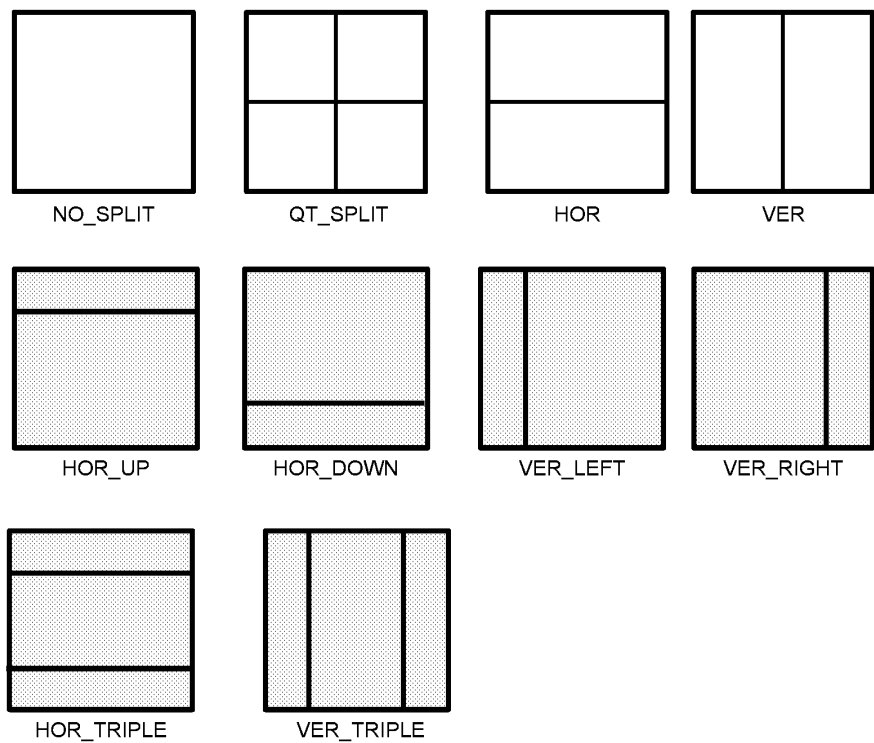


Figure 4

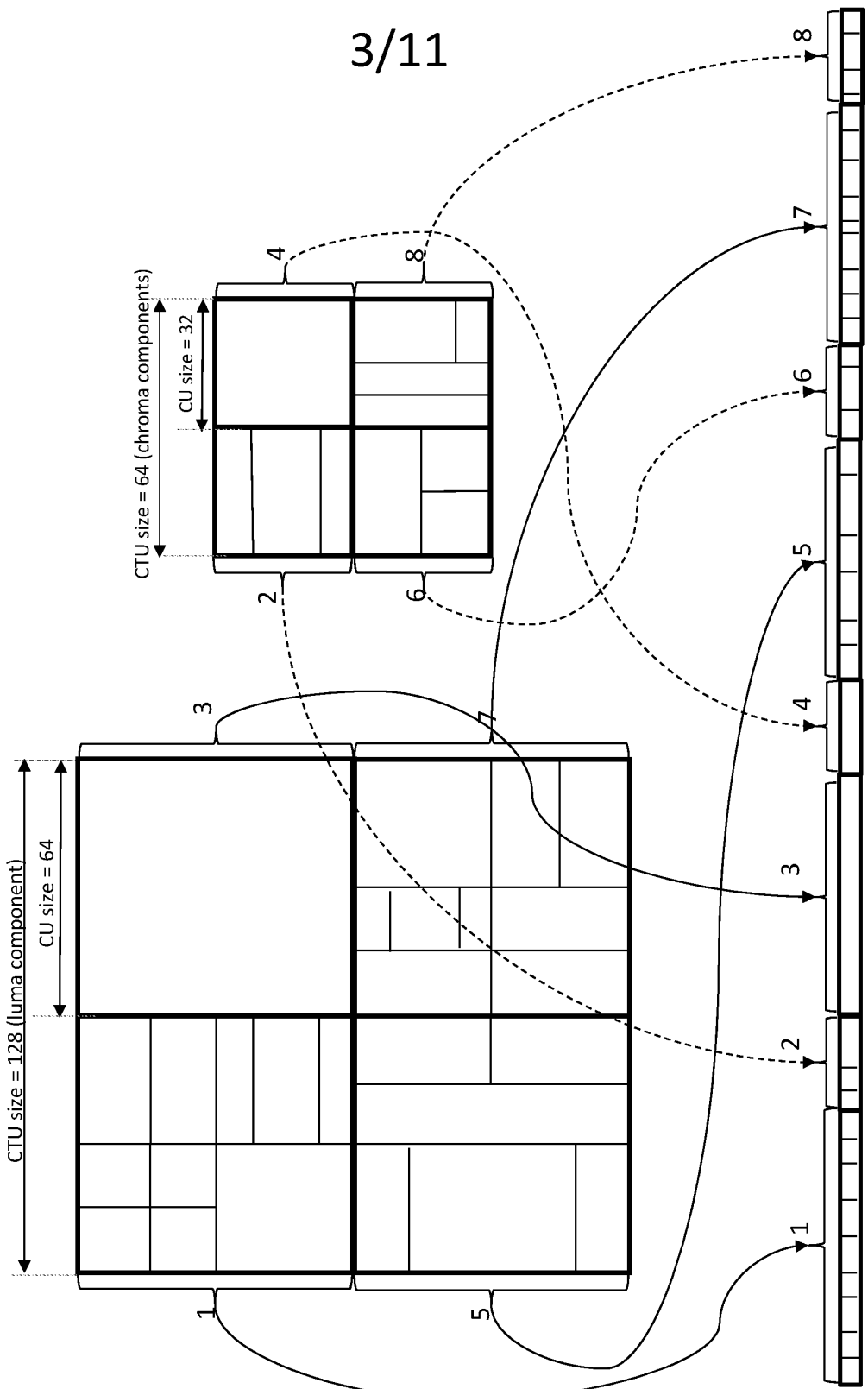


Figure 5

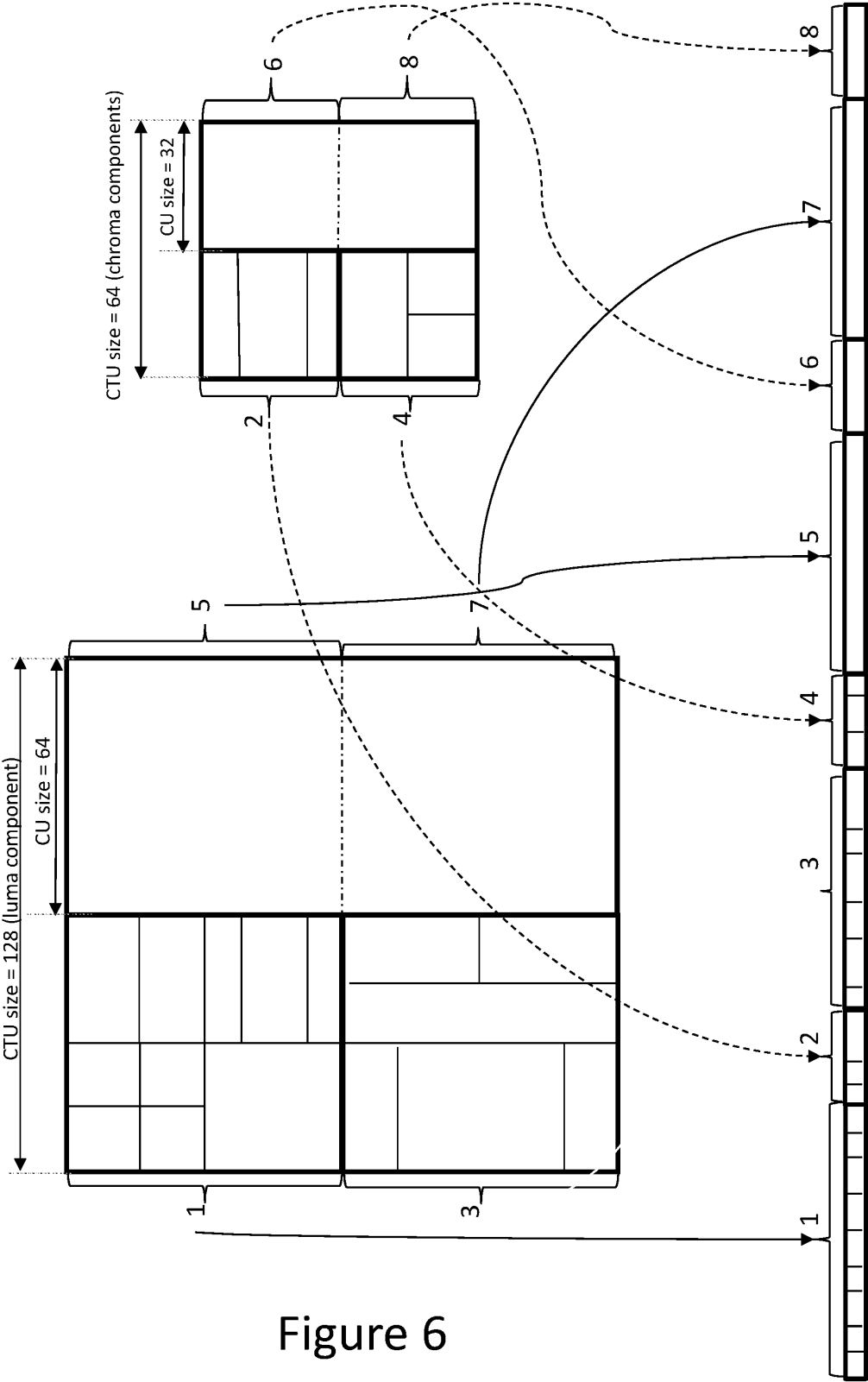


Figure 6

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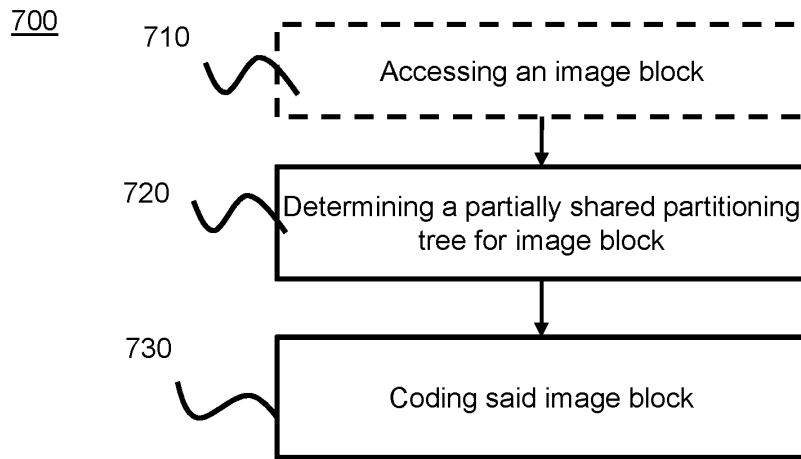


Figure 7

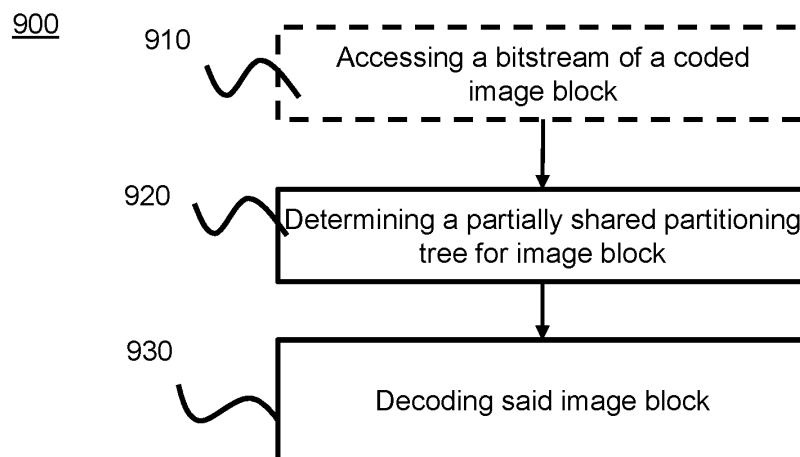


Figure 9

6/11

720

- Coding tree to process *currCU* with size (*w*; *h*) and position (*x*; *y*)
- Decoding Pipeline Unit Size: DPU (typically 64)

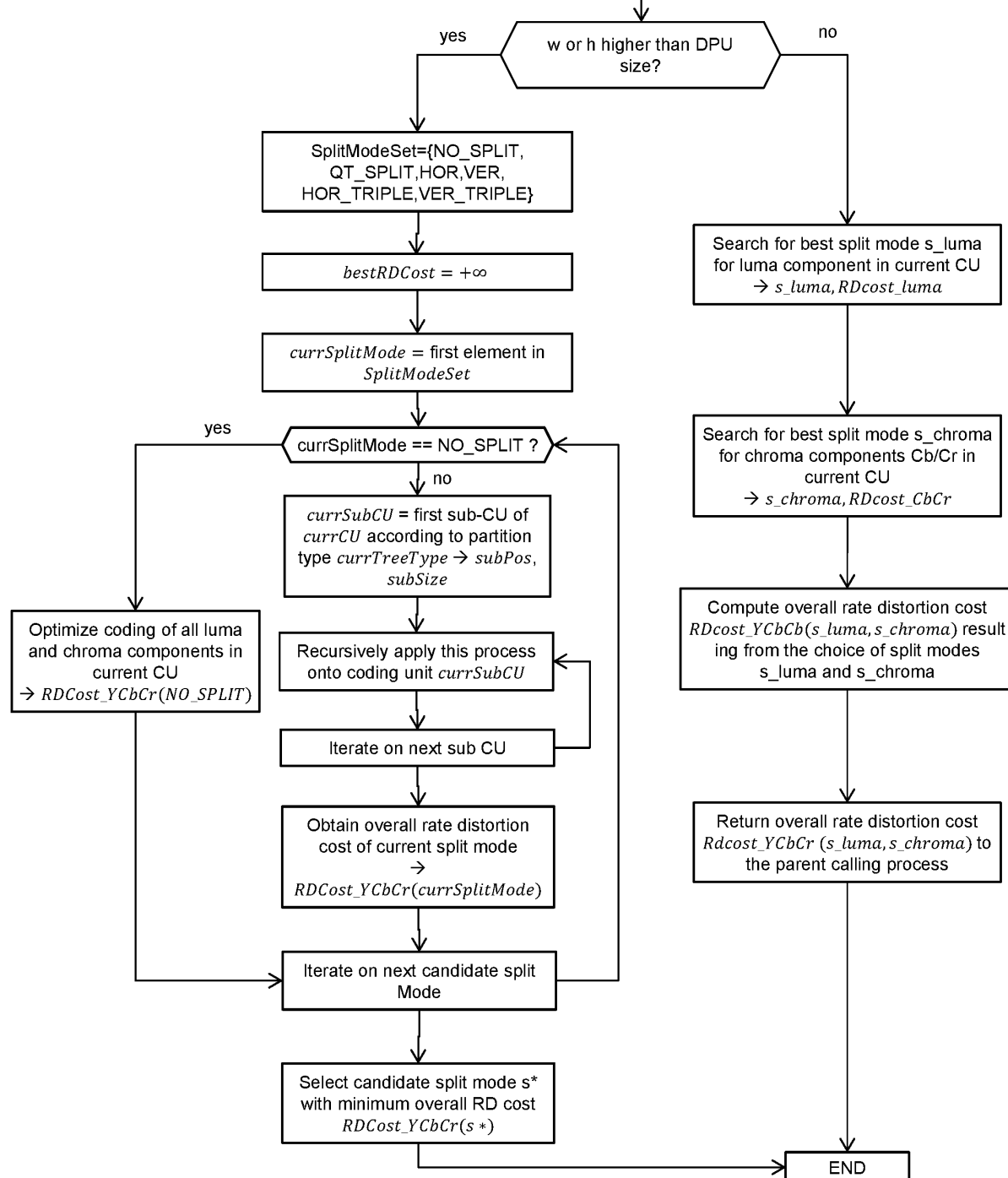


Figure 8

7/11

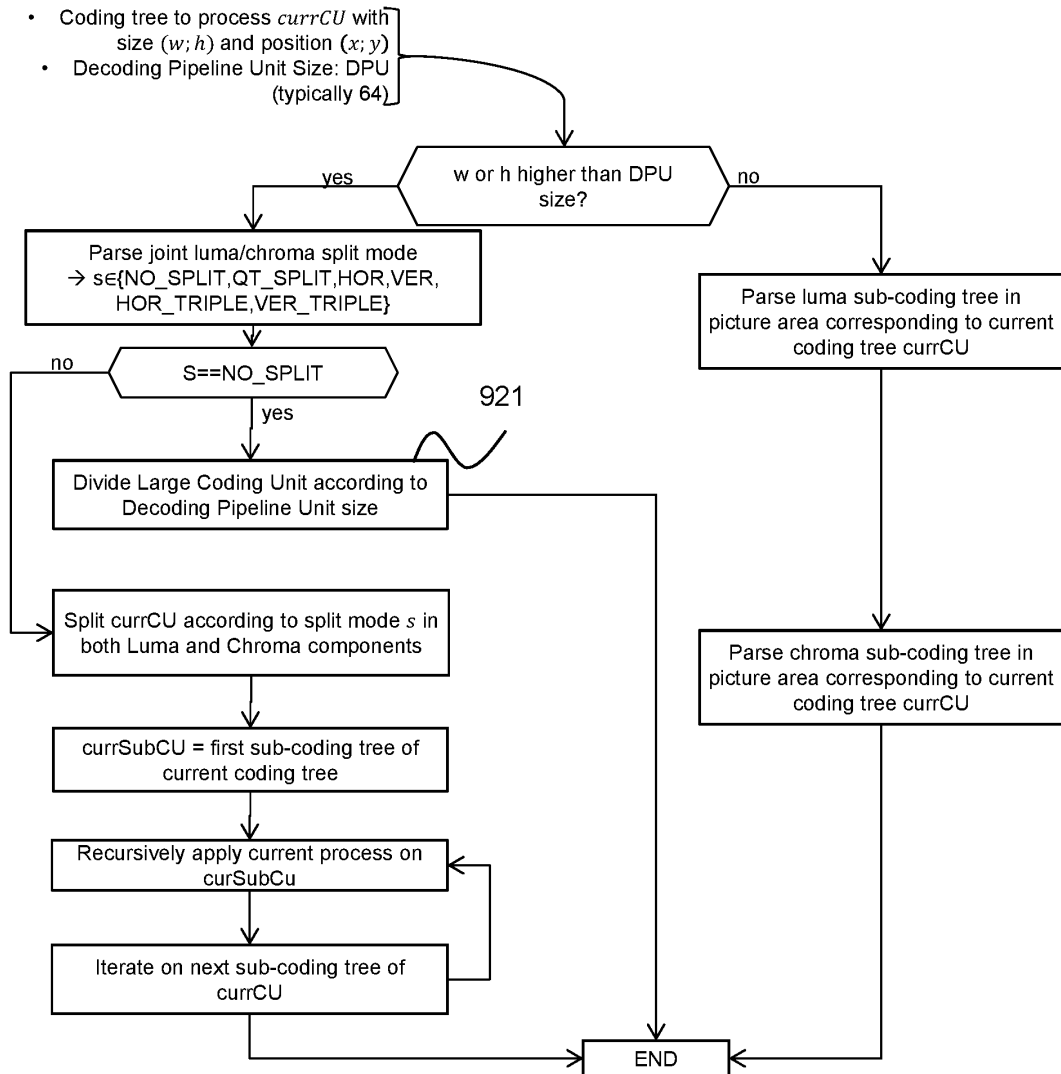


Figure 10

8/11

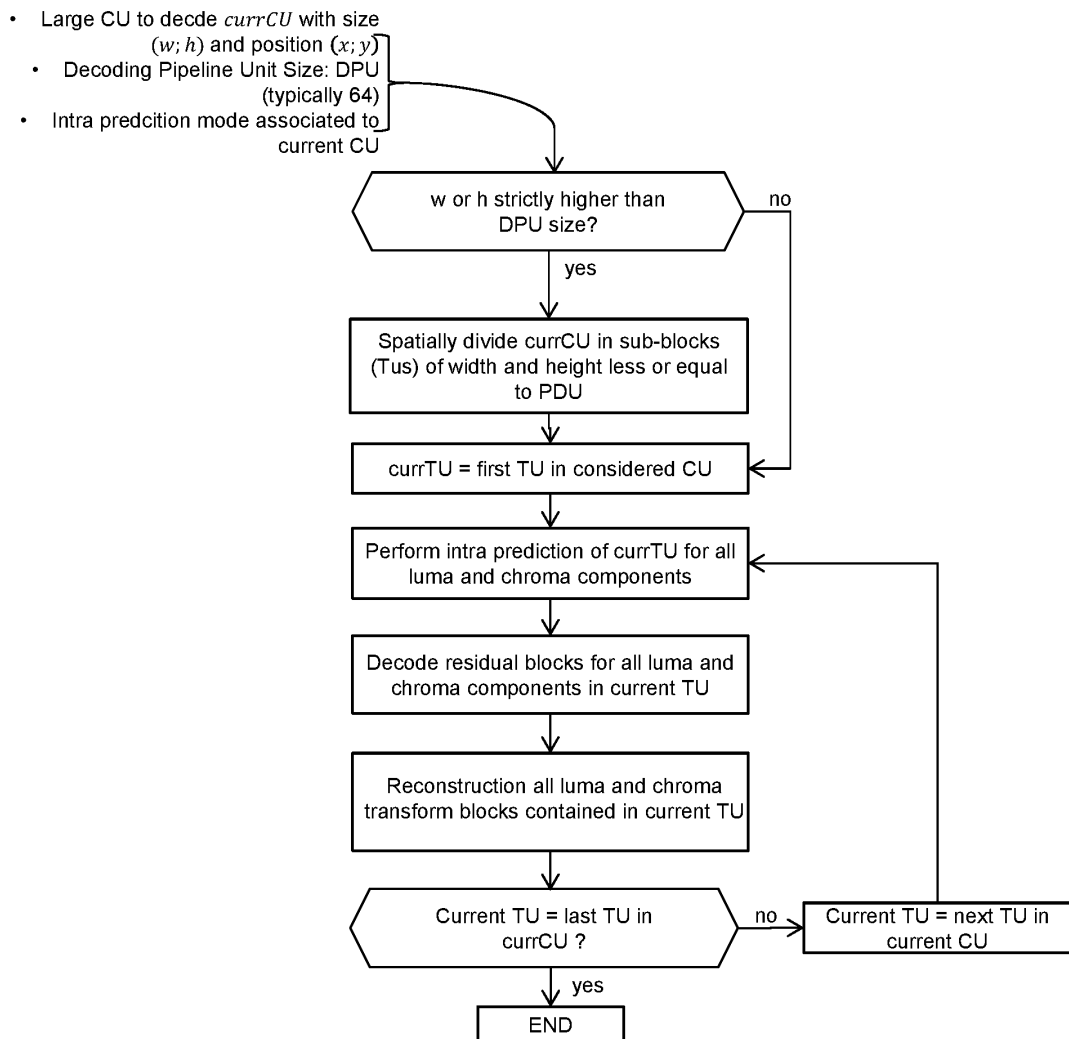


Figure 11

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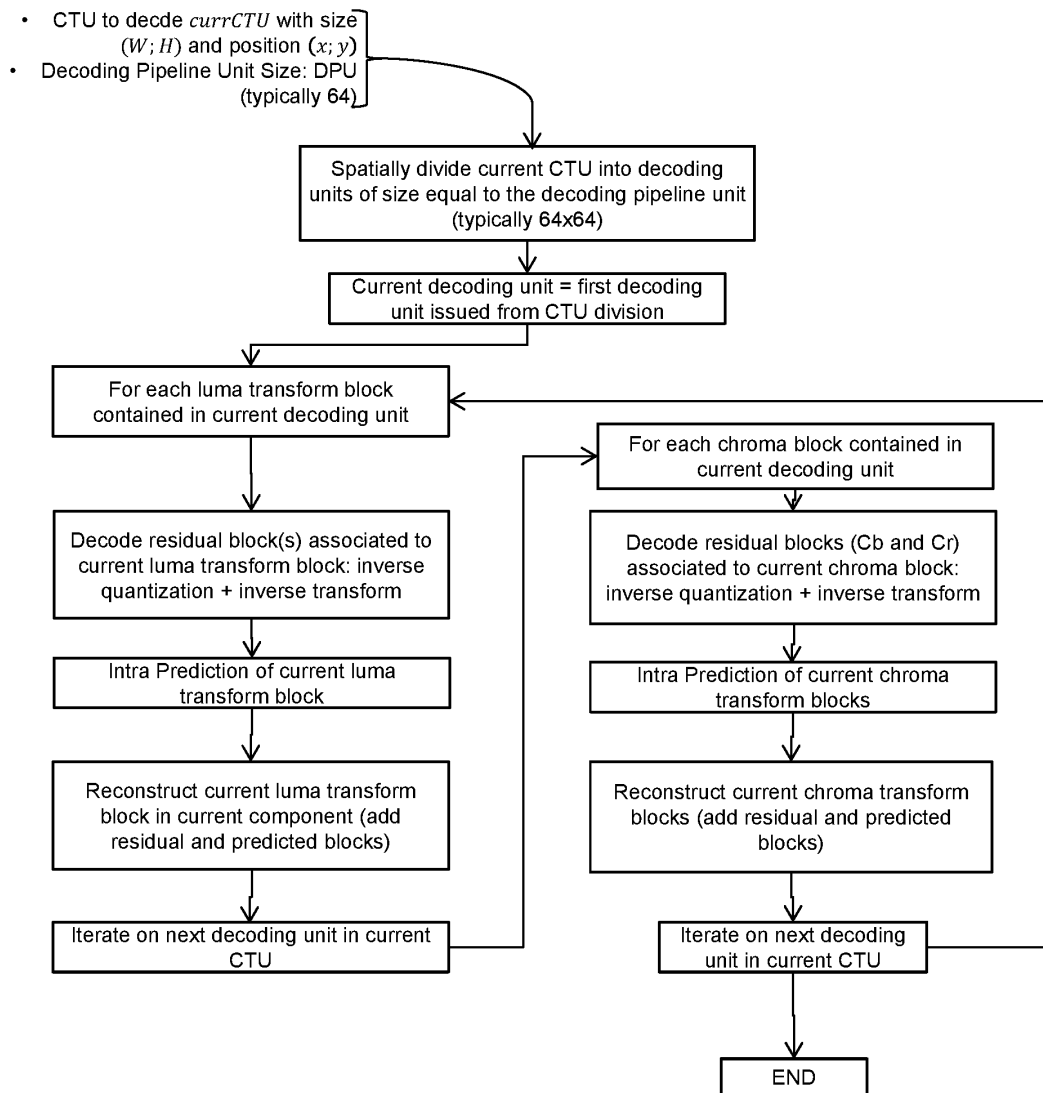


Figure 12

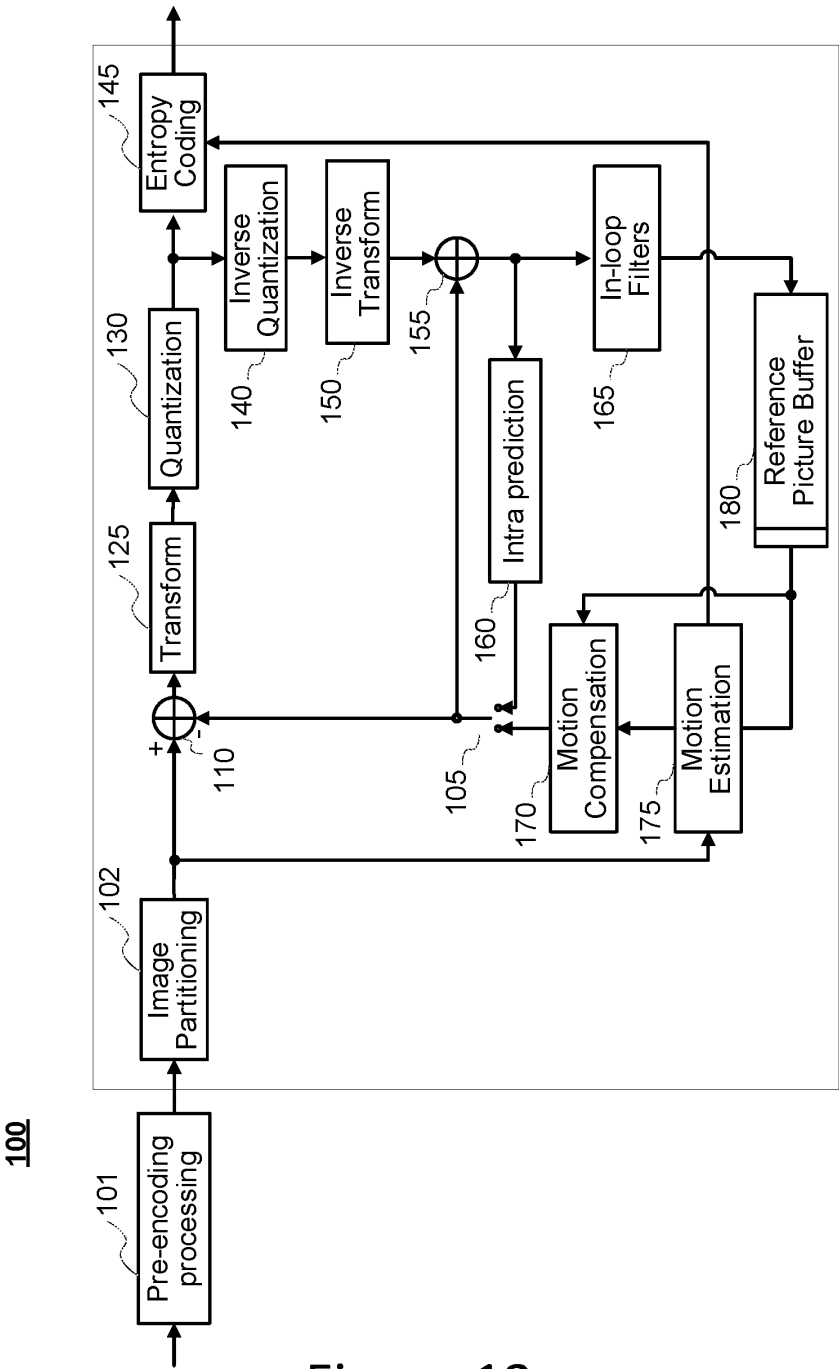


Figure 13

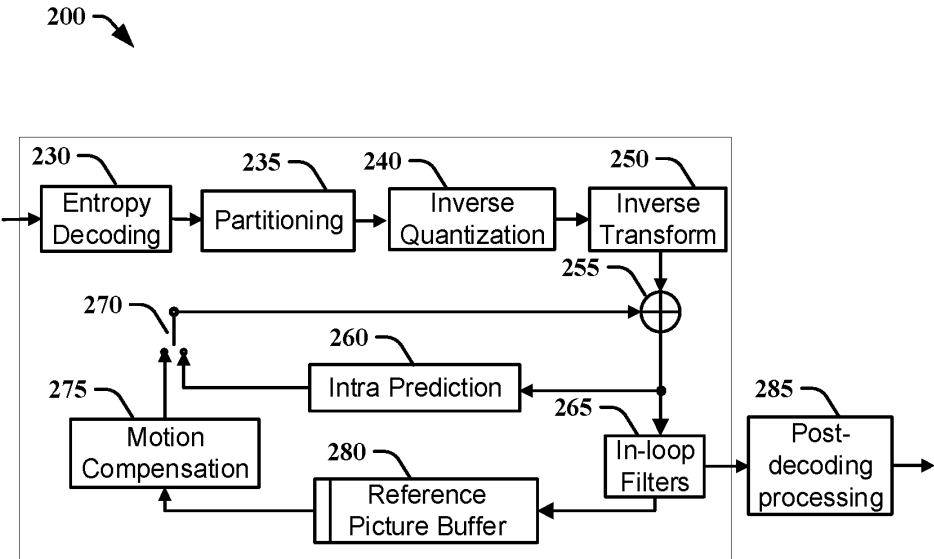


Figure 14

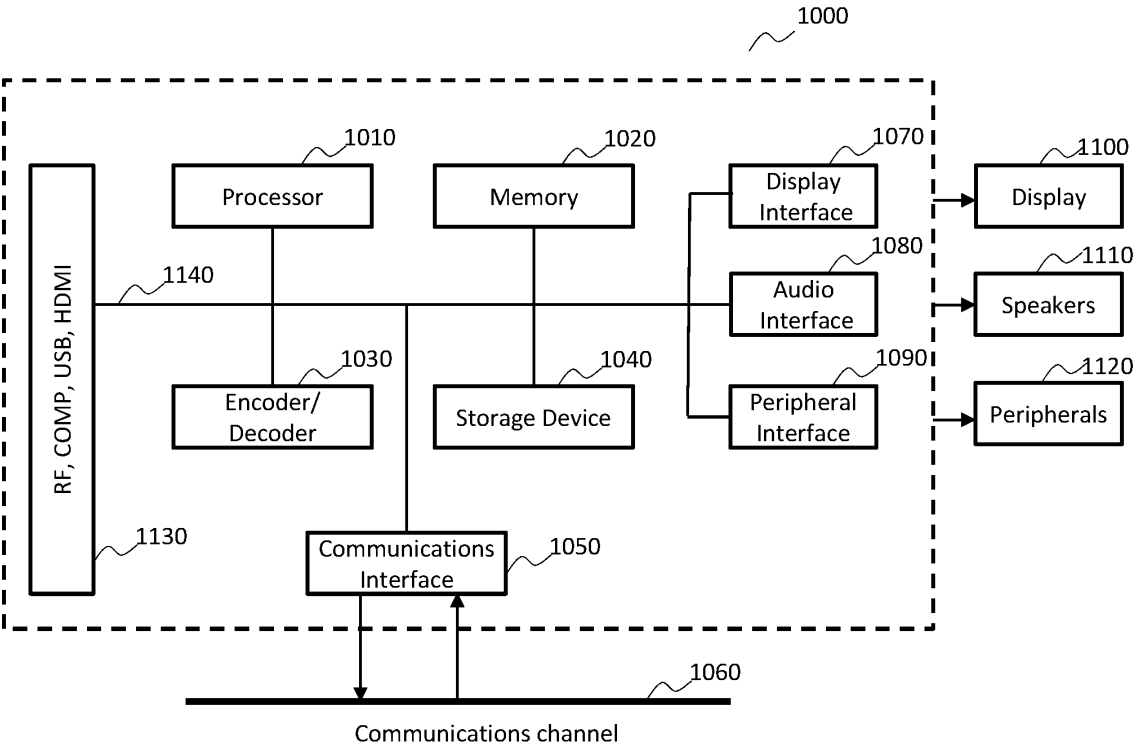


Figure 15