

(19) World Intellectual Property Organization  
International Bureau



(43) International Publication Date  
21 June 2007 (21.06.2007)

PCT

(10) International Publication Number  
**WO 2007/068041 A1**

(51) International Patent Classification:

**H02H 9/00** (2006.01) **G01R 19/04** (2006.01)  
**G05F 1/455** (2006.01) **H02H 3/20** (2006.01)  
**H05B 39/04** (2006.01)

**James, Robert** [AU/AU]; 12 Park Terrace, Bowden, S.A. 5007 (AU).

(21) International Application Number:

PCT/AU2006/001882

(22) International Filing Date:

12 December 2006 (12.12.2006)

(25) Filing Language:

English

(26) Publication Language:

English

(30) Priority Data:

2005906949 12 December 2005 (12.12.2005) AU  
2005906990 12 December 2005 (12.12.2005) AU

(71) Applicant (for all designated States except US): **CLIP-SAL AUSTRALIA PTY LTD** [AU/AU]; 12 Park Terrace, Bowden, S.A. 5007 (AU).

(72) Inventor; and

(75) Inventor/Applicant (for US only): **VANDERZON,**

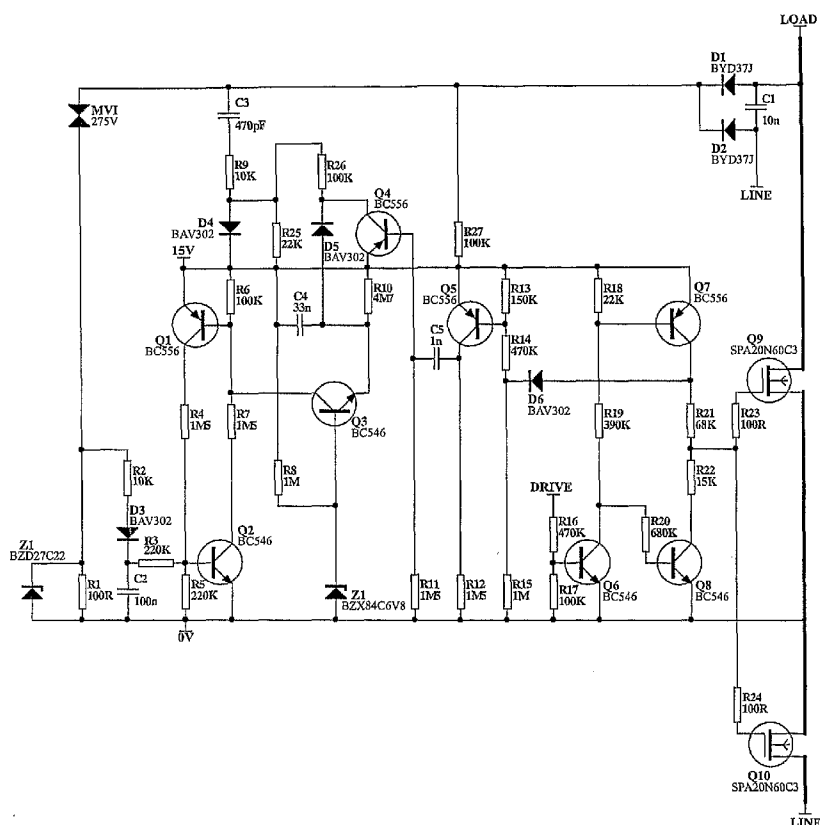
(74) Agent: **MADDERN**; Level 1, 64 Hindmarsh Square, Adelaide, S.A. 5000 (AU).

(81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, GT, HN, HR, HU, ID, IL, IN, IS, JP, KE, KG, KM, KN, KP, KR, KZ, LA, LC, LK, LR, LS, LT, LU, LV, LY, MA, MD, MG, MK, MN, MW, MX, MY, MZ, NA, NG, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RS, RU, SC, SD, SE, SG, SK, SL, SM, SV, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, ZA, ZM, ZW.

(84) Designated States (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GH, GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IS, IT, LT, LU, LV, MC, NL, PL, PT,

[Continued on next page]

(54) Title: **LOAD DETECTOR FOR A DIMMER**



(57) Abstract: Disclosed is a circuit for detecting the presence of an inductive load. The circuit uses a voltage ringing detector to detect a voltage ringing signal across the inductive load and a signal generator for generating a signal indicating the presence of the inductive load upon detection of the ringing signal. The circuit may be used in a dimmer circuit for controlling the load. The circuit may be used as a load detector for a universal dimmer or may be used in a protection circuit for circuits not designed to control inductive loads.



RO, SE, SI, SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

*For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.*

**Published:**

- *with international search report*
- *before the expiration of the time limit for amending the claims and to be republished in the event of receipt of amendments*