



ABSTRACT OF THE DISCLOSURE

ACTIVE MATRIX DISPLAY DEVICE AND TIMING GENERATOR

an active matrix display device comprises a plurality of pixels, a vertical scanning circuit, a horizontal scanning circuit and a thinning-out circuit. The plurality of pixels are arranged in a matrix on a normal standard screen. The vertical scanning circuit is for sequentially selecting pixels every line. The horizontal scanning circuit is for writing single horizontal period portions of a wide standard image signal for selected lines of pixels and the thinning-out circuit is for controlling timing of the vertical scanning circuit sequential selection and thinning-out a prescribed number of horizontal period portions from a wide standard image signal in such a manner that wide displaying compressed in the longitudinal direction of the screen is carried out. It is therefore possible for a normal standard screen to change over to displaying a wide standard image.