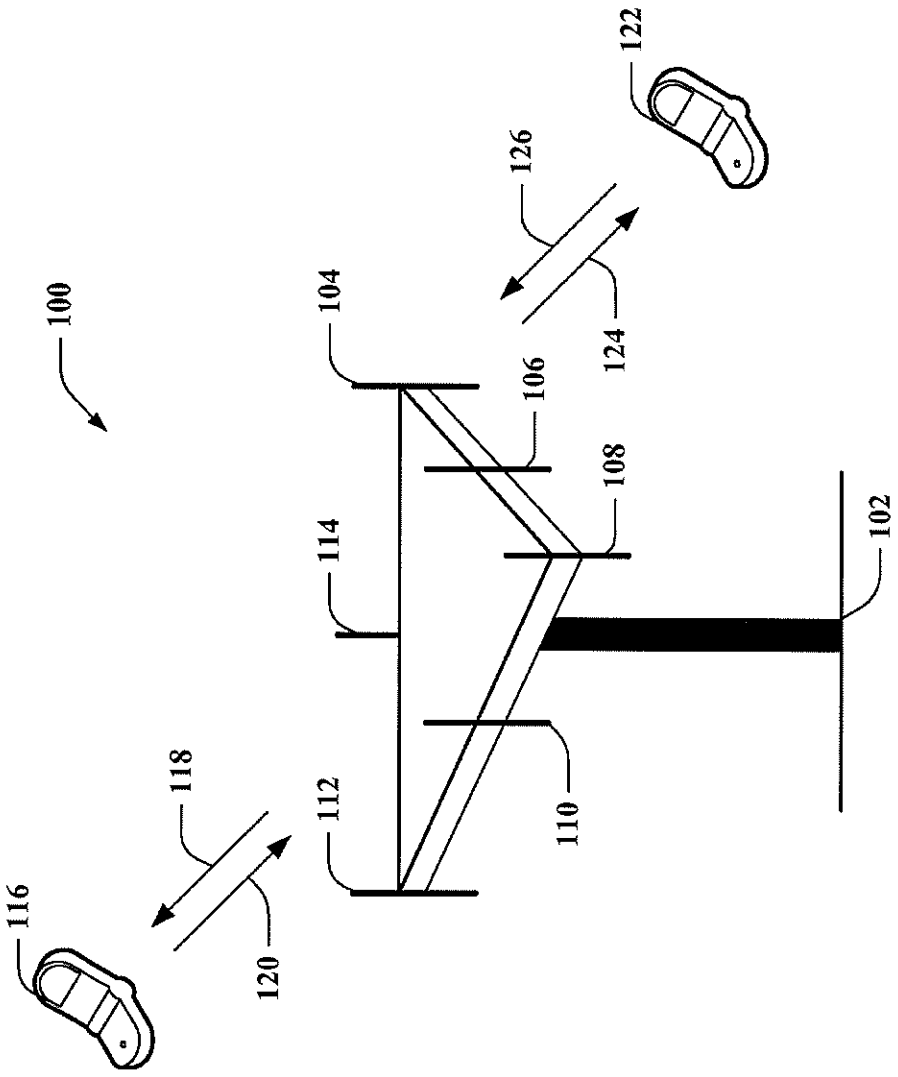


FIG. 1

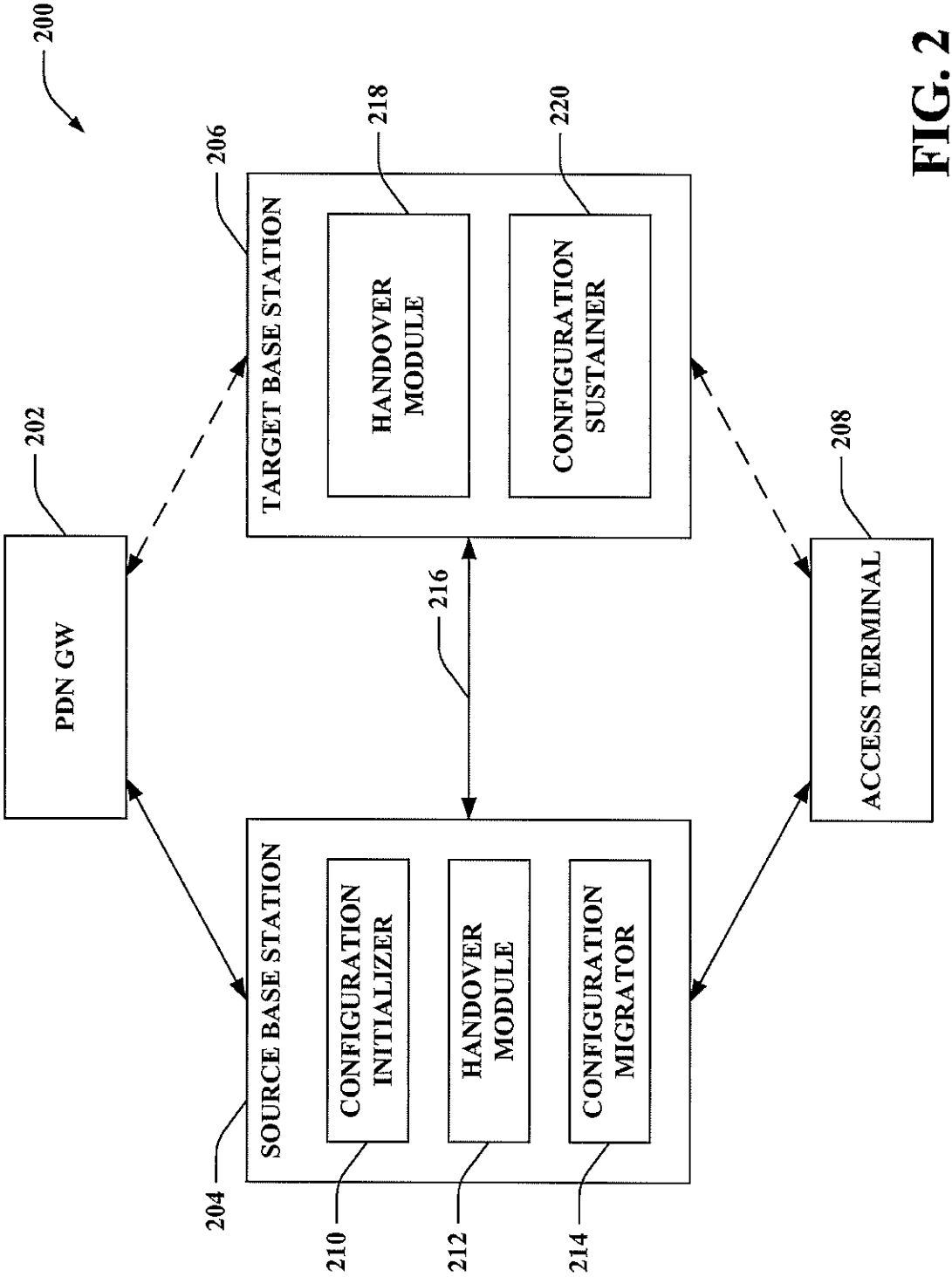

200
206
218
220
TARGET BASE STATION
HANDOVER MODULE
CONFIGURATION SUSTAINER
202
208
PDN GW
216
ACCESS TERMINAL
SOURCE BASE STATION
CONFIGURATION INITIALIZER
HANDOVER MODULE
CONFIGURATION MIGRATOR
204
210
212
214

FIG. 2

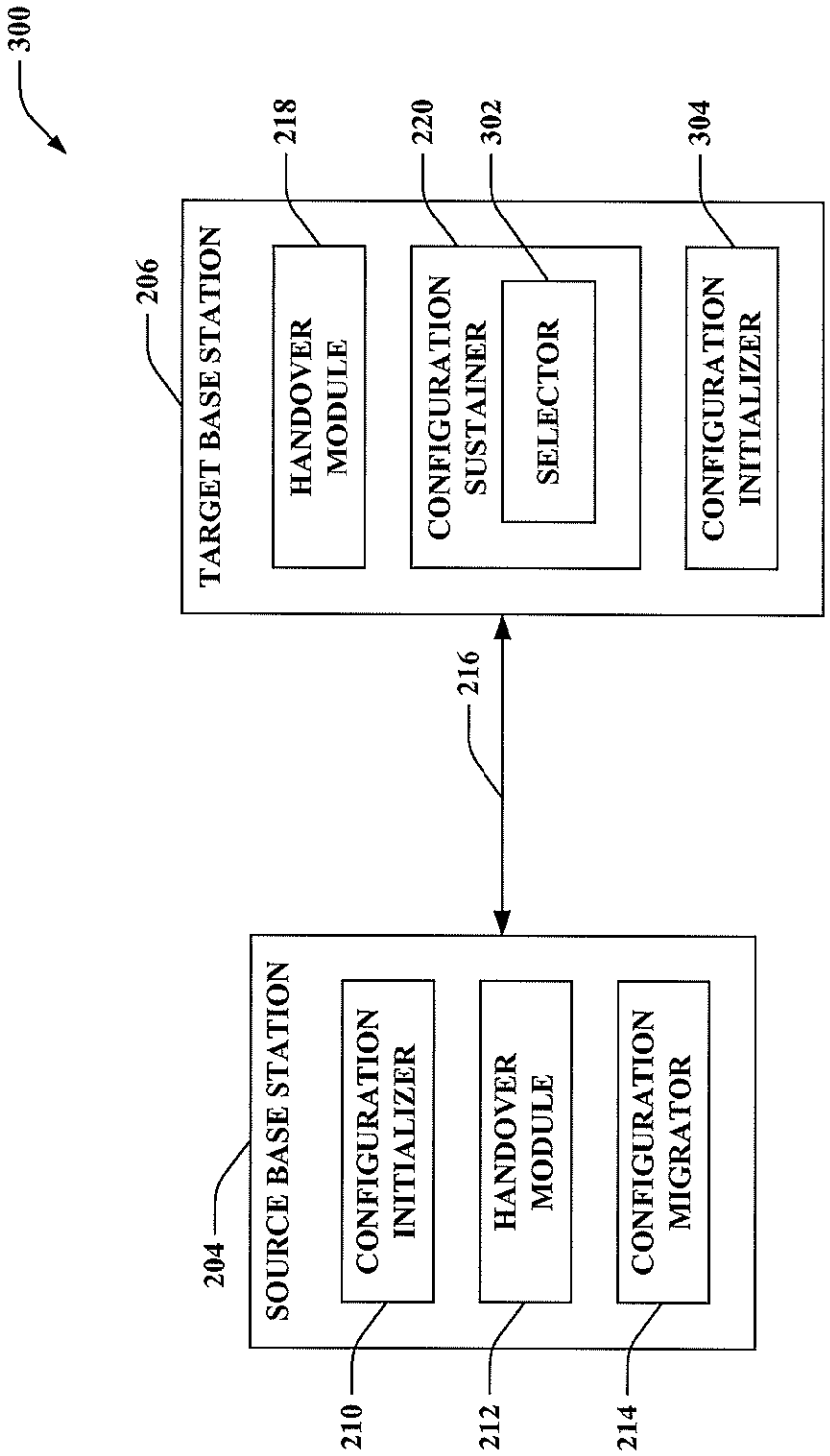

300
206
TARGET BASE STATION
218
HANDOVER MODULE
220
CONFIGURATION SUSTAINER
302
SELECTOR
304
CONFIGURATION INITIALIZER
216
204
SOURCE BASE STATION
CONFIGURATION INITIALIZER
HANDOVER MODULE
CONFIGURATION MIGRATOR
210
212
214

FIG. 3

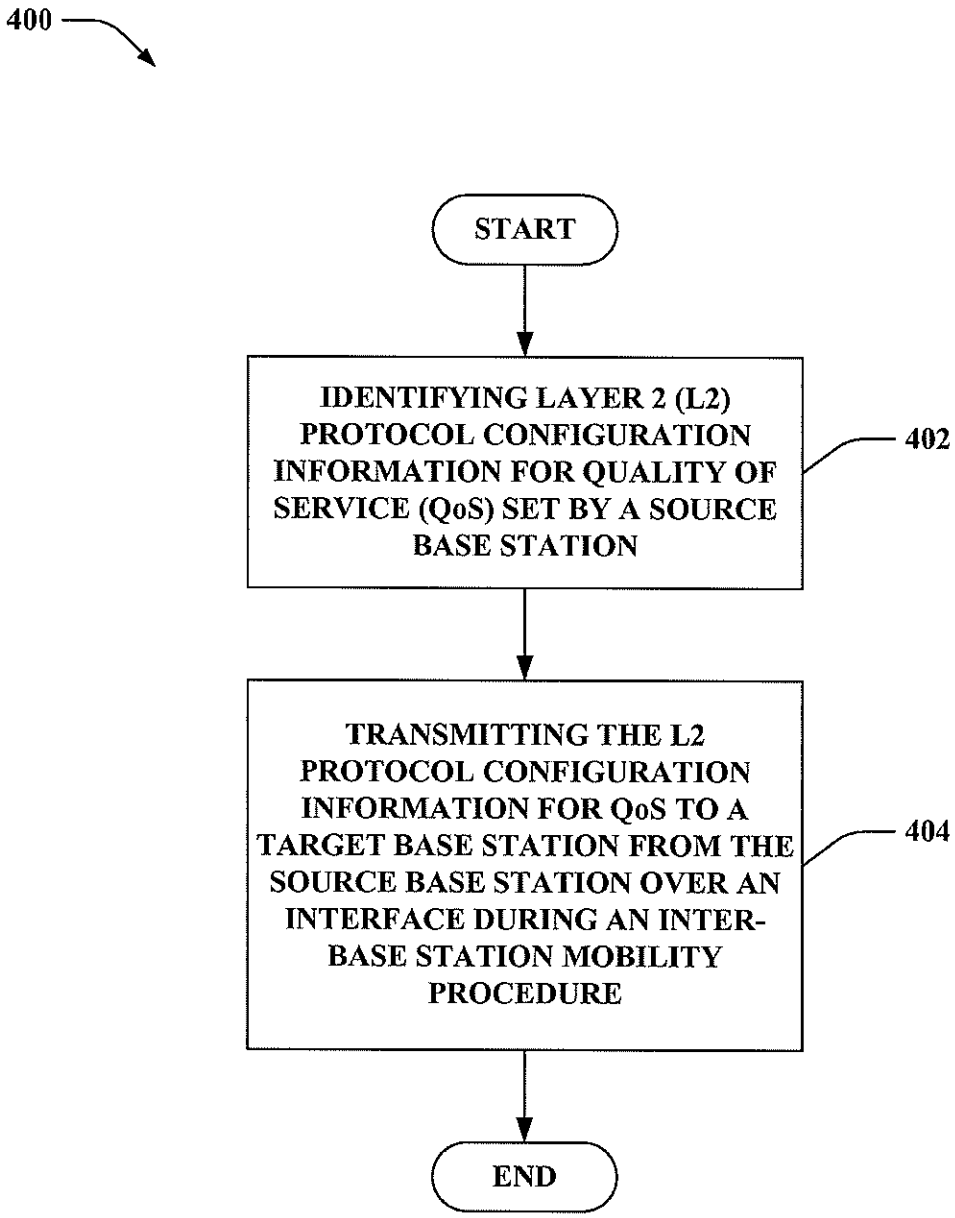

400
START
IDENTIFYING LAYER 2 (L2) PROTOCOL CONFIGURATION INFORMATION FOR QUALITY OF SERVICE (QoS) SET BY A SOURCE BASE STATION
402
TRANSMITTING THE L2 PROTOCOL CONFIGURATION INFORMATION FOR QoS TO A TARGET BASE STATION FROM THE SOURCE BASE STATION OVER AN INTERFACE DURING AN INTER-BASE STATION MOBILITY PROCEDURE
404
END

FIG. 4

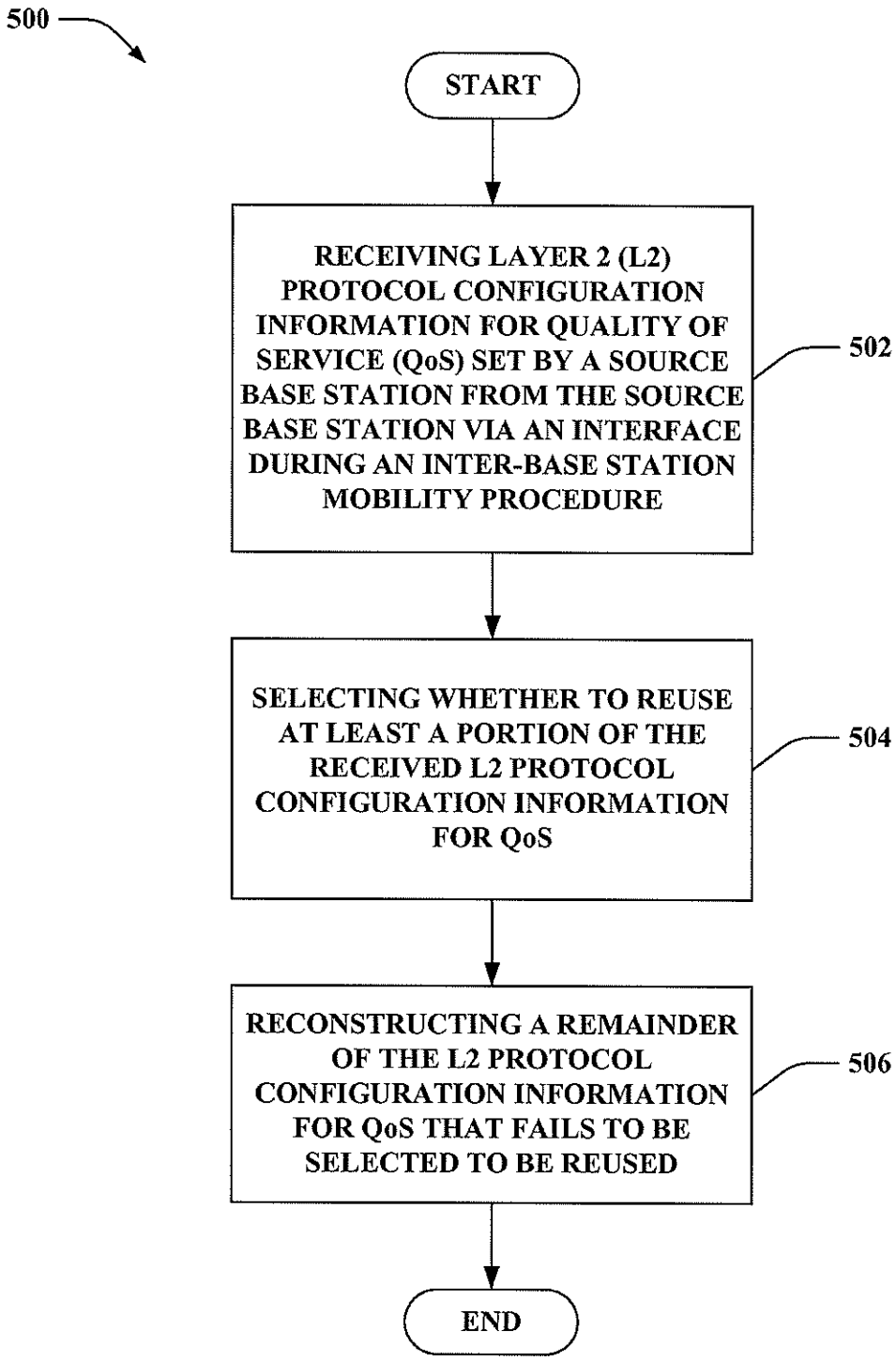

500
START
RECEIVING LAYER 2 (L2) PROTOCOL CONFIGURATION INFORMATION FOR QUALITY OF SERVICE (QoS) SET BY A SOURCE BASE STATION FROM THE SOURCE BASE STATION VIA AN INTERFACE DURING AN INTER-BASE STATION MOBILITY PROCEDURE
502
SELECTING WHETHER TO REUSE AT LEAST A PORTION OF THE RECEIVED L2 PROTOCOL CONFIGURATION INFORMATION FOR QoS
504
RECONSTRUCTING A REMAINDER OF THE L2 PROTOCOL CONFIGURATION INFORMATION FOR QoS THAT FAILS TO BE SELECTED TO BE REUSED
506
END

FIG. 5

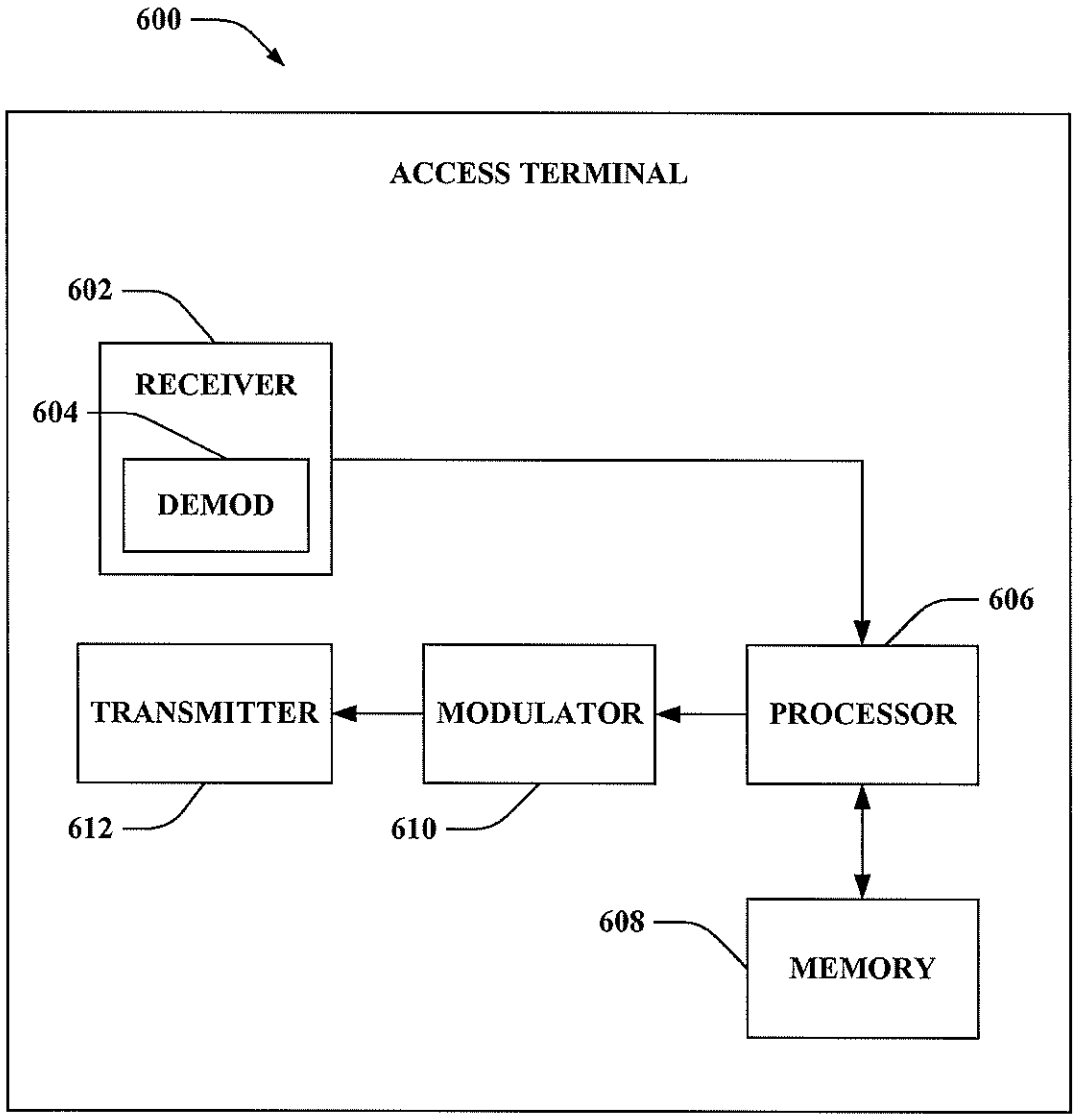

600
ACCESS TERMINAL
602
RECEIVER
604
DEMOD
606
TRANSMITTER
MODULATOR
PROCESSOR
612
610
608
MEMORY

FIG. 6

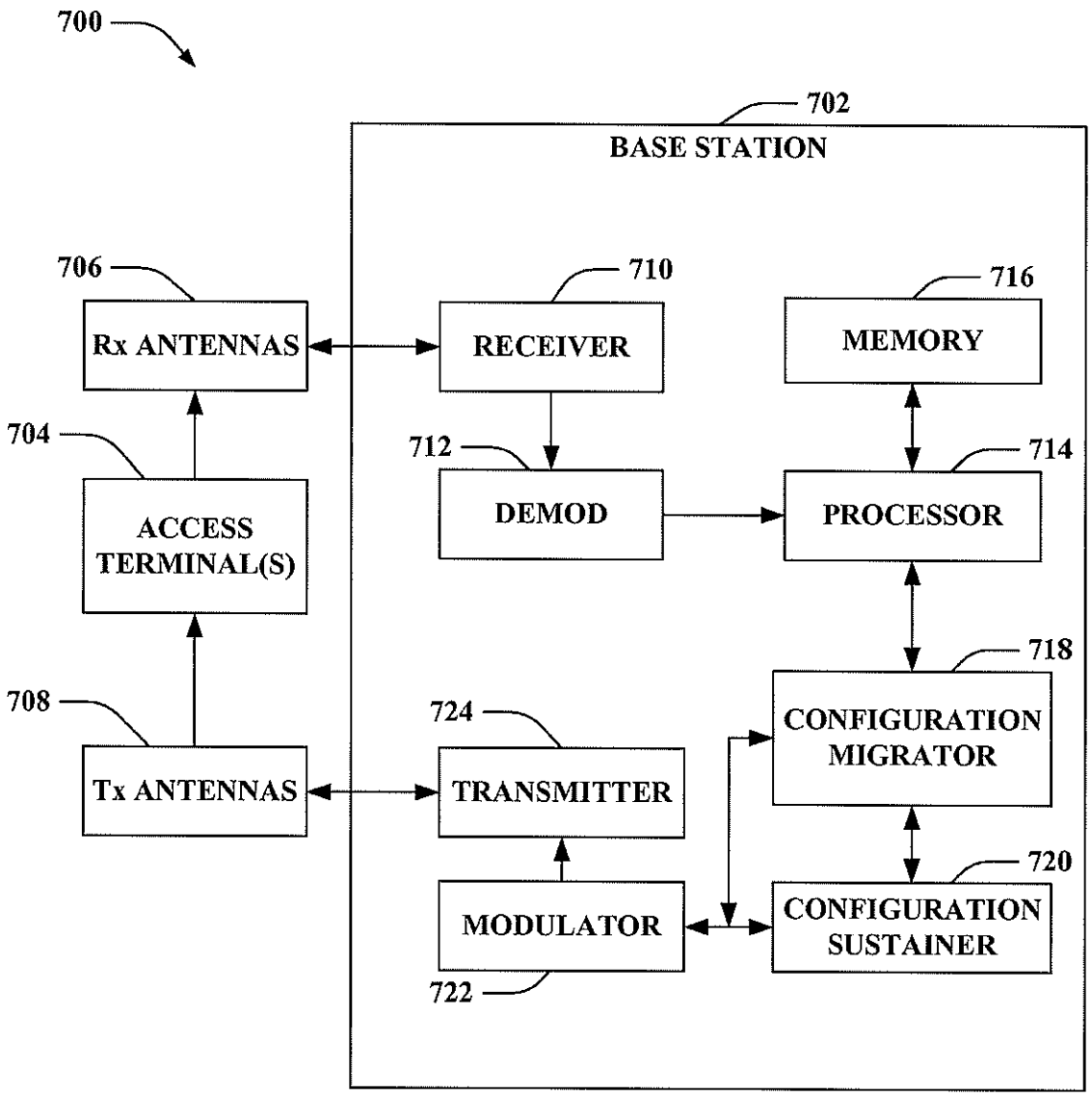

700
702
BASE STATION
706
Rx ANTENNAS
710
RECEIVER
716
MEMORY
704
ACCESS TERMINAL(S)
712
DEMOD
714
PROCESSOR
718
CONFIGURATION MIGRATOR
708
Tx ANTENNAS
724
TRANSMITTER
720
CONFIGURATION SUSTAINER
MODULATOR
722

FIG. 7

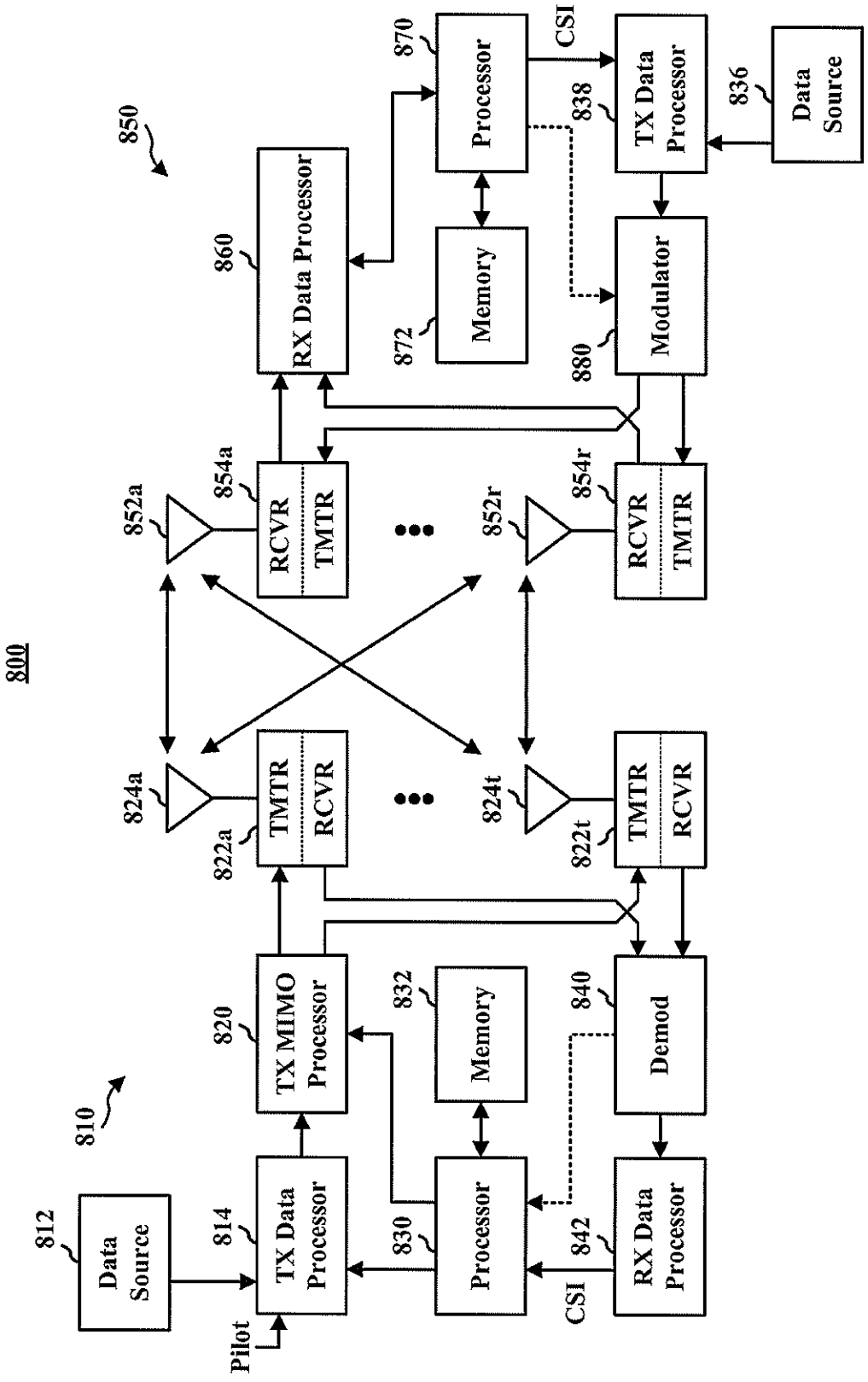

800
810
812
Data Source
814
TX Data Processor
Pilot
820
TX MIMO Processor
822a
TMTR
RCVR
824a
824t
822t
TMTR
RCVR
830
Processor
832
Memory
840
Demod
842
RX Data Processor
CSI
850
852a
854a
RCVR
TMTR
852r
854r
RCVR
TMTR
860
RX Data Processor
870
Processor
872
Memory
CSI
838
TX Data Processor
880
Modulator
836
Data Source

FIG. 8

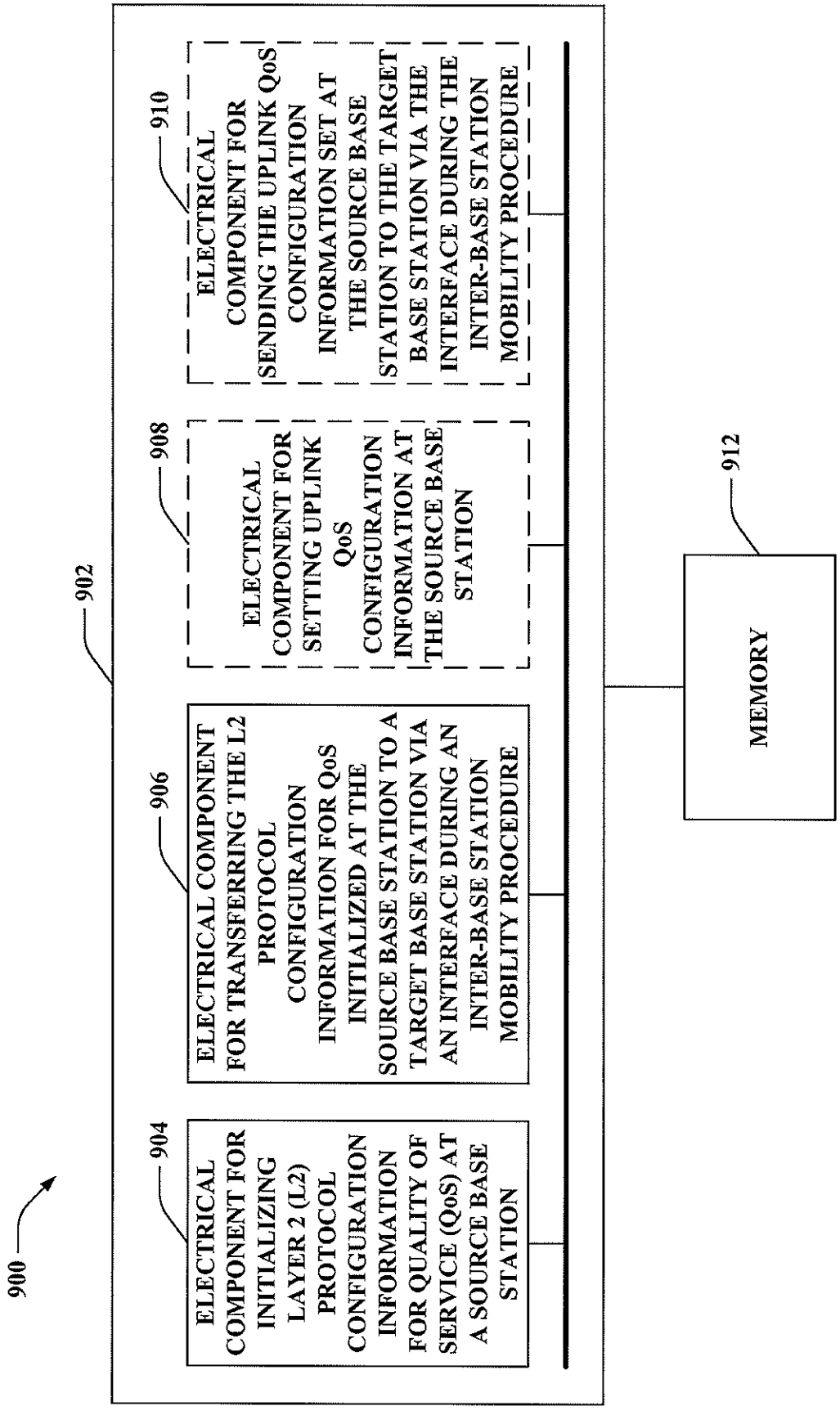

900
902
904
ELECTRICAL COMPONENT FOR INITIALIZING LAYER 2 (L2) PROTOCOL CONFIGURATION INFORMATION FOR QUALITY OF SERVICE (QoS) AT A SOURCE BASE STATION
906
ELECTRICAL COMPONENT FOR TRANSFERRING THE L2 PROTOCOL CONFIGURATION INFORMATION FOR QoS INITIALIZED AT THE SOURCE BASE STATION TO A TARGET BASE STATION VIA AN INTERFACE DURING AN INTER-BASE STATION MOBILITY PROCEDURE
908
ELECTRICAL COMPONENT FOR SETTING UPLINK QoS CONFIGURATION INFORMATION AT THE SOURCE BASE STATION
910
ELECTRICAL COMPONENT FOR SENDING THE UPLINK QoS CONFIGURATION INFORMATION SET AT THE SOURCE BASE STATION TO THE TARGET BASE STATION VIA THE INTERFACE DURING THE INTER-BASE STATION MOBILITY PROCEDURE
912
MEMORY

FIG. 9

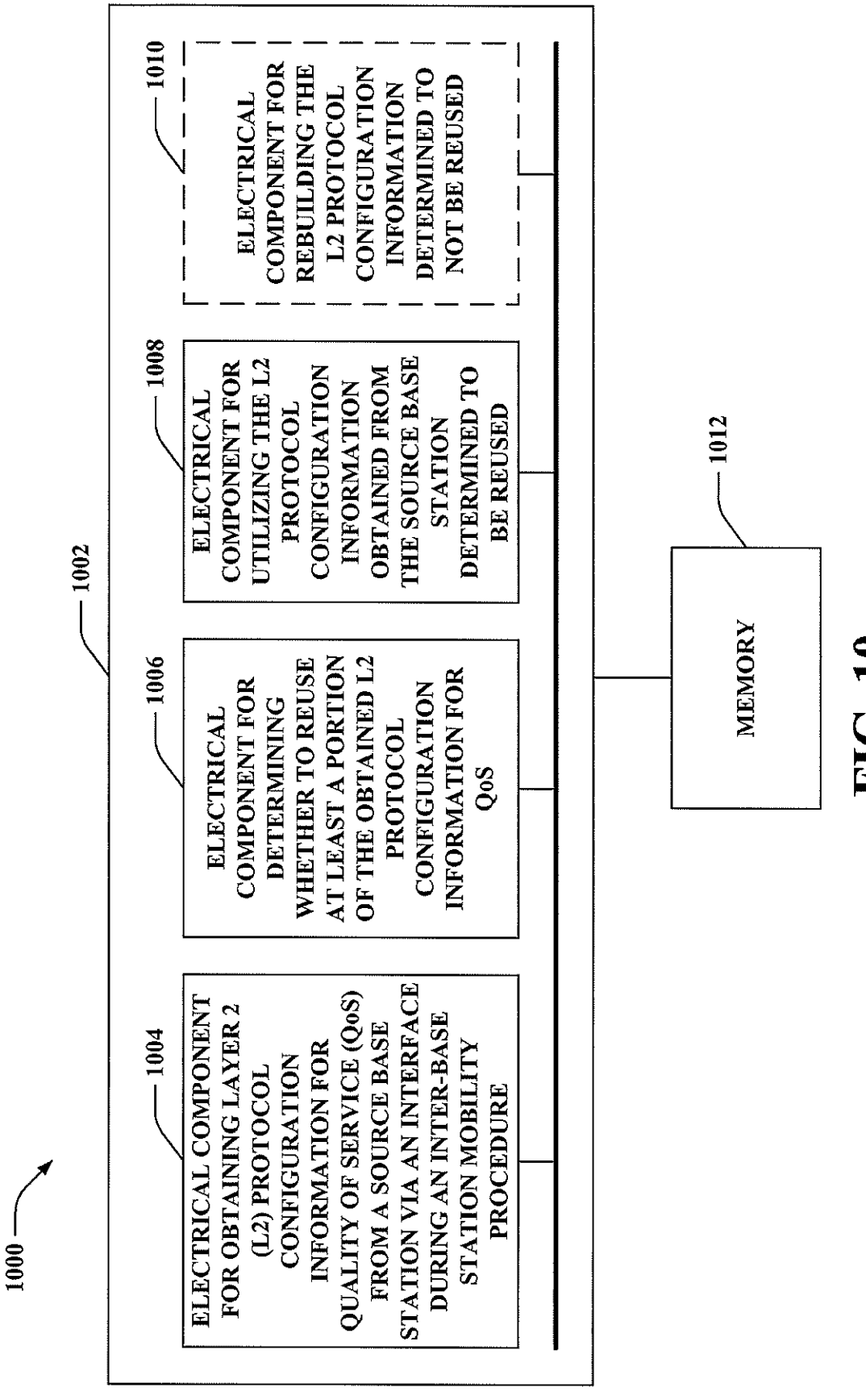

1000
1002
1004
ELECTRICAL COMPONENT FOR OBTAINING LAYER 2 (L2) PROTOCOL CONFIGURATION INFORMATION FOR QUALITY OF SERVICE (QoS) FROM A SOURCE BASE STATION VIA AN INTERFACE DURING AN INTER-BASE STATION MOBILITY PROCEDURE
1006
ELECTRICAL COMPONENT FOR DETERMINING WHETHER TO REUSE AT LEAST A PORTION OF THE OBTAINED L2 PROTOCOL CONFIGURATION INFORMATION FOR QoS
1008
ELECTRICAL COMPONENT FOR UTILIZING THE L2 PROTOCOL CONFIGURATION INFORMATION OBTAINED FROM THE SOURCE BASE STATION DETERMINED TO BE REUSED
1010
ELECTRICAL COMPONENT FOR REBUILDING THE L2 PROTOCOL CONFIGURATION INFORMATION DETERMINED TO NOT BE REUSED
1012
MEMORY

FIG. 10