

FIG. 1

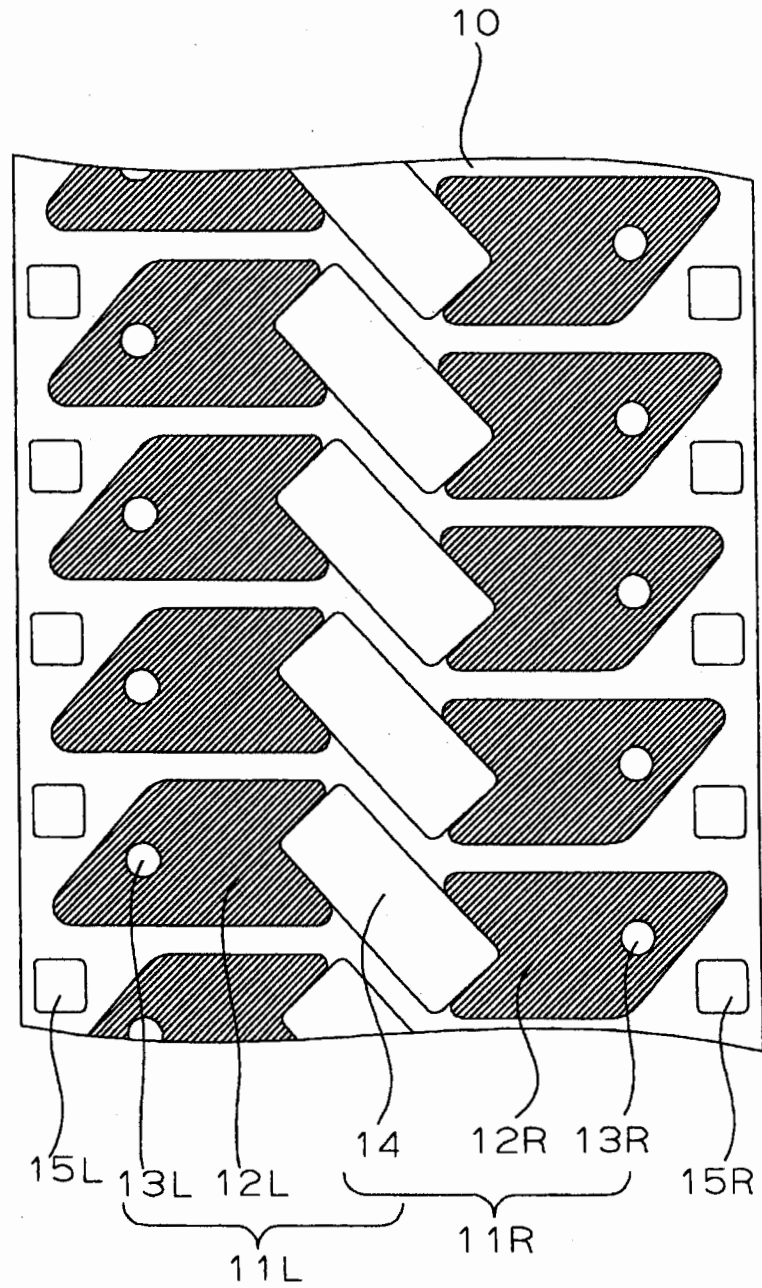


FIG. 2A

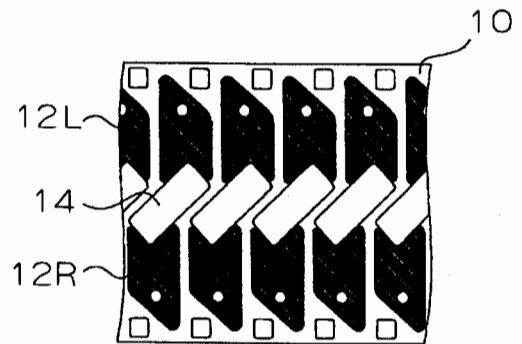
CIRCUIT
SUBSTRATE

FIG. 2B

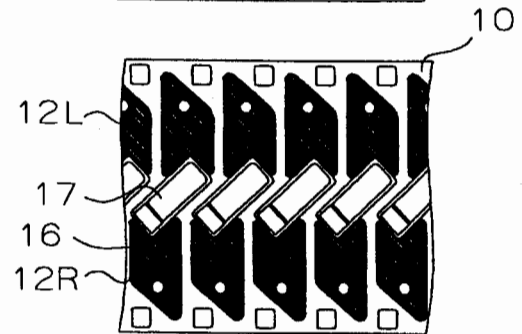
ONE SIDE
MOUNTING

FIG. 2C

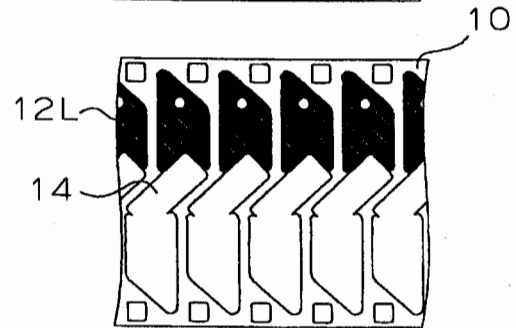
ONE SIDE
BLANK-THROUGH

FIG. 2D

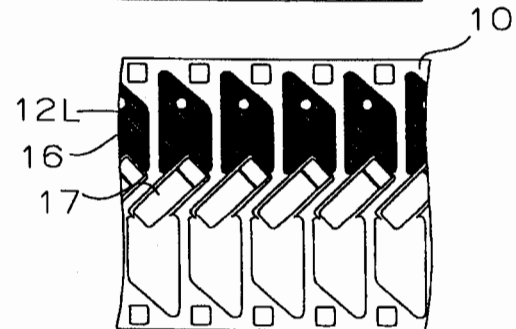
REMAINING ONE
SIDE MOUNTING

FIG. 2E

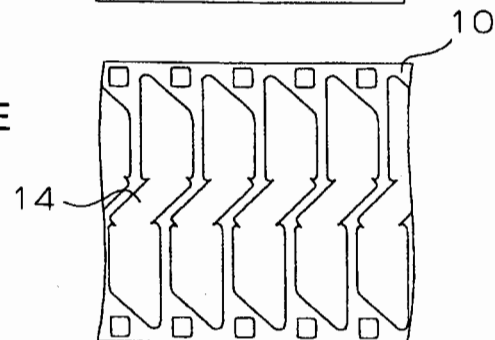
REMAINING ONE SIDE
BLANK-THROUGH

FIG. 3

