

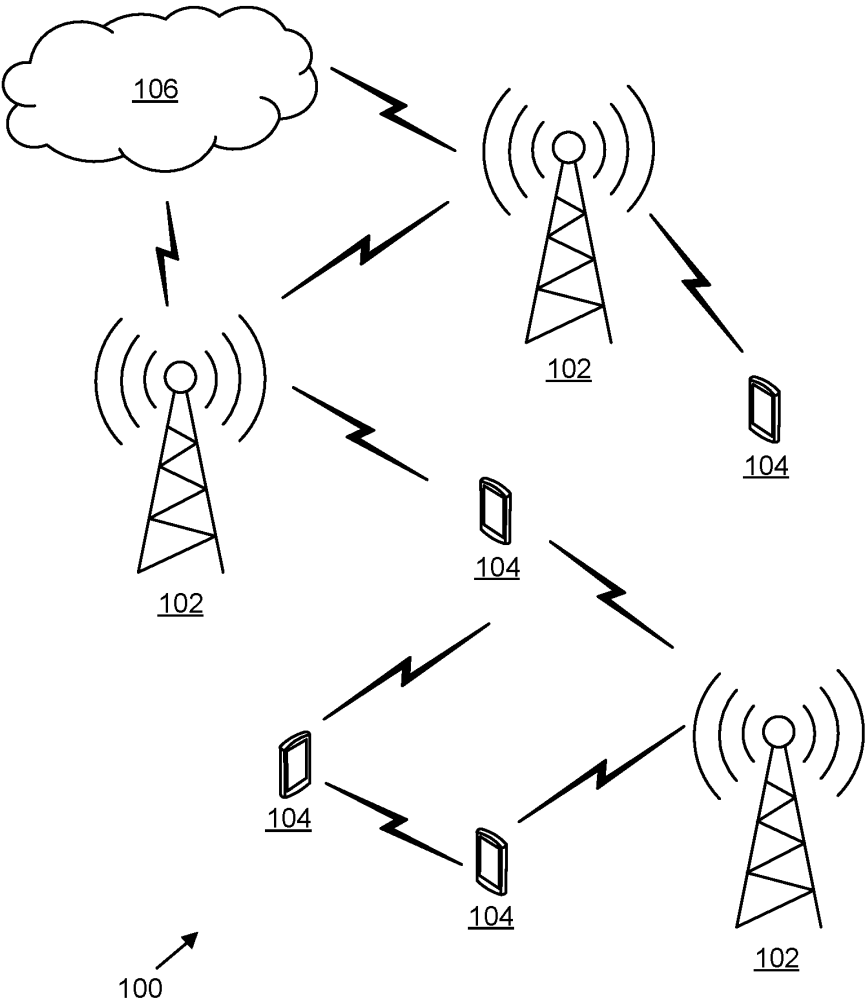


FIG. 1

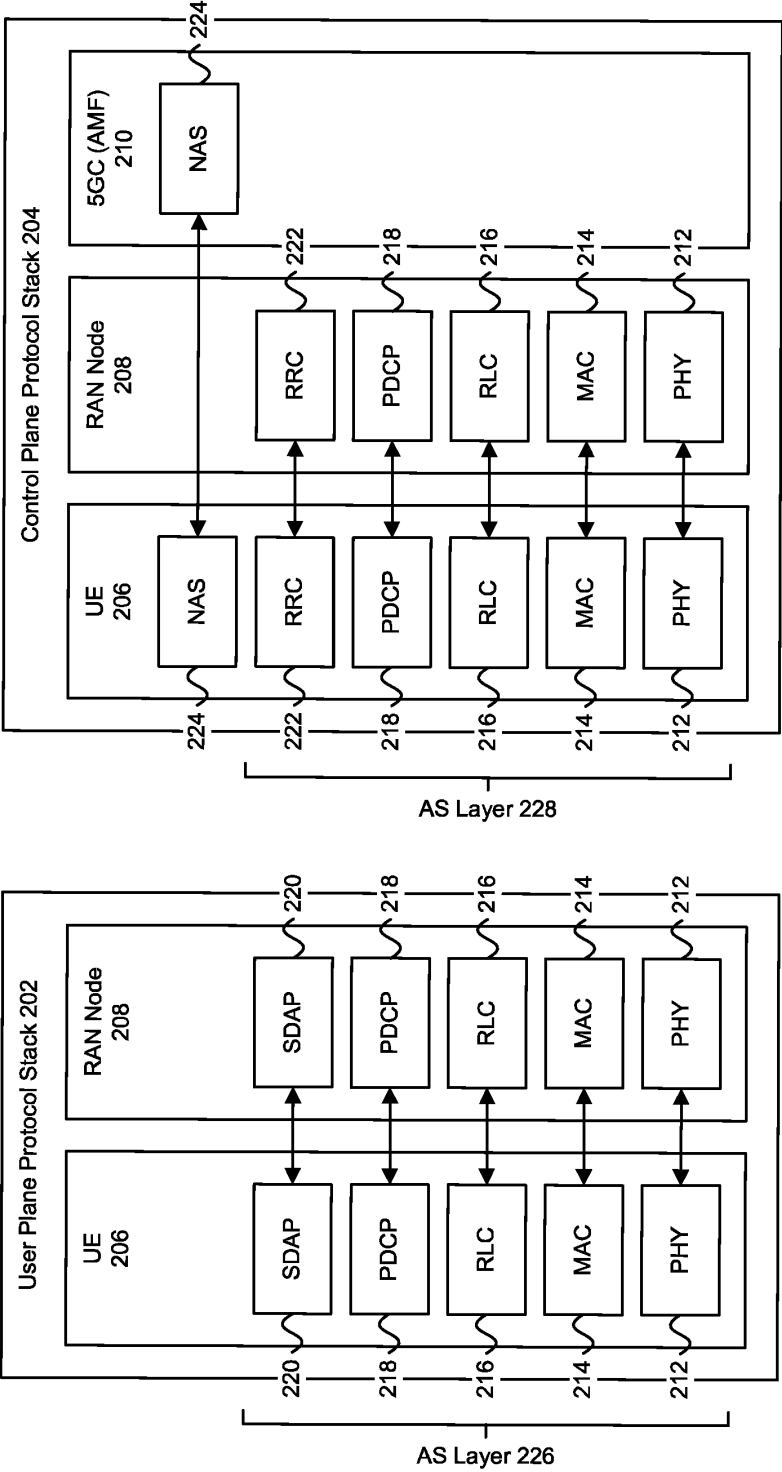


FIG. 2

```
-- ASN1START
-- TAG-DRX-CONFIG-START
DRX-Config ::=
    drx-onDurationTimer
        SEQUENCE {
            CHOICE {
                subMilliSeconds INTEGER (1..31),
                milliSeconds ENUMERATED {
                    ms1, ms2, ms3, ms4, ms5, ms6, ms8, ms10, ms20, ms30, ms40, ms50, ms60,
                    ms80, ms100, ms200, ms300, ms400, ms500, ms600, ms800, ms1000, ms1200,
                    ms1600, spare8, spare7, spare6, spare5, spare4, spare3, spare2, spare1 }
            }
        }
    drx-InactivityTimer
        ENUMERATED {
            ms0, ms1, ms2, ms3, ms4, ms5, ms6, ms8, ms10, ms20, ms30, ms40, ms50, ms60, ms80,
            ms100, ms200, ms300, ms500, ms750, ms1280, ms1920, ms2560, spare9, spare8, spare7,
            spare6, spare5, spare4, spare3, spare2, spare1},
            INTEGER (0..56),
            INTEGER (0..56),
            ENUMERATED {
                sl0, sl1, sl2, sl4, sl6, sl8, sl16, sl24, sl33, sl40, sl64, sl80, sl96, sl112,
                sl128, sl160, sl320, spare15, spare14, spare13, spare12, spare11, spare10, spare9,
                spare8, spare7, spare6, spare5, spare4, spare3, spare2, spare1},
            ENUMERATED {
                sl0, sl1, sl2, sl4, sl6, sl8, sl16, sl24, sl33, sl40, sl64, sl80, sl96, sl112,
                sl128, sl160, sl320, spare15, spare14, spare13, spare12, spare11, spare10, spare9,
                spare8, spare7, spare6, spare5, spare4, spare3, spare2, spare1 },
            ...
        }
```

FIG. 3A

```
... drx-LongCycleStartOffset CHOICE {
    ms10 INTEGER(0..9),
    ms20 INTEGER(0..19),
    ms32 INTEGER(0..31),
    ms40 INTEGER(0..39),
    ms60 INTEGER(0..59),
    ms64 INTEGER(0..63),
    ms70 INTEGER(0..69),
    ms80 INTEGER(0..79),
    ms128 INTEGER(0..127),
    ms160 INTEGER(0..159),
    ms256 INTEGER(0..255),
    ms320 INTEGER(0..319),
    ms512 INTEGER(0..511),
    ms640 INTEGER(0..639),
    ms1024 INTEGER(0..1023),
    ms1280 INTEGER(0..1279),
    ms2048 INTEGER(0..2047),
    ms2560 INTEGER(0..2559),
    ms5120 INTEGER(0..5119),
    ms10240 INTEGER(0..10239)
}
shortDRX
  drx-ShortCycle
    ENUMERATED {
      ms2, ms3, ms4, ms5, ms6, ms7, ms8, ms10, ms14, ms16, ms20, ms30, ms32,
      ms35, ms40, ms64, ms80, ms128, ms160, ms256, ms320, ms512, ms640, spare9,
      spare8, spare7, spare6, spare5, spare4, spare3, spare2, spare1 },
      INTEGER(1..16)
    }
    drx-ShortCycleTimer
      OPTIONAL, -- Need R
    }
    drx-SlotOffset
      INTEGER(0..31)
    }
  }
DRX-ConfigExt-v1700 ::=
  drx-HARQ-RTT-TimerDL-r17
  drx-HARQ-RTT-TimerUL-r17
  INTEGER(0..448)
  INTEGER(0..448)
}
-- TAG-DRX-CONFIG-STOP
-- ASN1STOP
```

FIG. 3B

```
-- ASN1START
-- TAG-CELLDTXDRX-CONFIG-START
CellDTXDRX-Config-r18 ::=
cellDTXDRX-onDurationTimer-r18
SEQUENCE {
    CHOICE {
        subMilliSeconds INTEGER (1..31),
        milliSeconds ENUMERATED {
            ms1, ms2, ms3, ms4, ms5, ms6, ms8, ms10, ms20, ms30, ms40, ms50, ms60,
            ms80, ms100, ms200, ms300, ms400, ms500, ms600, ms800, ms1000, ms1200,
            ms1600, spare8, spare7, spare6, spare5, spare4, spare3, spare2, spare1 }
        }
    CHOICE {
        INTEGER (0..9),
        INTEGER (0..19),
        INTEGER (0..31),
        INTEGER (0..39),
        INTEGER (0..59),
        INTEGER (0..63),
        INTEGER (0..69),
        INTEGER (0..79),
        INTEGER (0..127),
        INTEGER (0..159),
        INTEGER (0..255),
        INTEGER (0..319),
        INTEGER (0..511),
        INTEGER (0..639),
        INTEGER (0..1023),
        INTEGER (0..1279),
        INTEGER (0..2047),
        INTEGER (0..2559),
        INTEGER (0..5119),
        INTEGER (0..10239)
    }
}
cellDTXDRX-CycleStartOffset-r18
ms10
ms20
ms32
ms40
ms60
ms64
ms70
ms80
ms128
ms160
ms256
ms320
ms512
ms640
ms1024
ms1280
ms2048
ms2560
ms5120
ms10240
}
cellDTXDRX-SlotOffset-r18
cellDTXDRX-configType-r18
cellDTXDRXactivationStatus-r18
cellTurnOff-r18
}
-- TAG-CELLDTXDRX-CONFIG-STOP
-- ASN1STOP
```

OPTIONAL, -- Need M
OPTIONAL, -- Need M

INTEGER (0..31)
ENUMERATED {dtx, drx, dtxdrx},
ENUMERATED {activated, deactivated}
ENUMERATED {true},

FIG. 4

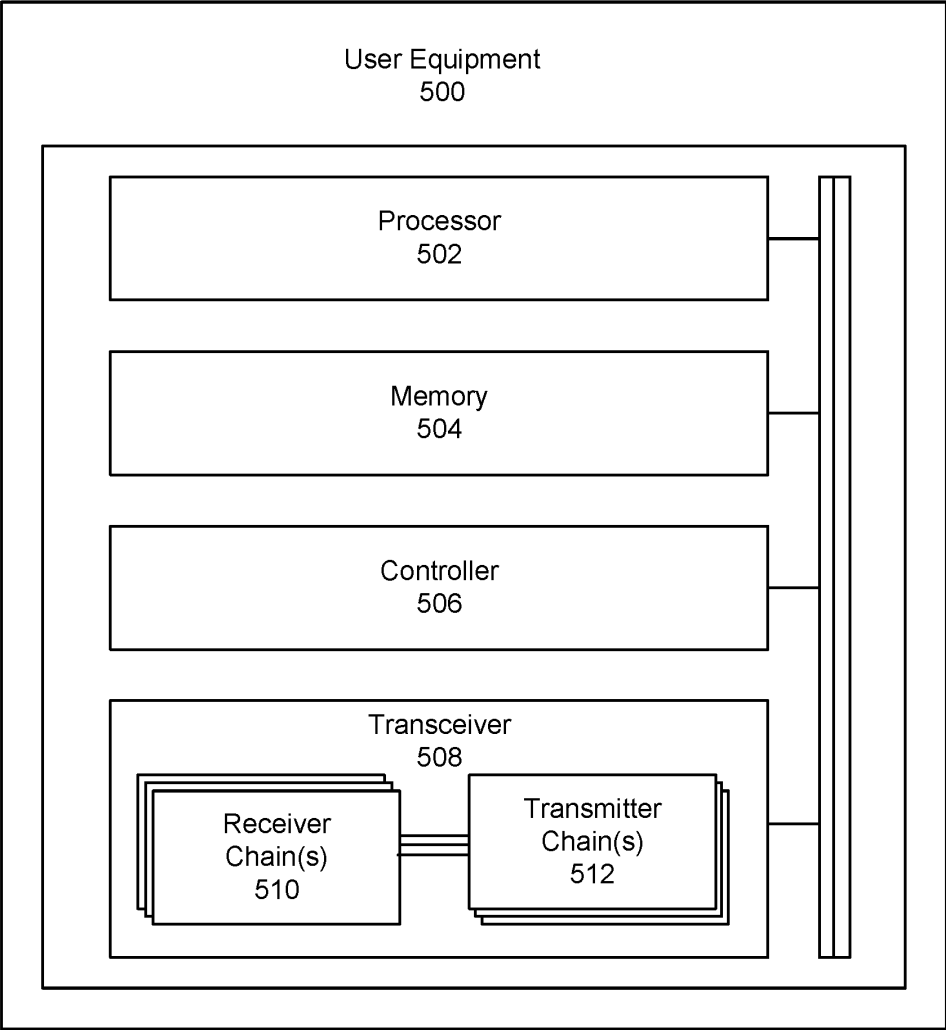


FIG. 5

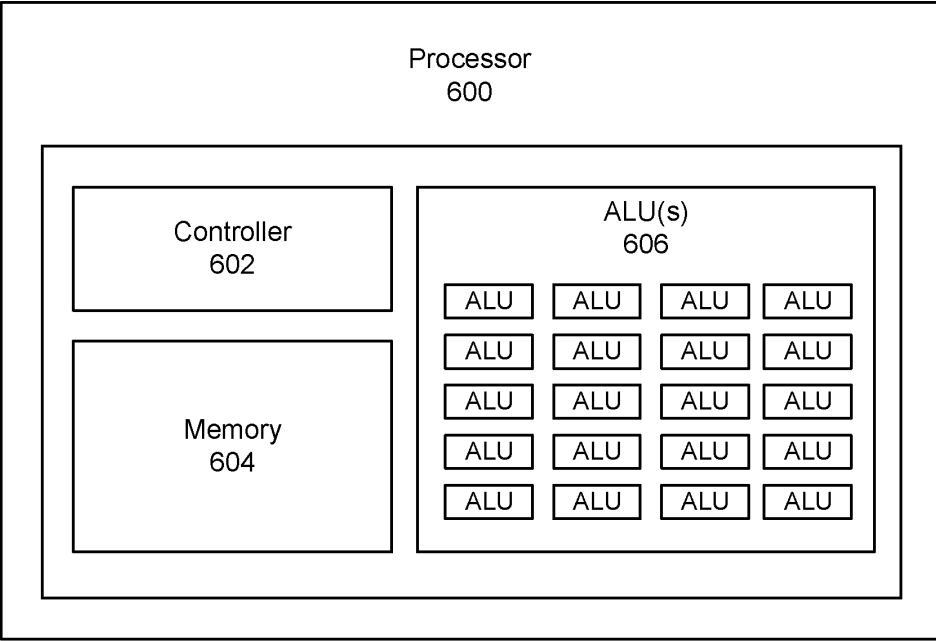


FIG. 6

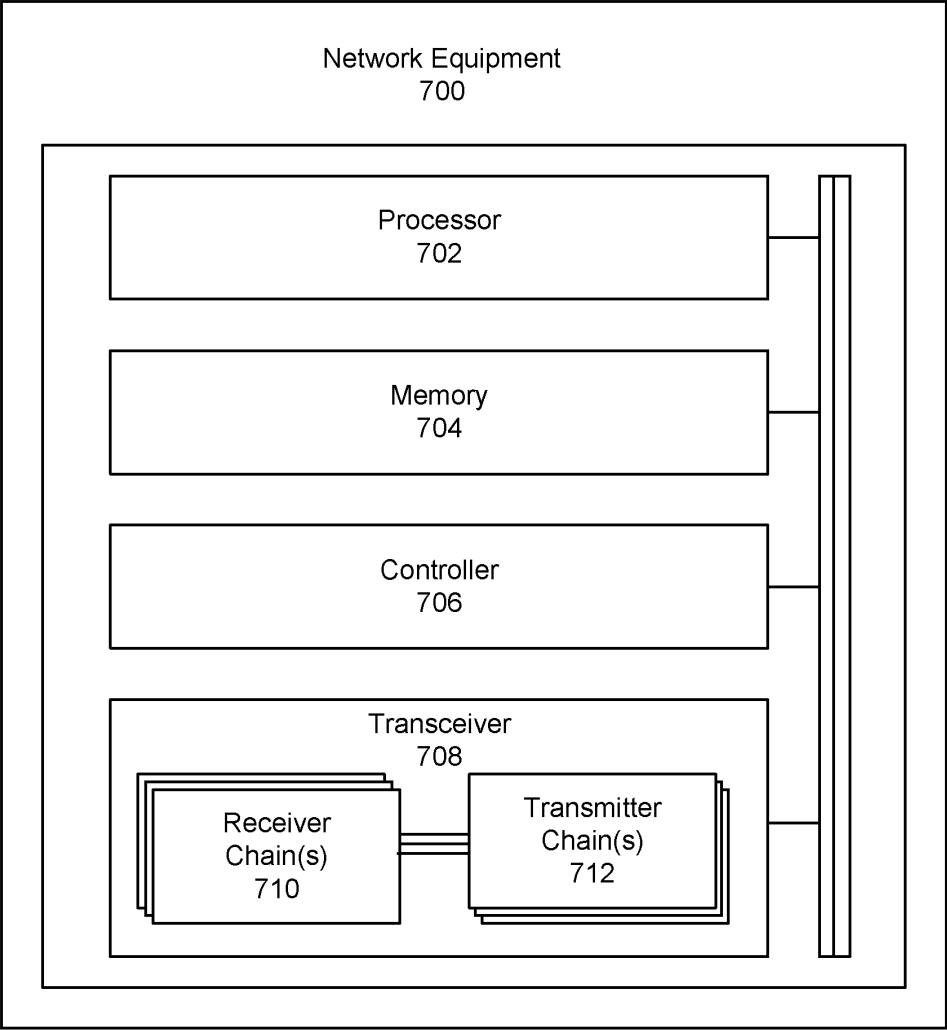


FIG. 7

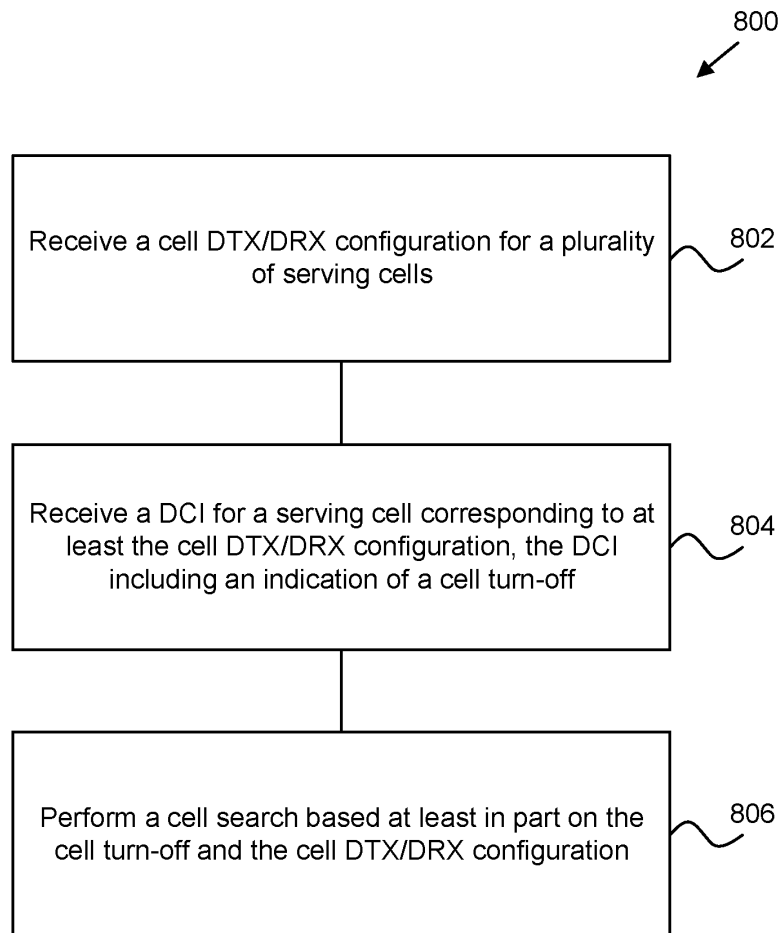


FIG. 8

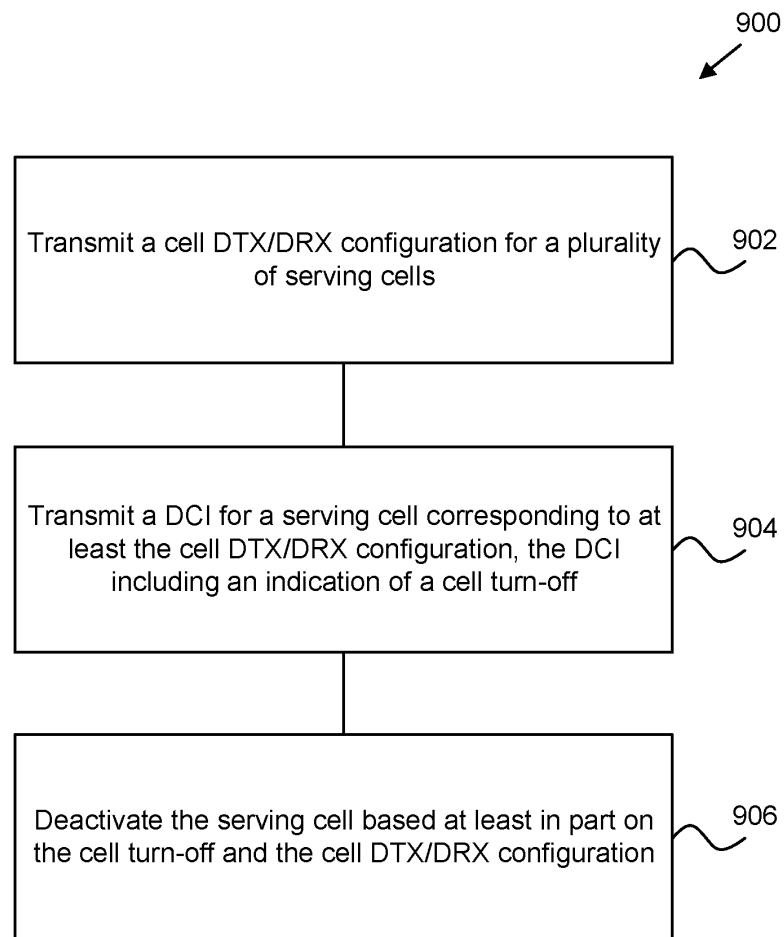


FIG. 9