

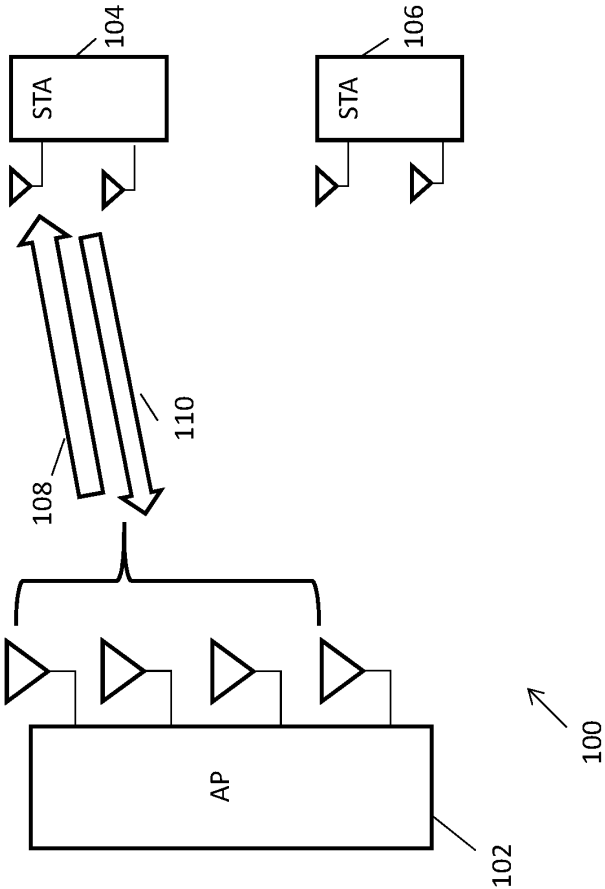


Fig. 1A

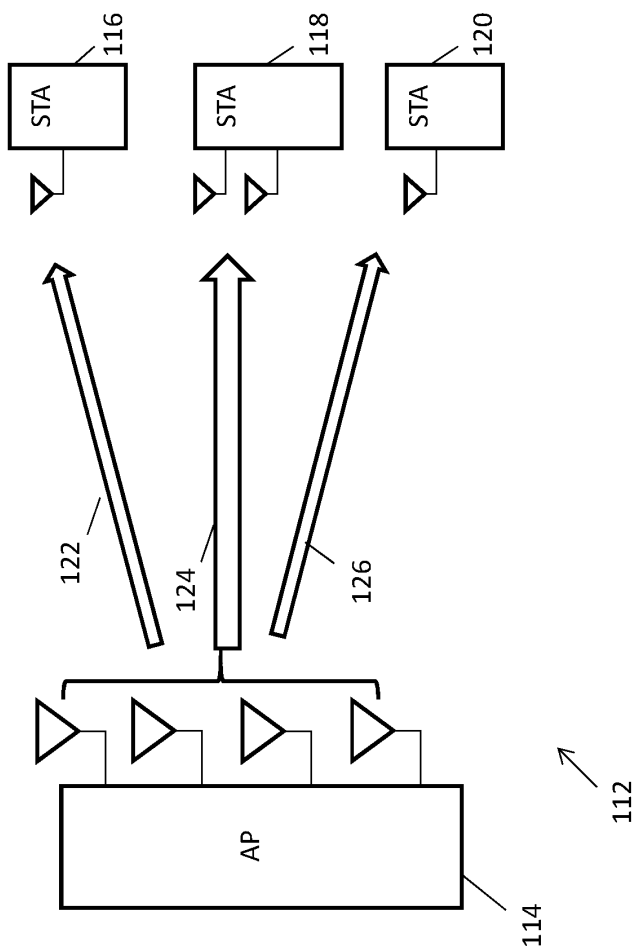


Fig. 1B

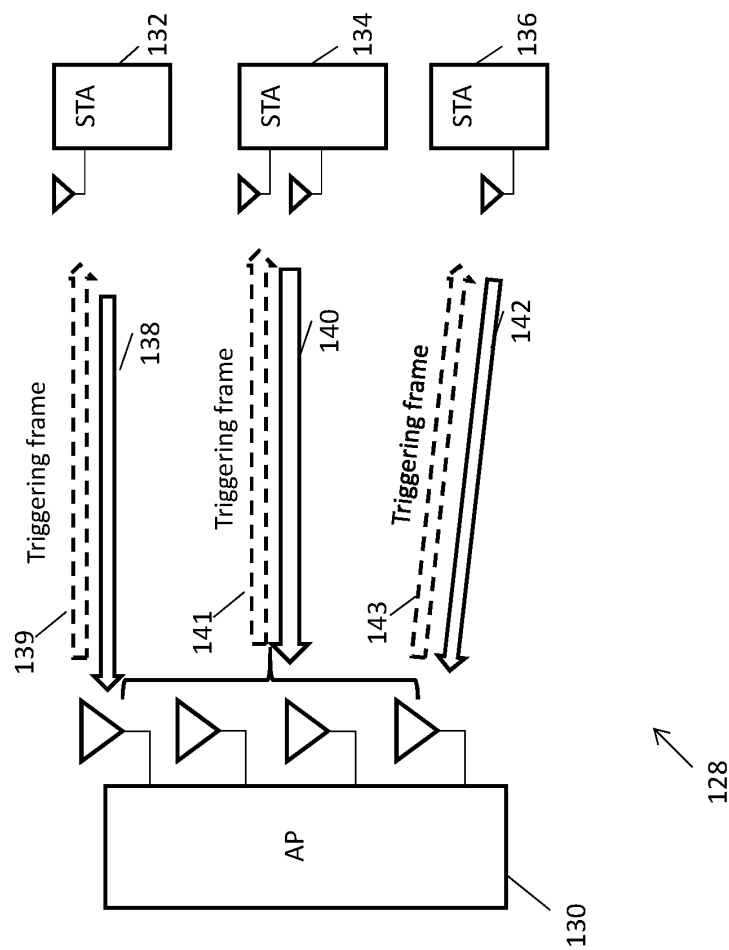


Fig. 1C

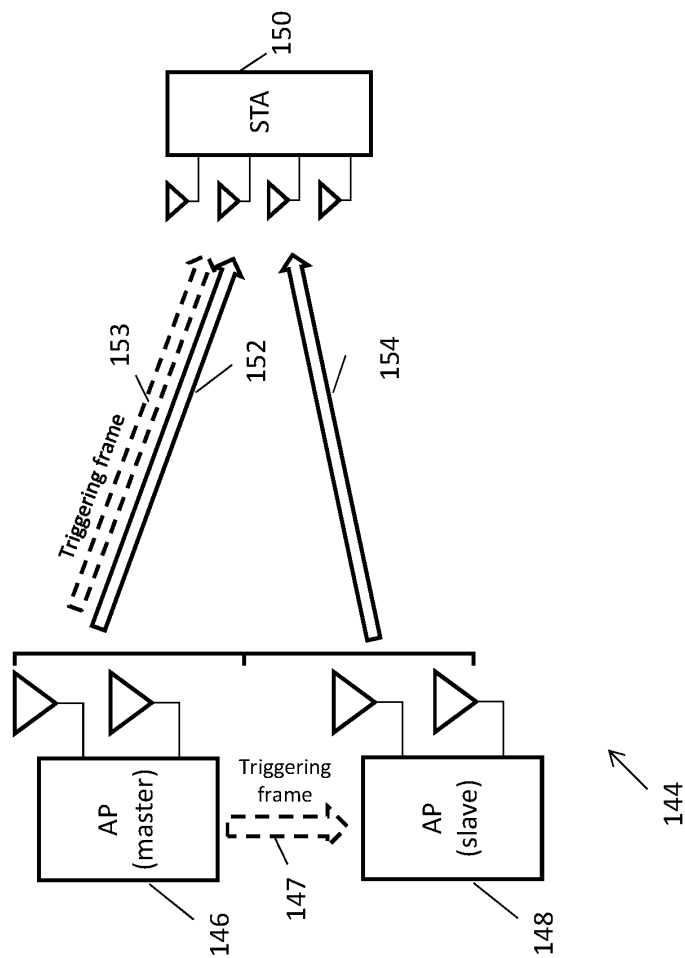


Fig. 1D

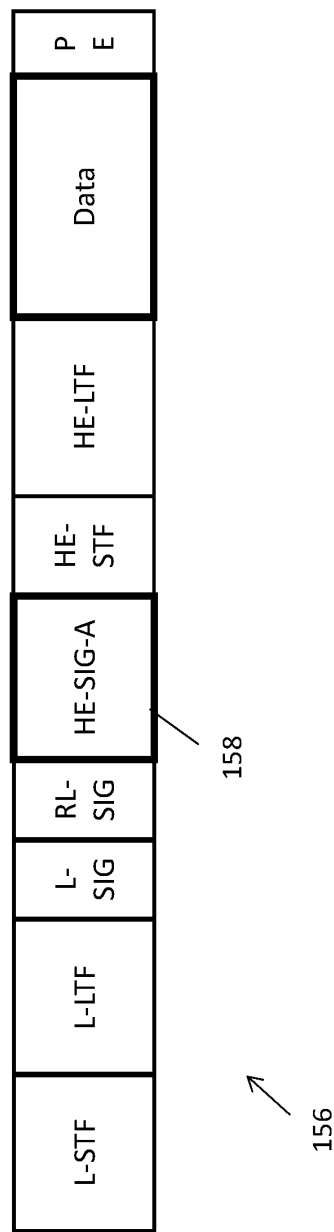


Fig. 2A

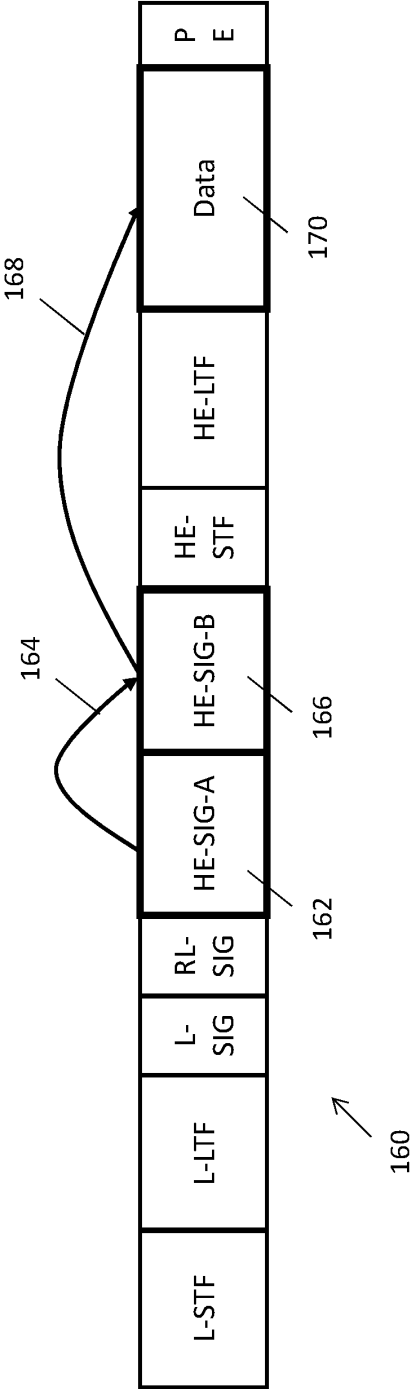


Fig. 2B

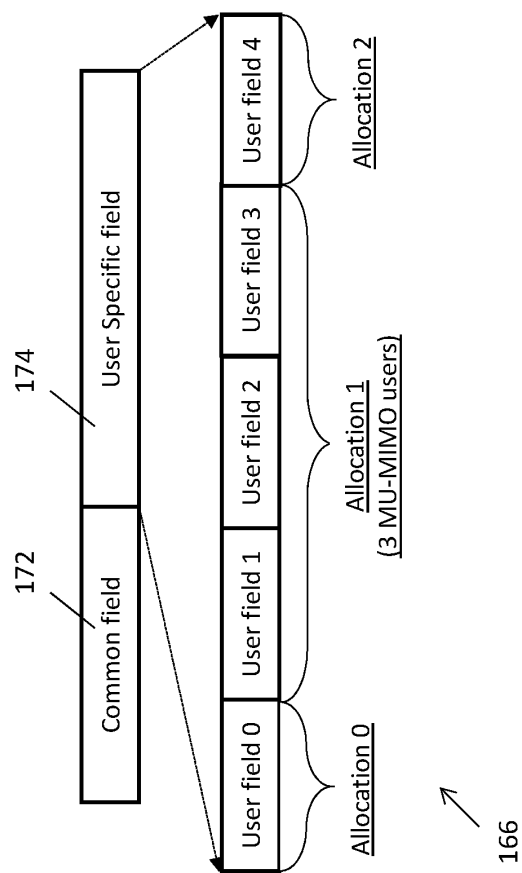


Fig. 2C

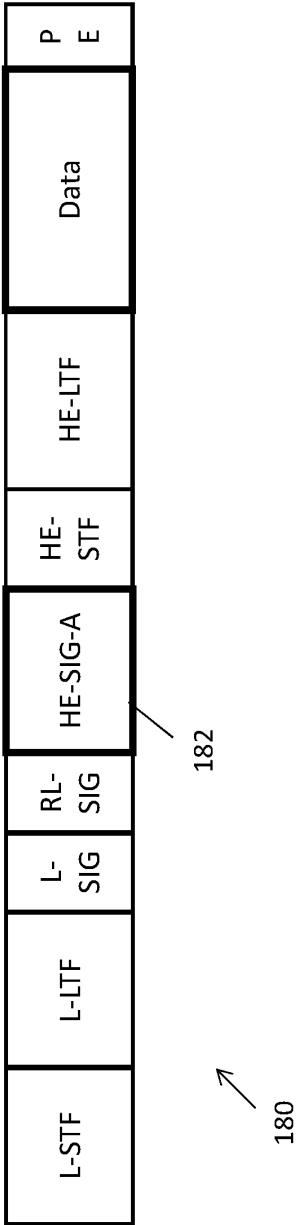
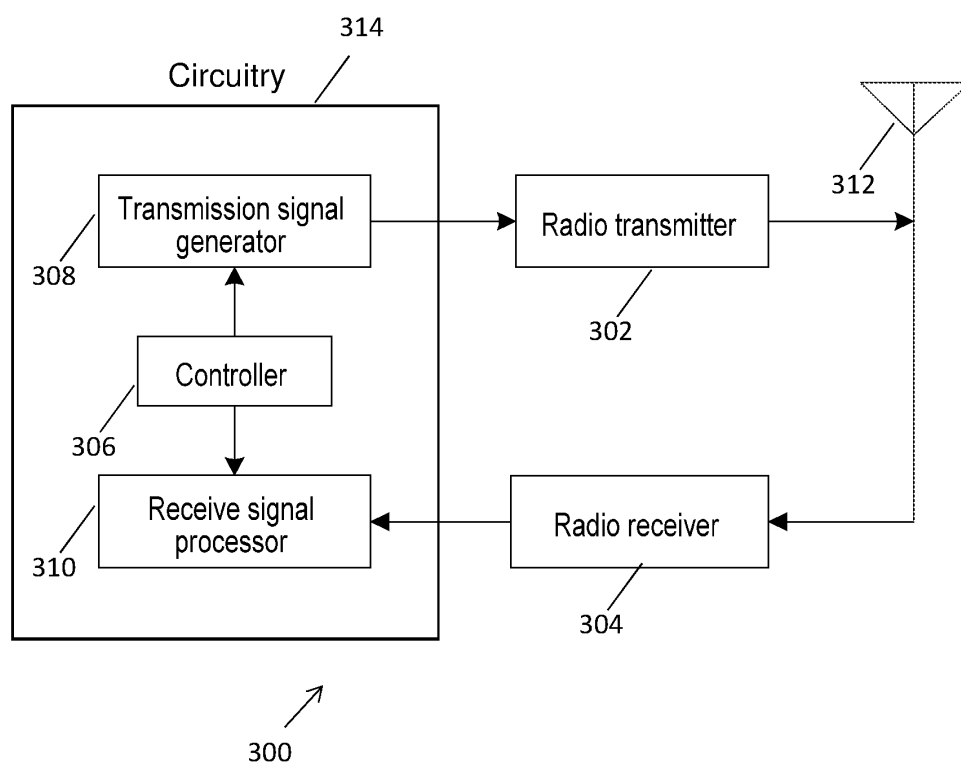


Fig. 2D

9/31

**Fig. 3A**

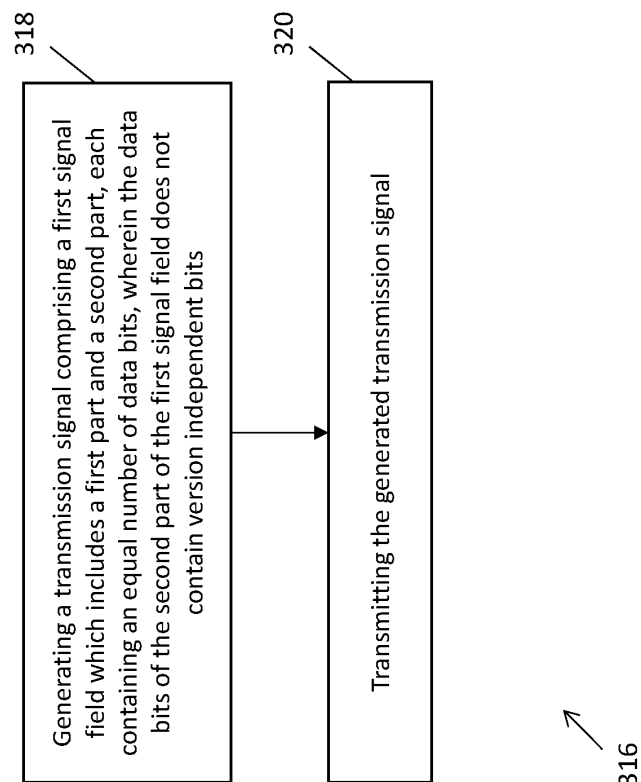


Fig. 3B

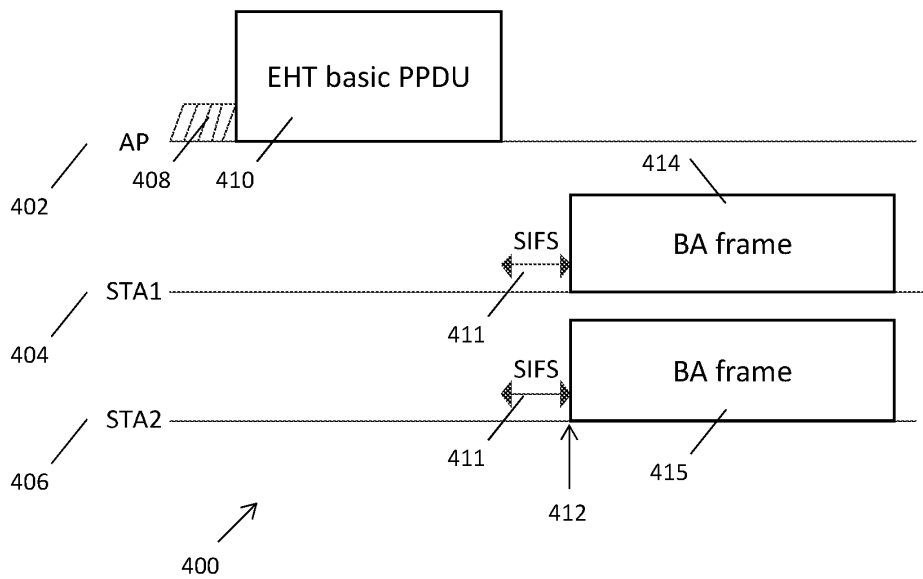


Fig. 4A

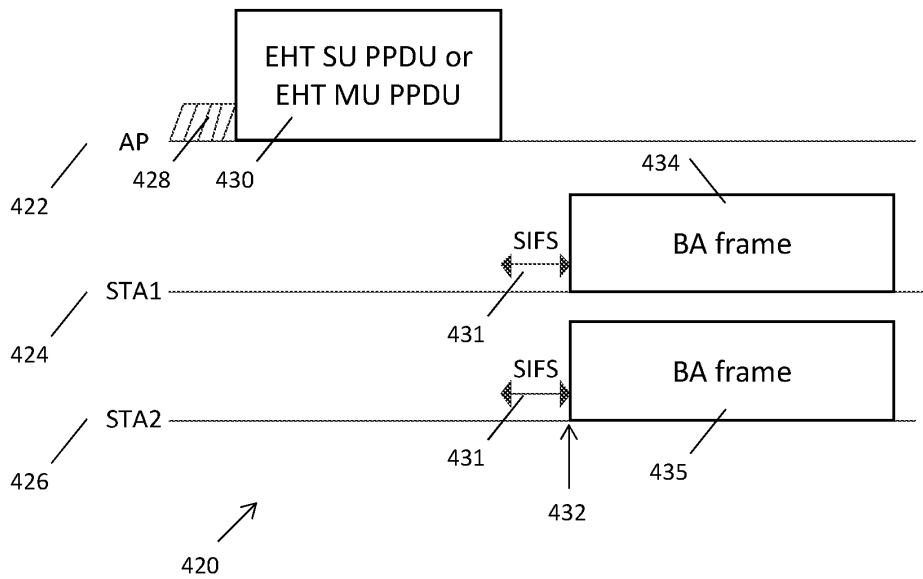


Fig. 4B

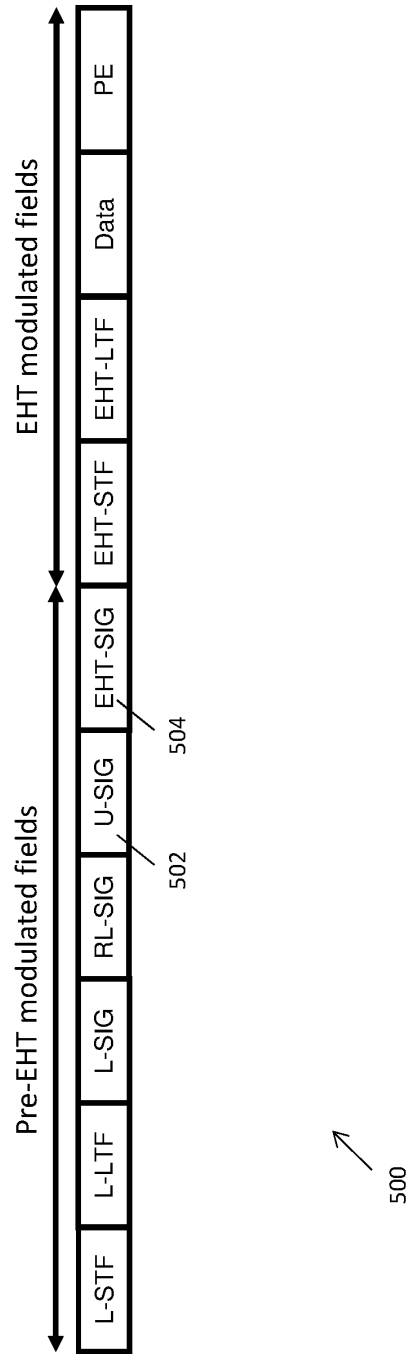


Fig. 5A

502 ↗

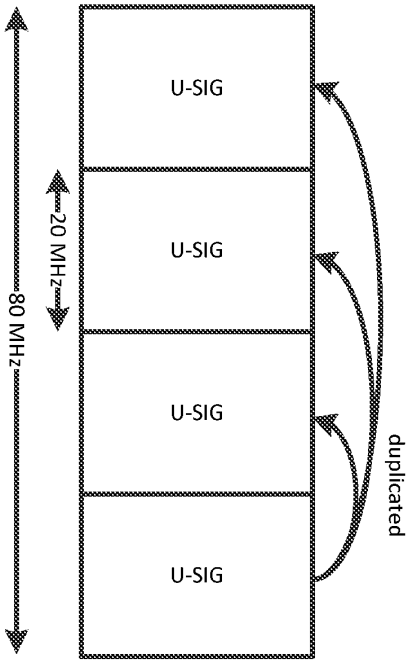


Fig. 5B

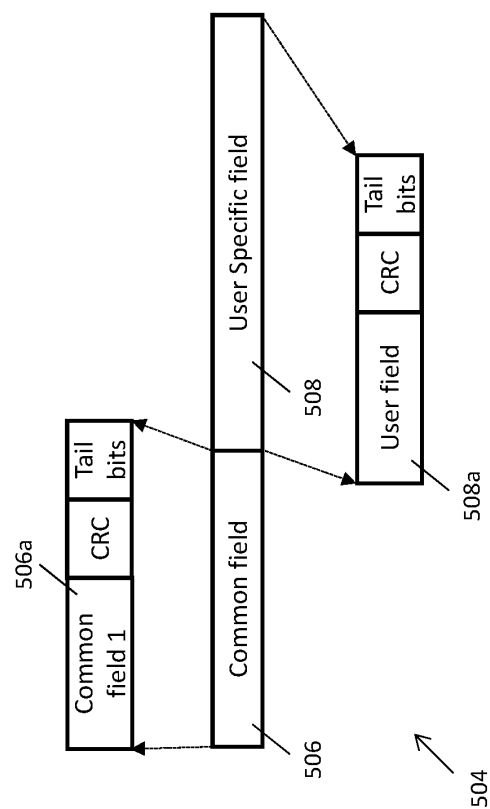


Fig. 5C

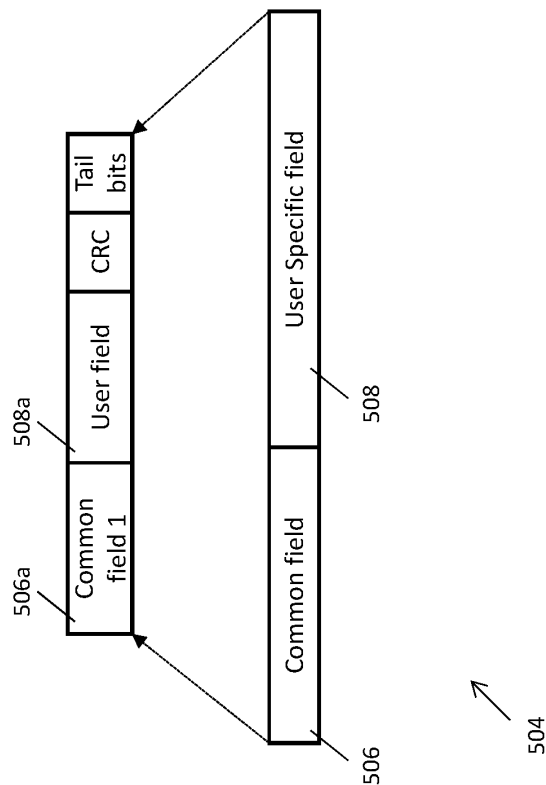


Fig. 5D

CBW (MHz)	L	
	1	2
20	1	N.A.
40	2	1
80	2	2
80+80, 160	2	2
160+160, 320	2	2

Fig. 5E

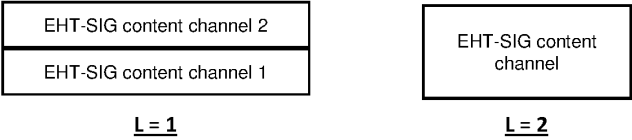


Fig. 5F

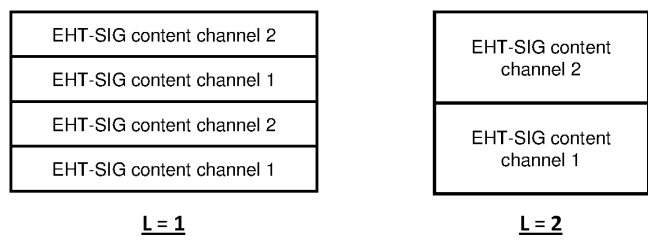


Fig. 5G

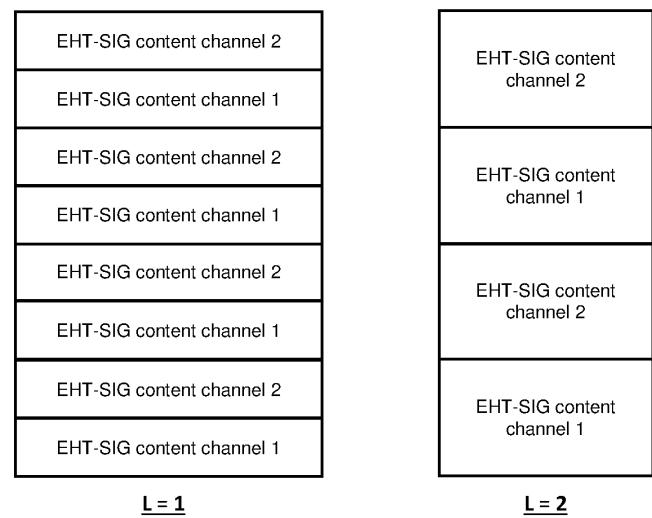


Fig. 5H

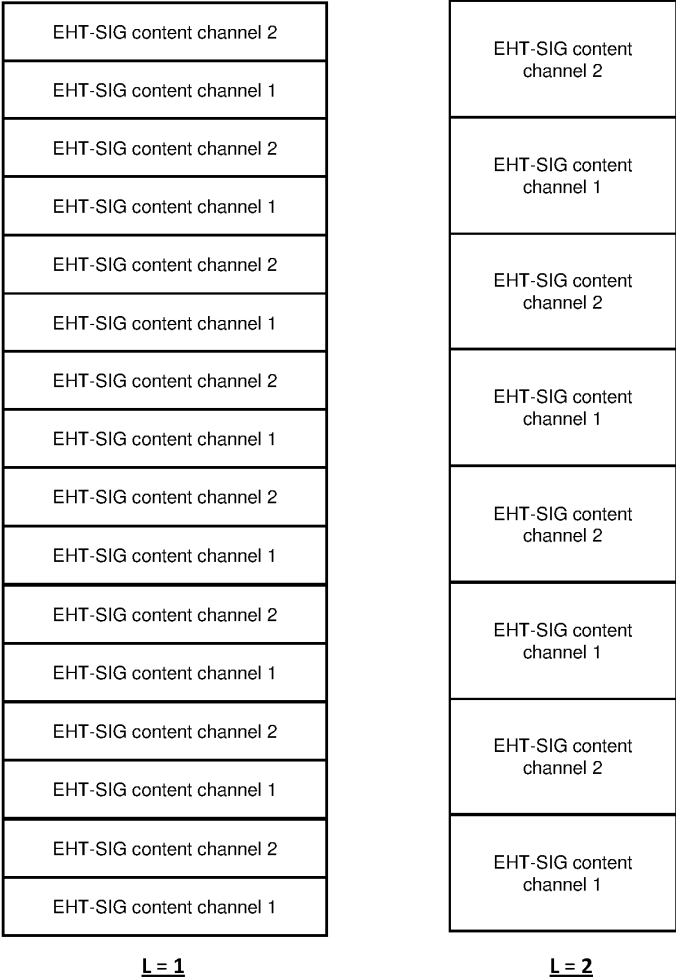


Fig. 5I

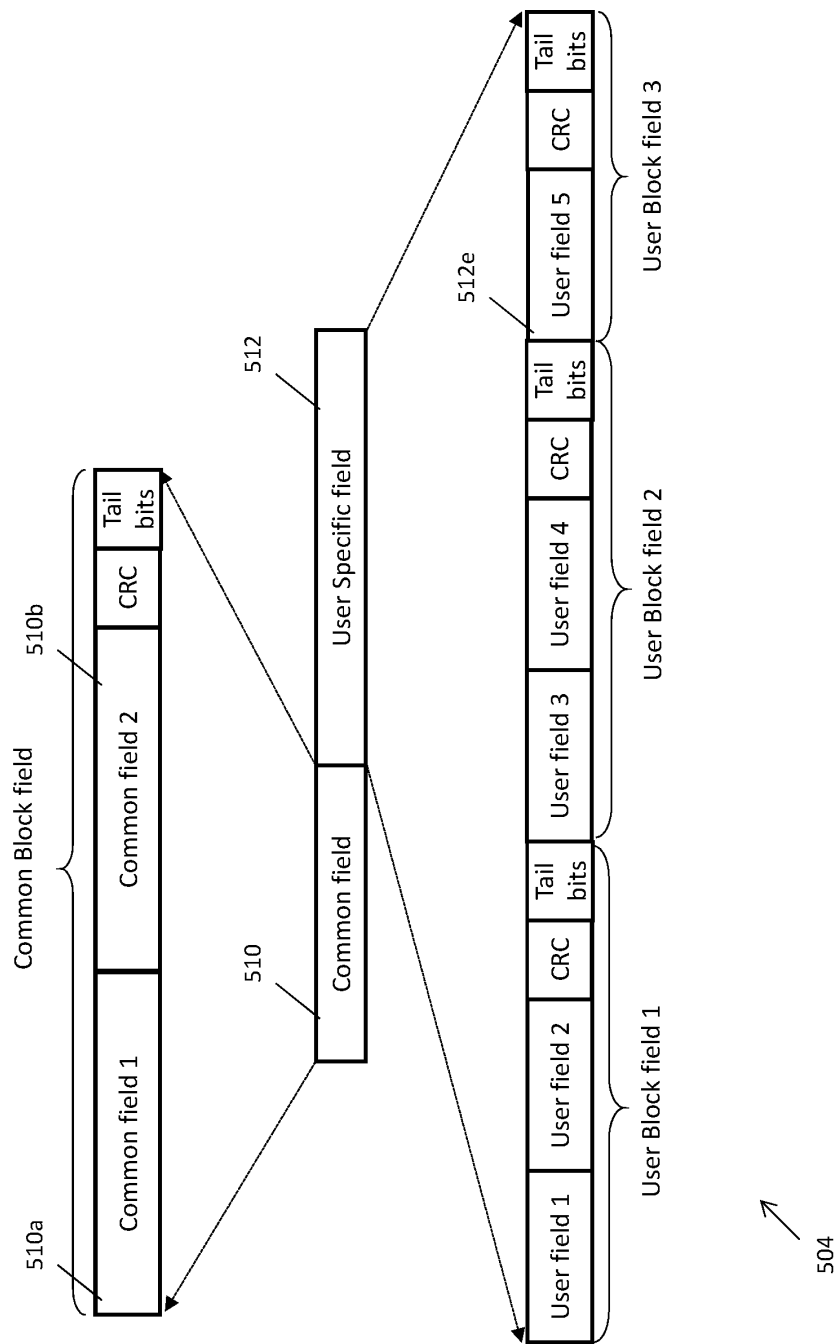


Fig. 5J

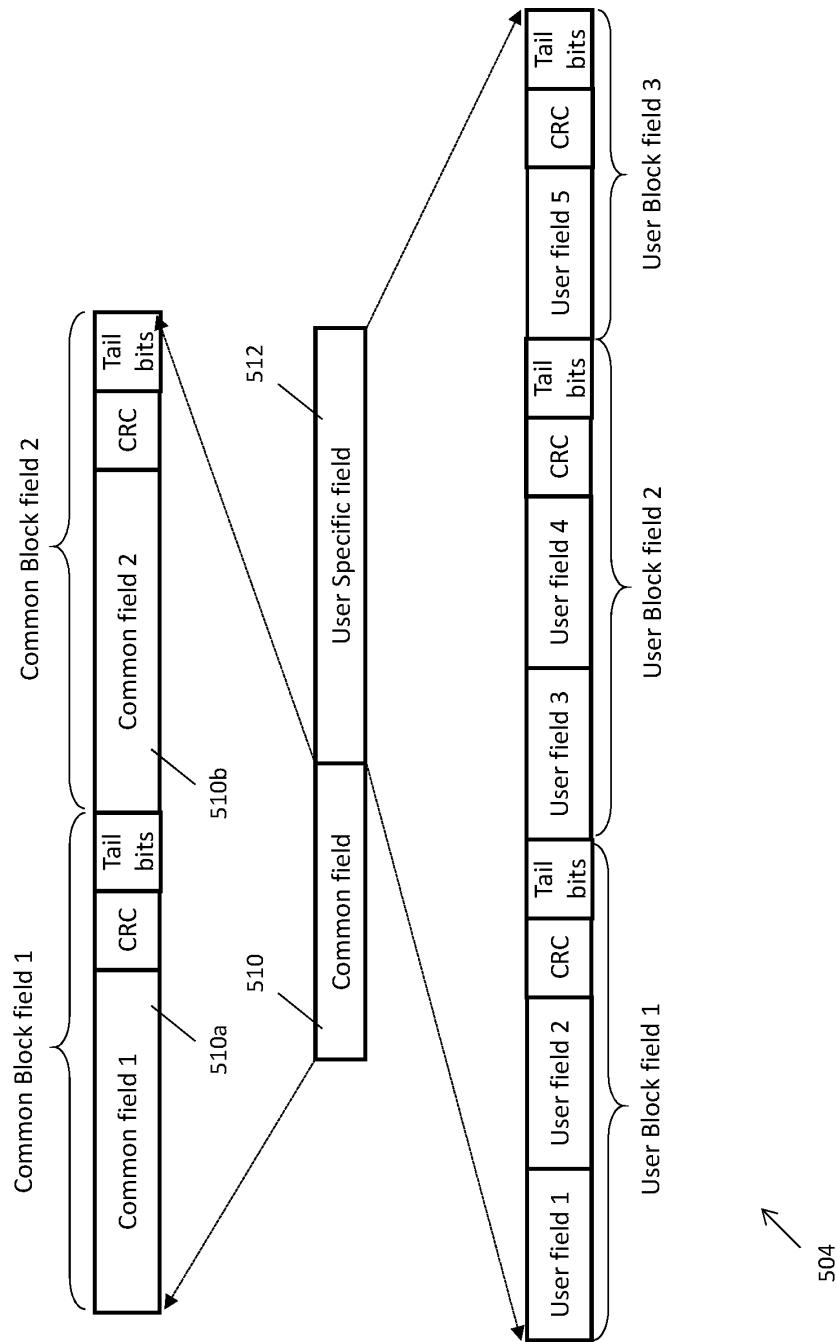


Fig. 5K

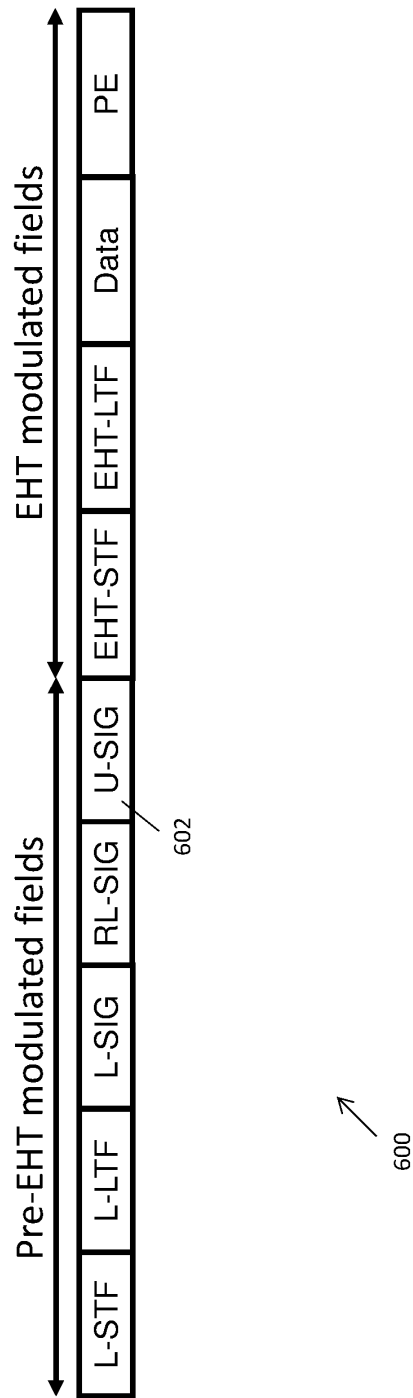


Fig. 6

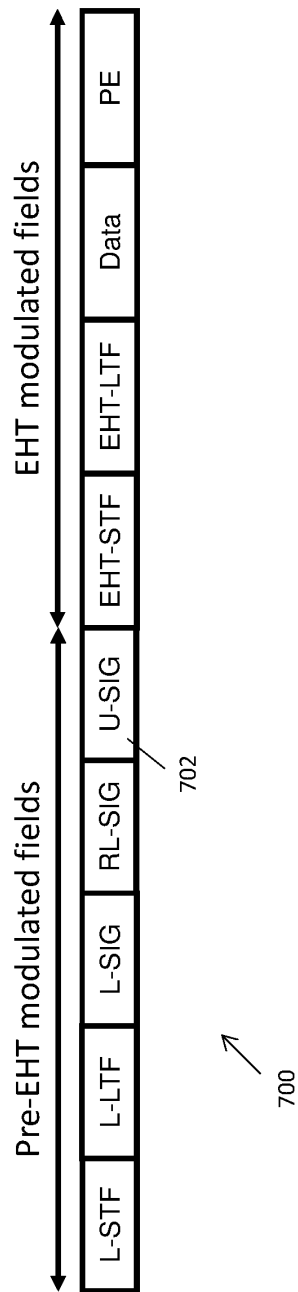


Fig. 7A

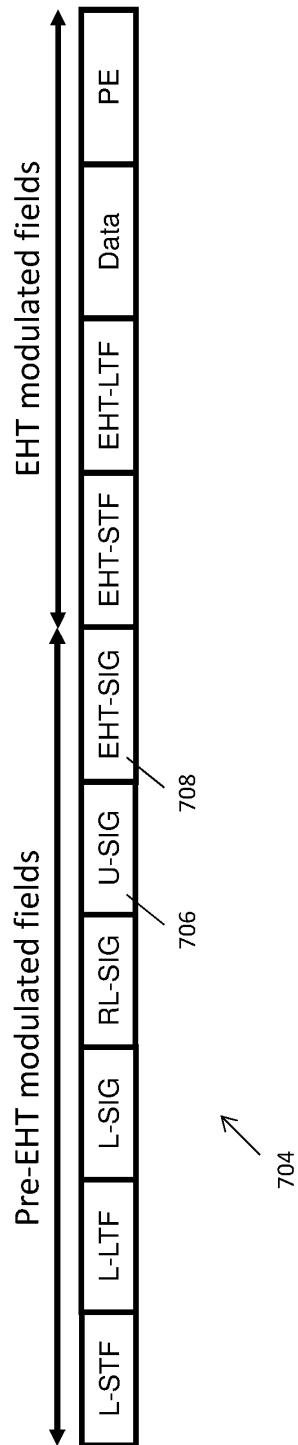


Fig. 7B

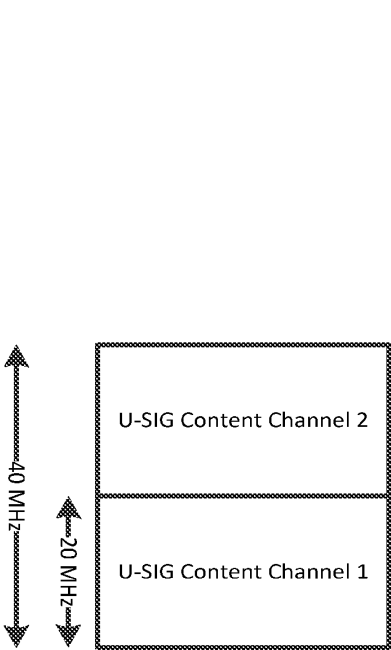


Fig. 7C

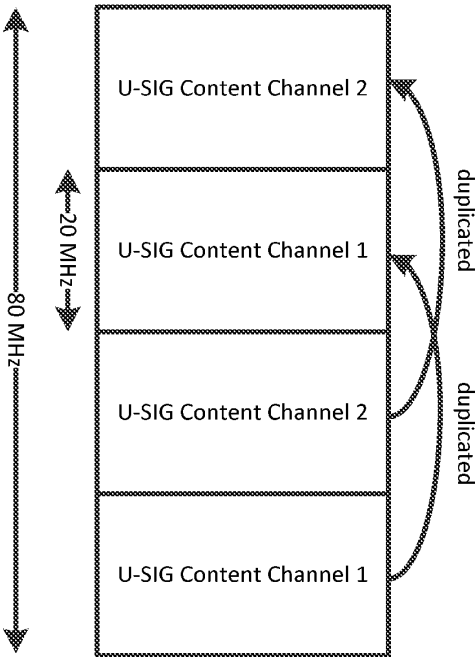


Fig. 7D

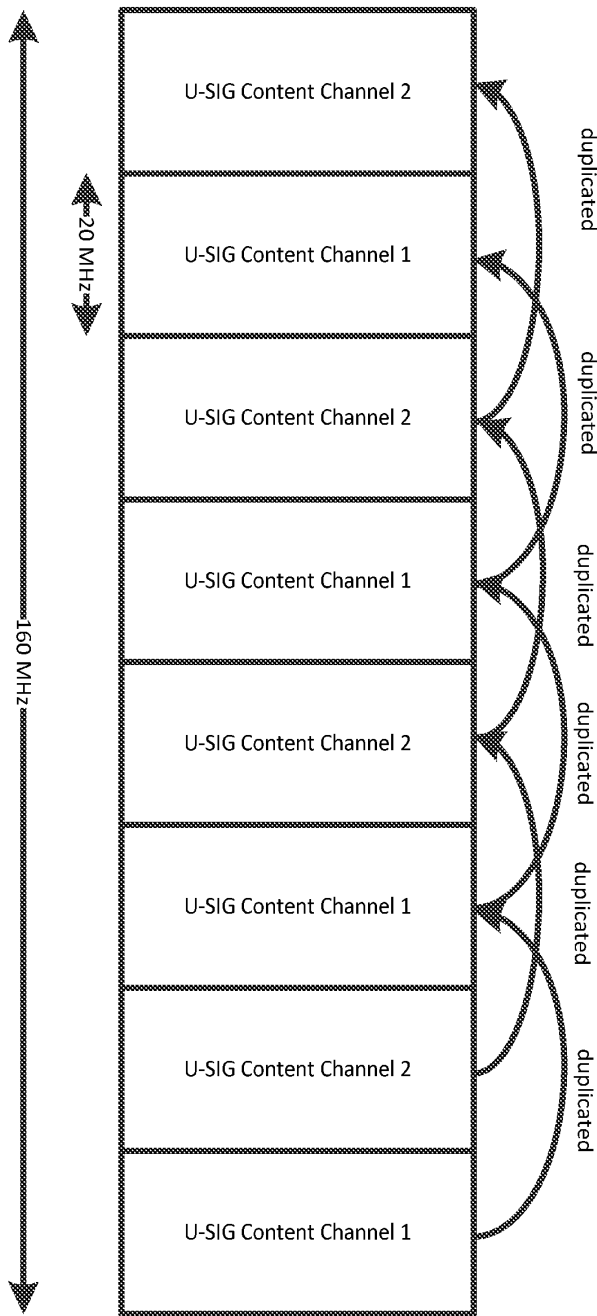


Fig. 7E

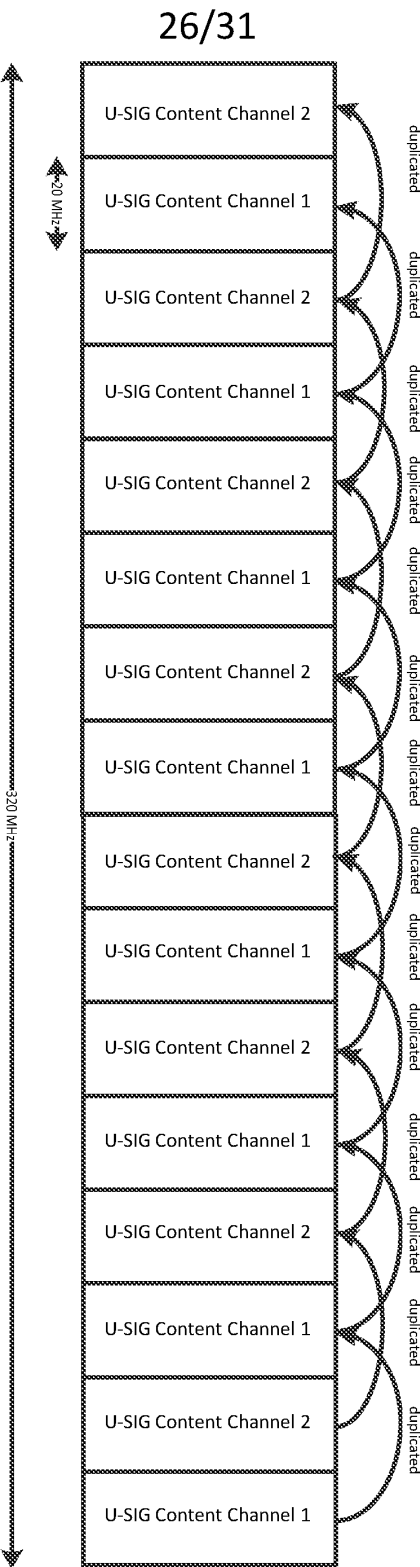
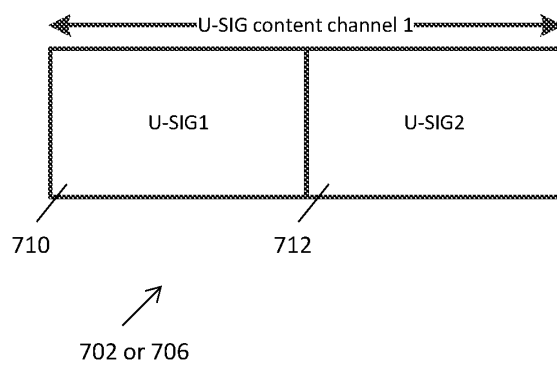
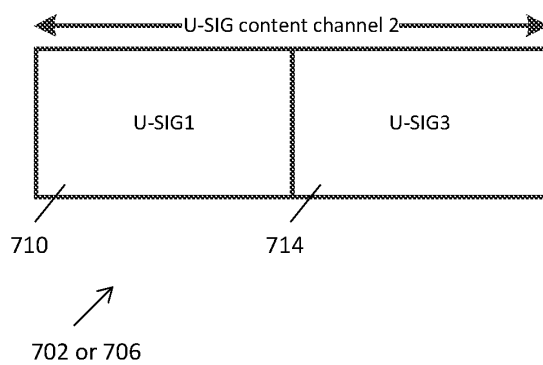


Fig. 7F

27/31

**Fig. 7G****Fig. 7H**

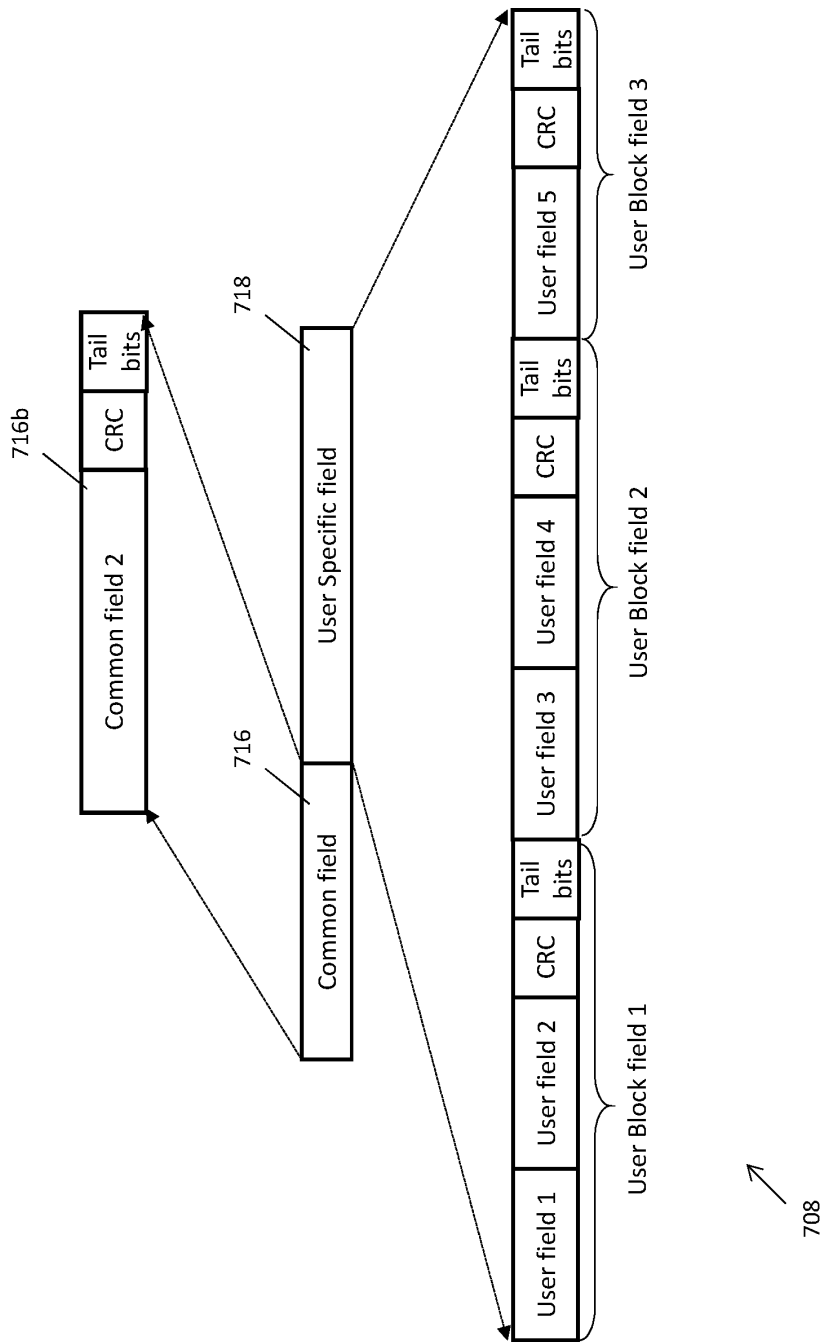


Fig. 7I

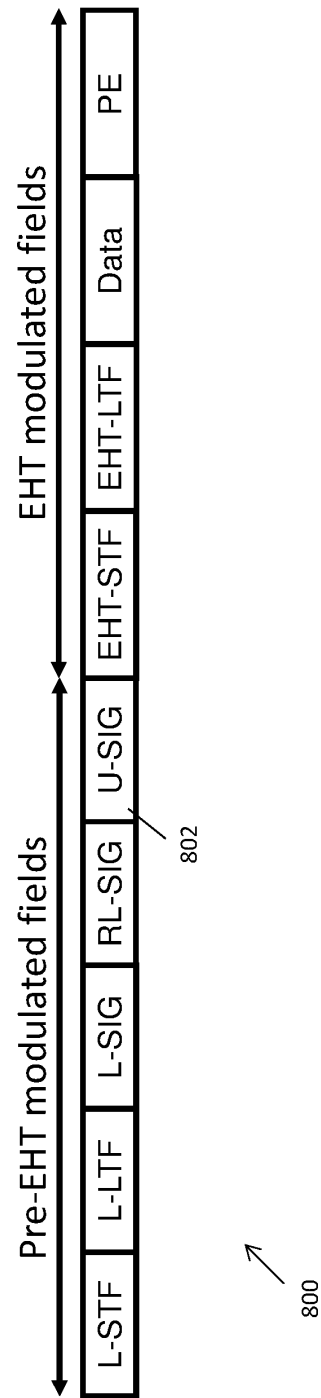
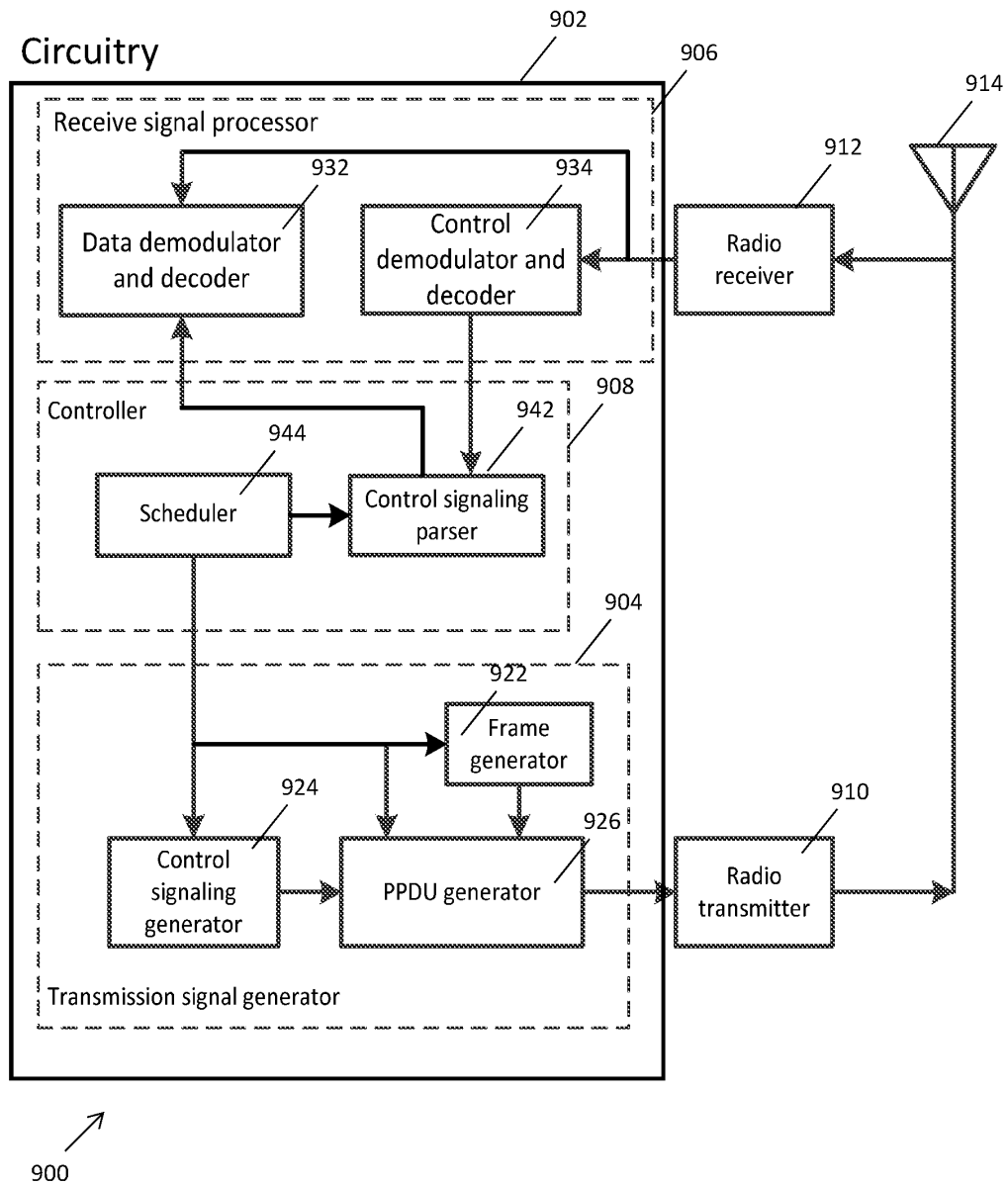
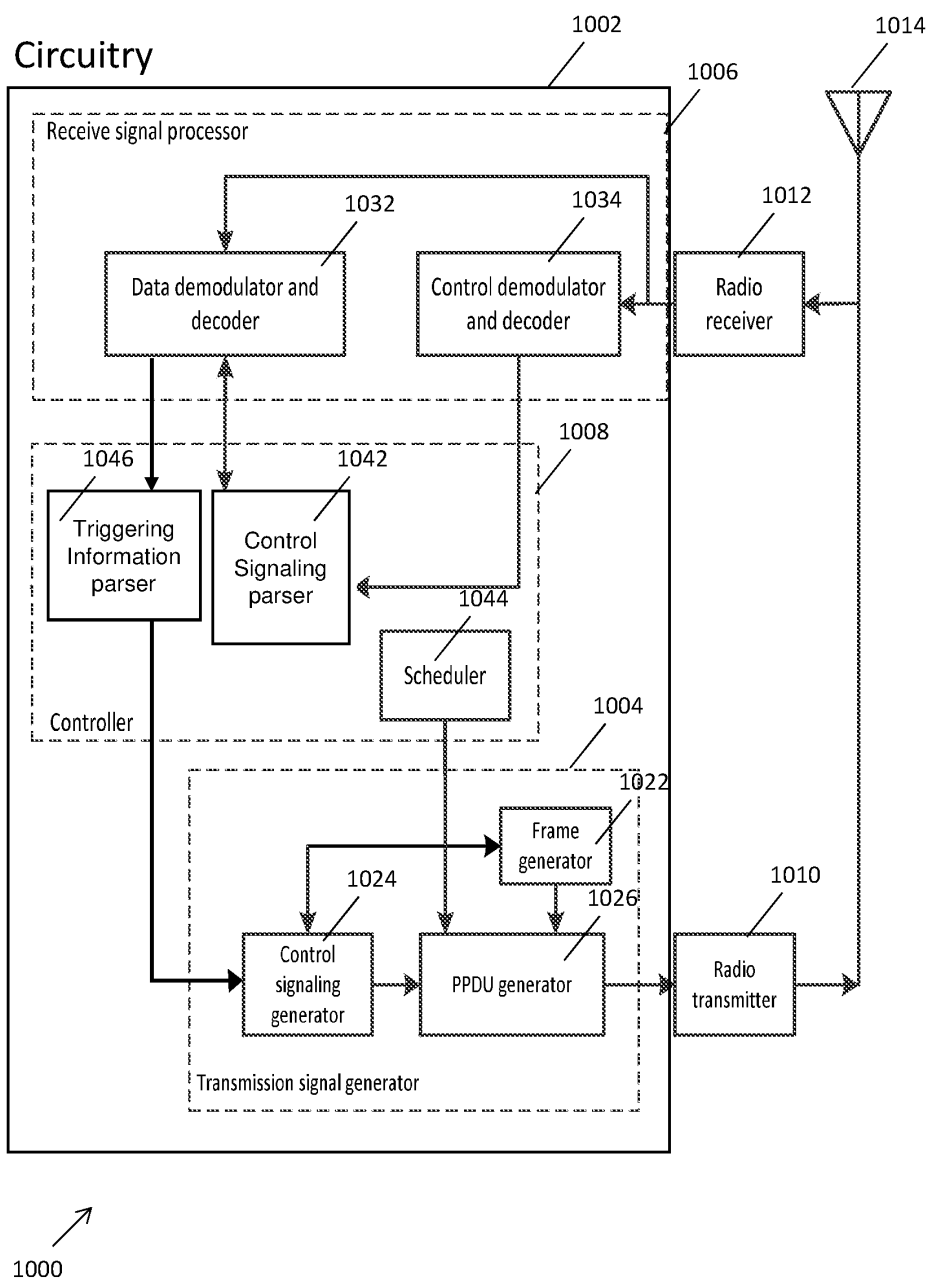


Fig. 8

30/31

**Fig. 9**

31/31

**Fig. 10**