SYSTEM AND METHOD FOR REDUCING POWER CONSUMPTION IN AN ELECTRONIC CIRCUIT

ABSTRACT OF THE DISCLOSURE

While load circuitry operates in a first power mode, per cycle of the load circuitry, up to N number of data bits are loaded from a memory, where N is an integer number and $N > 1$. While the load circuitry operates in a second power mode, per cycle of the load circuitry, up to M number of data bits are loaded from the memory, where M is an integer number and $0 < M < N$.

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