

DATA RECEIVING CIRCUIT, DATA RECEIVING SYSTEM AND STORAGE DEVICE

ABSTRACT

The embodiments of the present disclosure provide a data receiving circuit, a data receiving system and a storage device. The data receiving circuit includes: a first amplification module configured to receive a data signal and a reference signal, compare the data signal and the reference signal in response to a first sampling clock signal, and output a first voltage signal and a second voltage signal; a decision feedback control module configured to generate a second sampling clock signal in response to the enable signal; a decision feedback equalization module configured to, when the enable signal is in a first level value interval, perform decision feedback equalization in response to the second sampling clock signal and stop performing the decision feedback equalization when the enable signal is in a second level value interval; and a second amplification module configured to process the first voltage signal and the second voltage signal and output the first output signal and the second output signal. The embodiments of the present disclosure are favorable for at least improving the reception performance of the data receiving circuit and simultaneously reducing the power consumption of the data receiving circuit.

[Fig. 5]