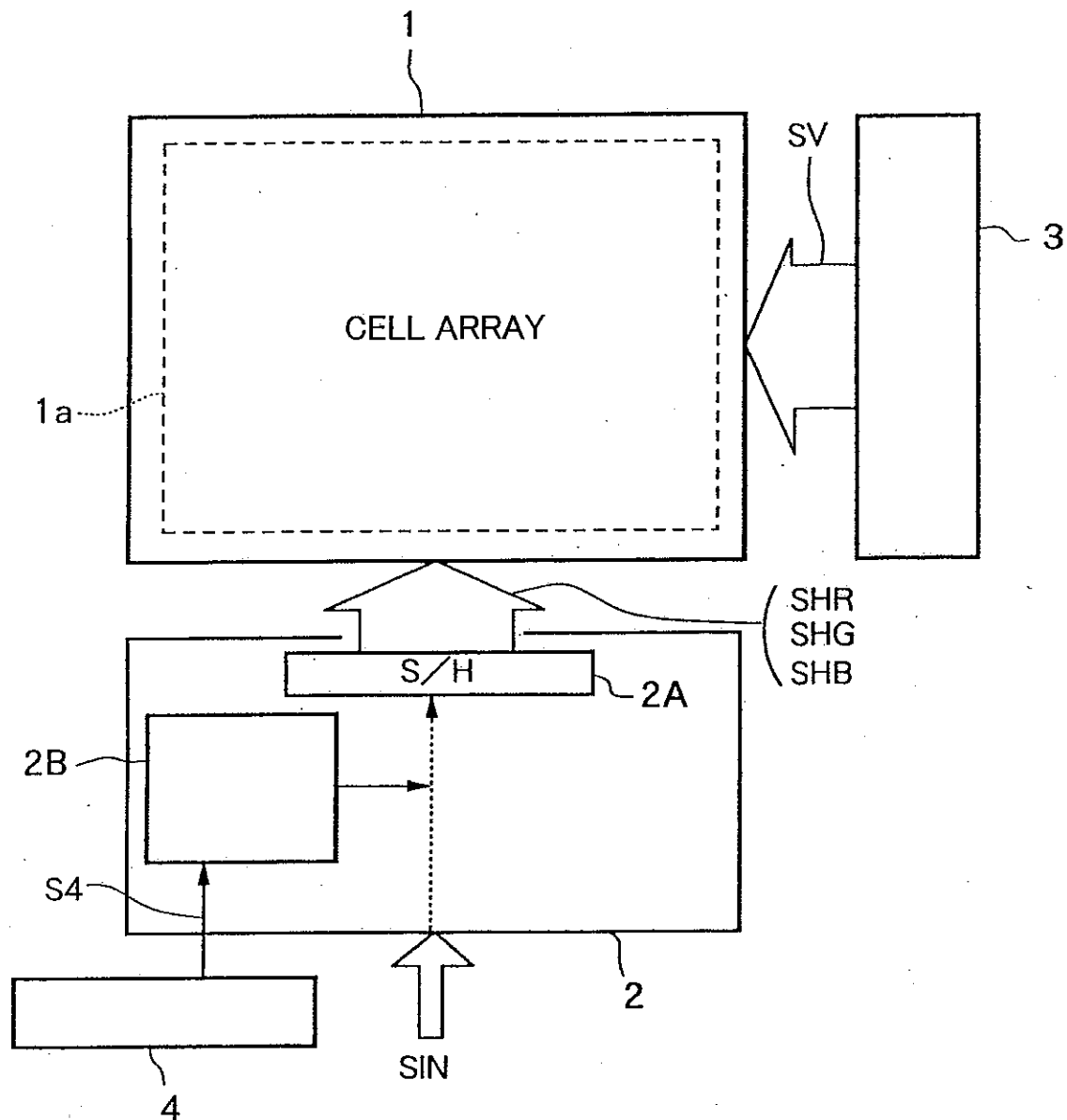


FIG. 1



2: CIRCUIT FOR GENERATING DRIVE SIGNAL FROM IMAGE SIGNAL

2A: SAMPLE HOLD CIRCUIT

2B: LEVEL ADJUSTMENT CIRCUIT

3: V-SCAN CIRCUIT

4: ADJUSTMENT INFORMATION RETRIEVE MEANS

FIG. 2

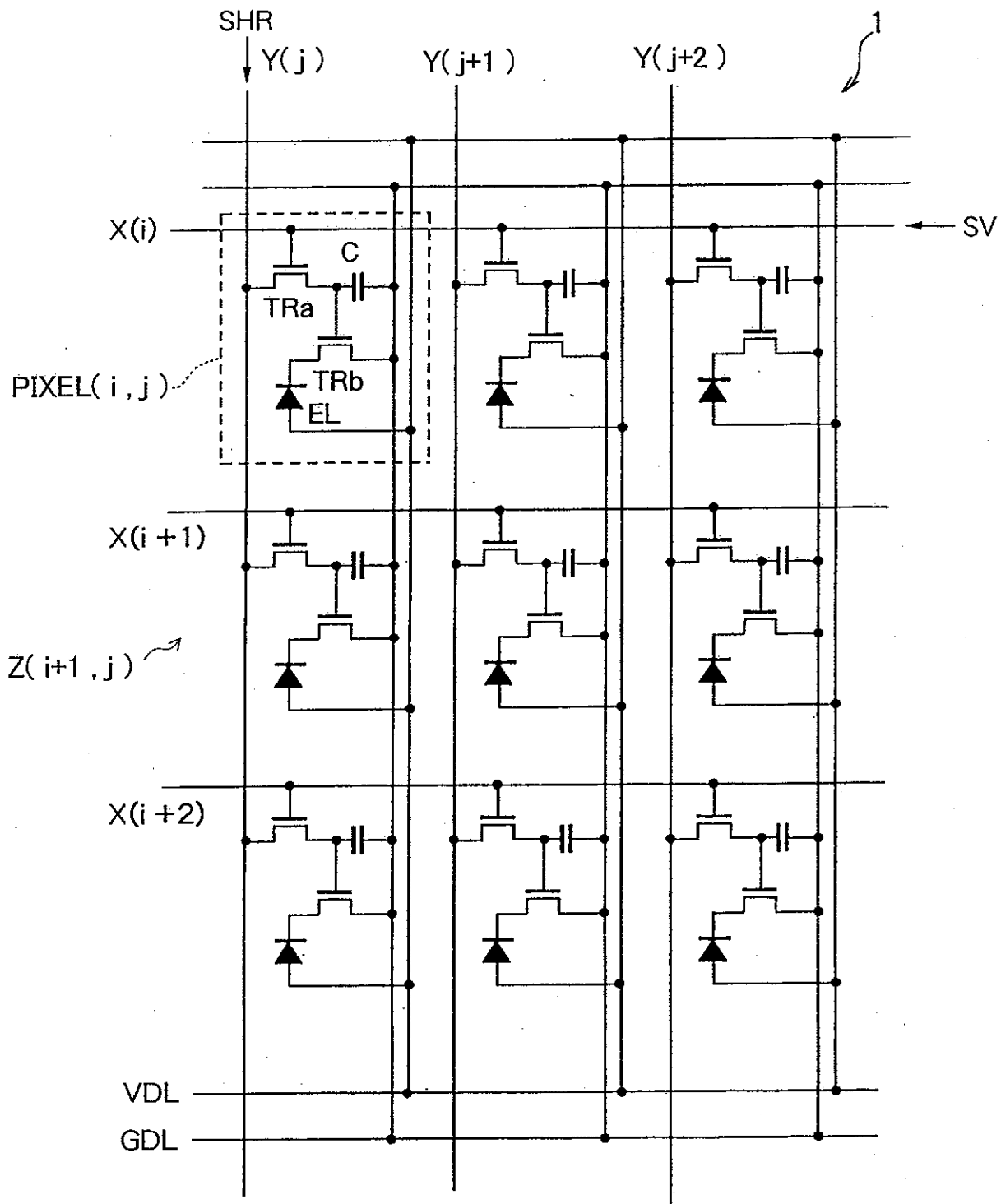


FIG. 3

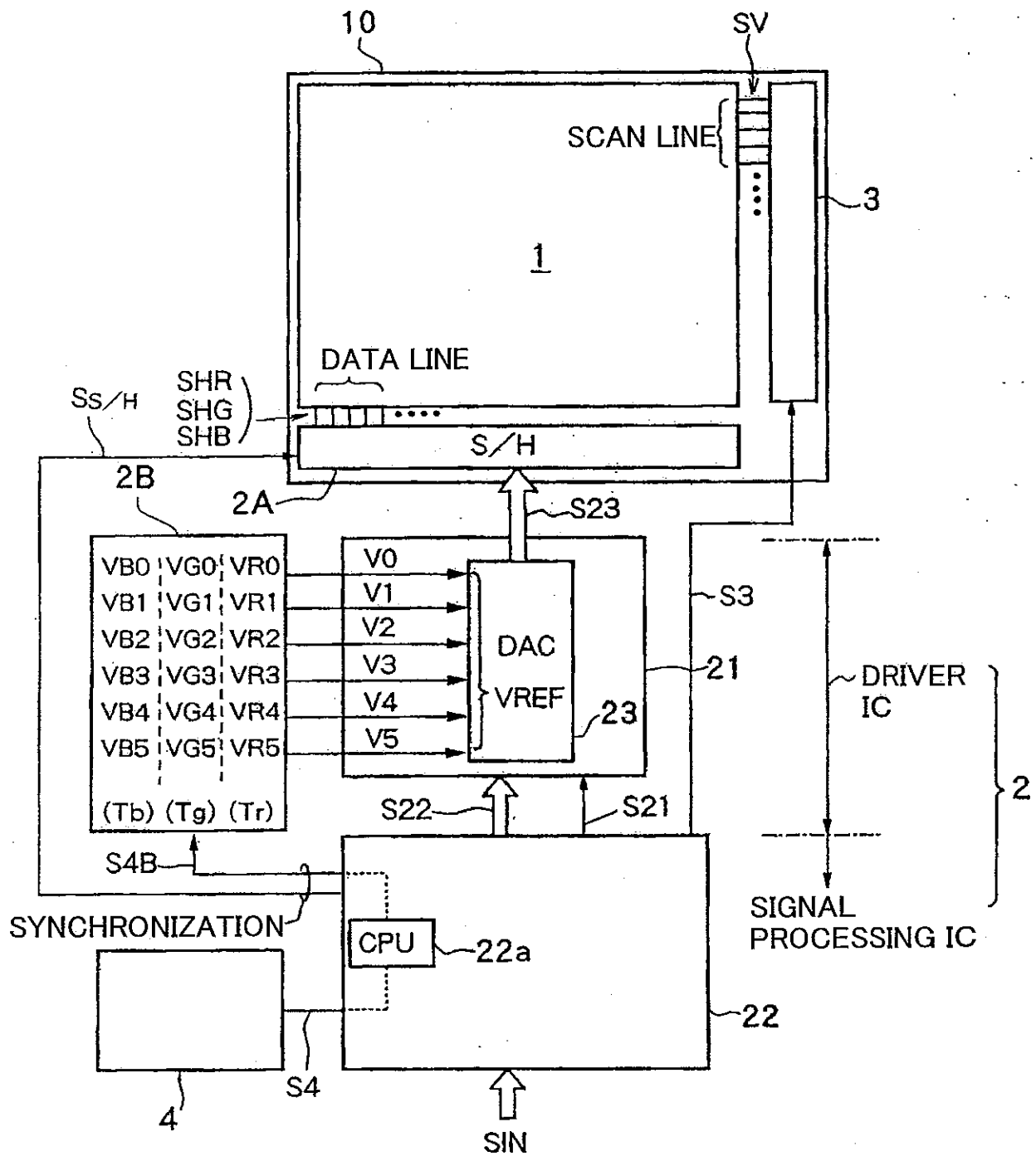


FIG. 4

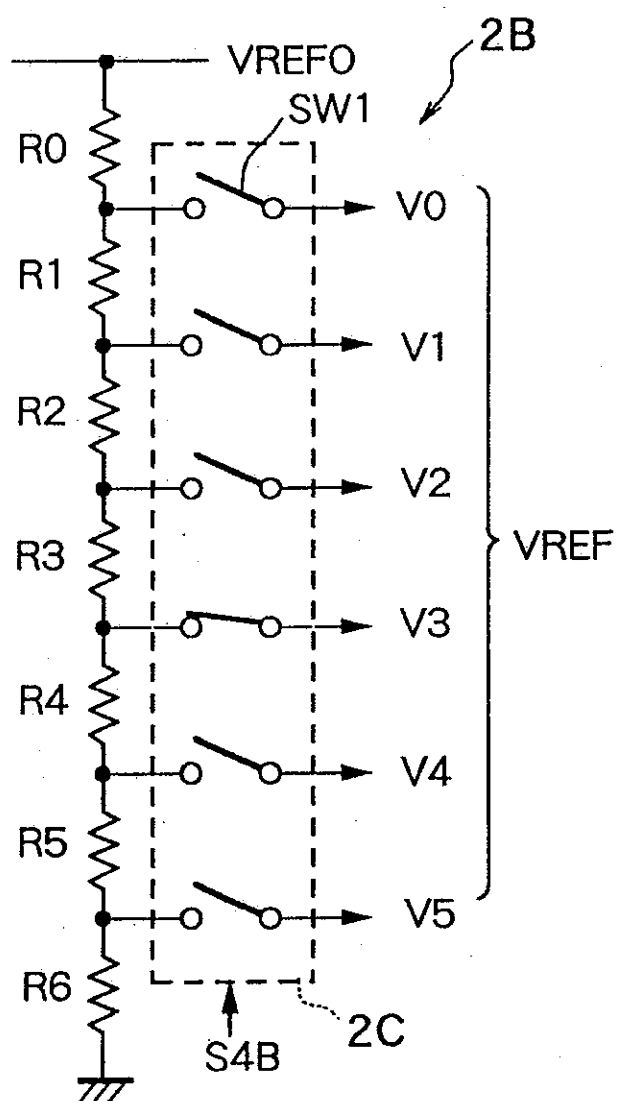


FIG. 5

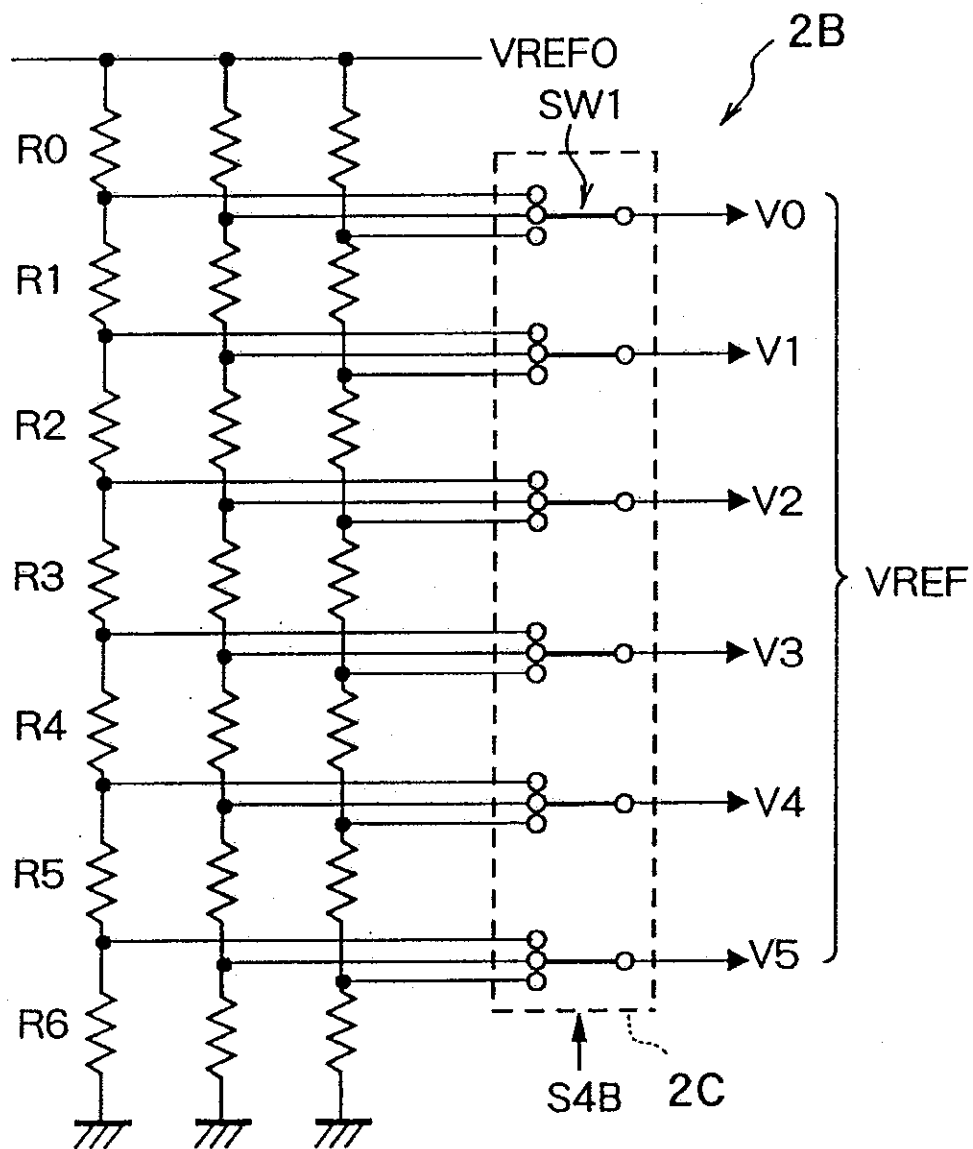


FIG. 6

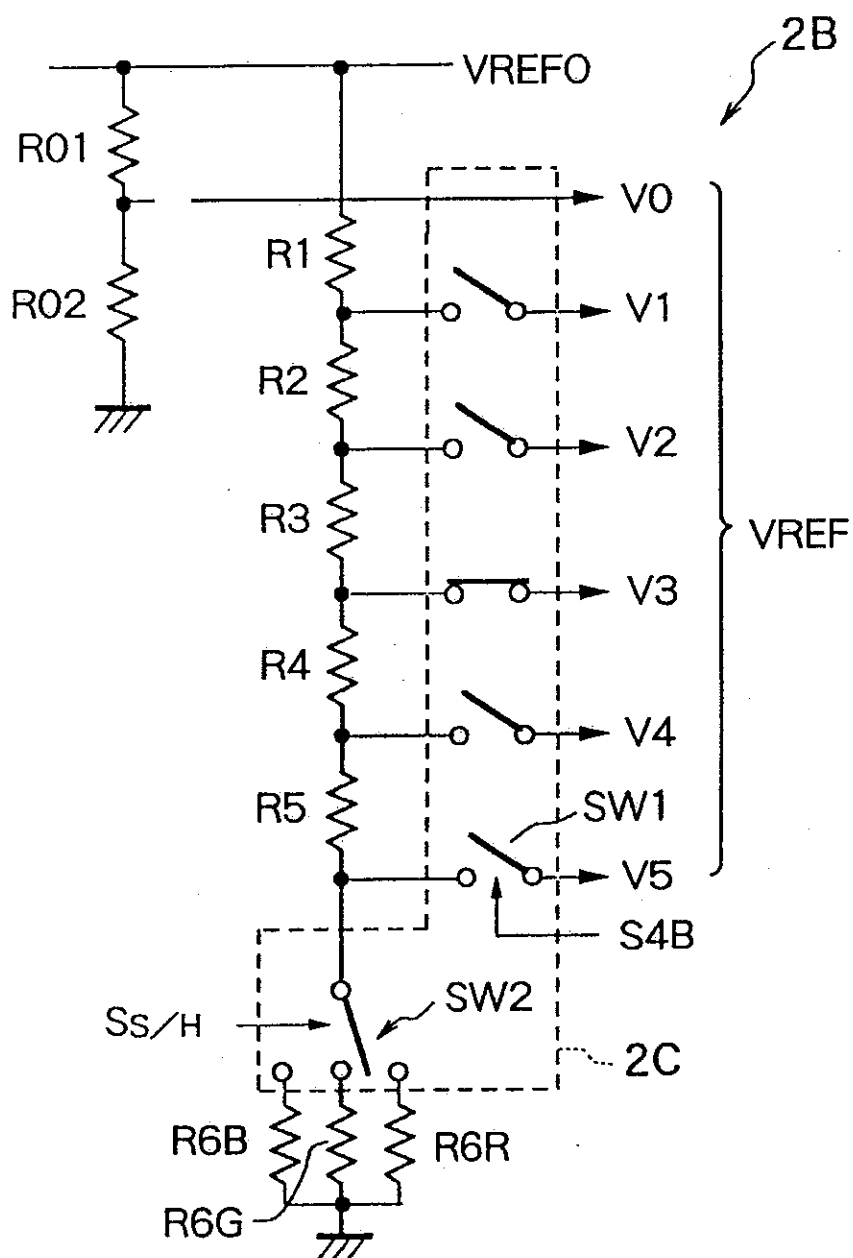


FIG. 7

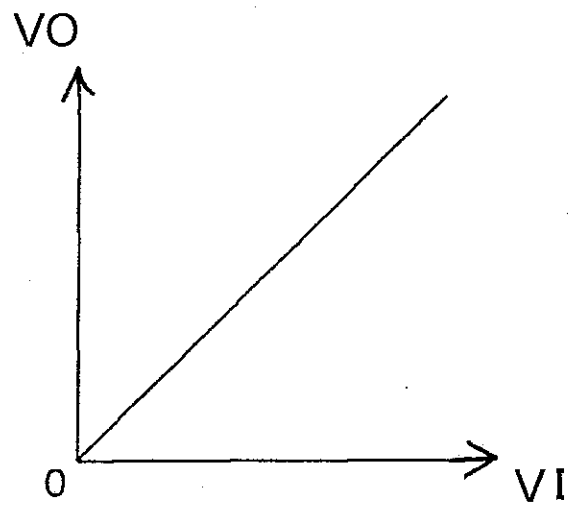


FIG. 8

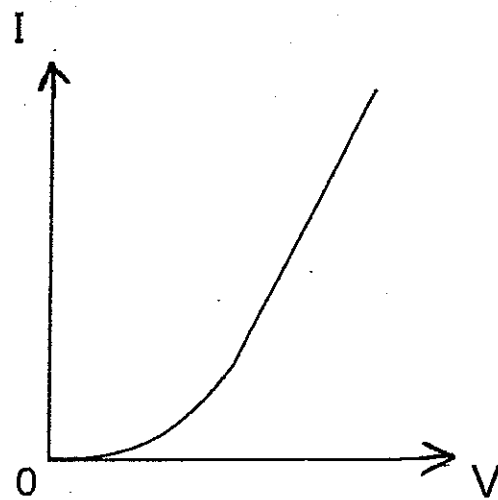


FIG. 9

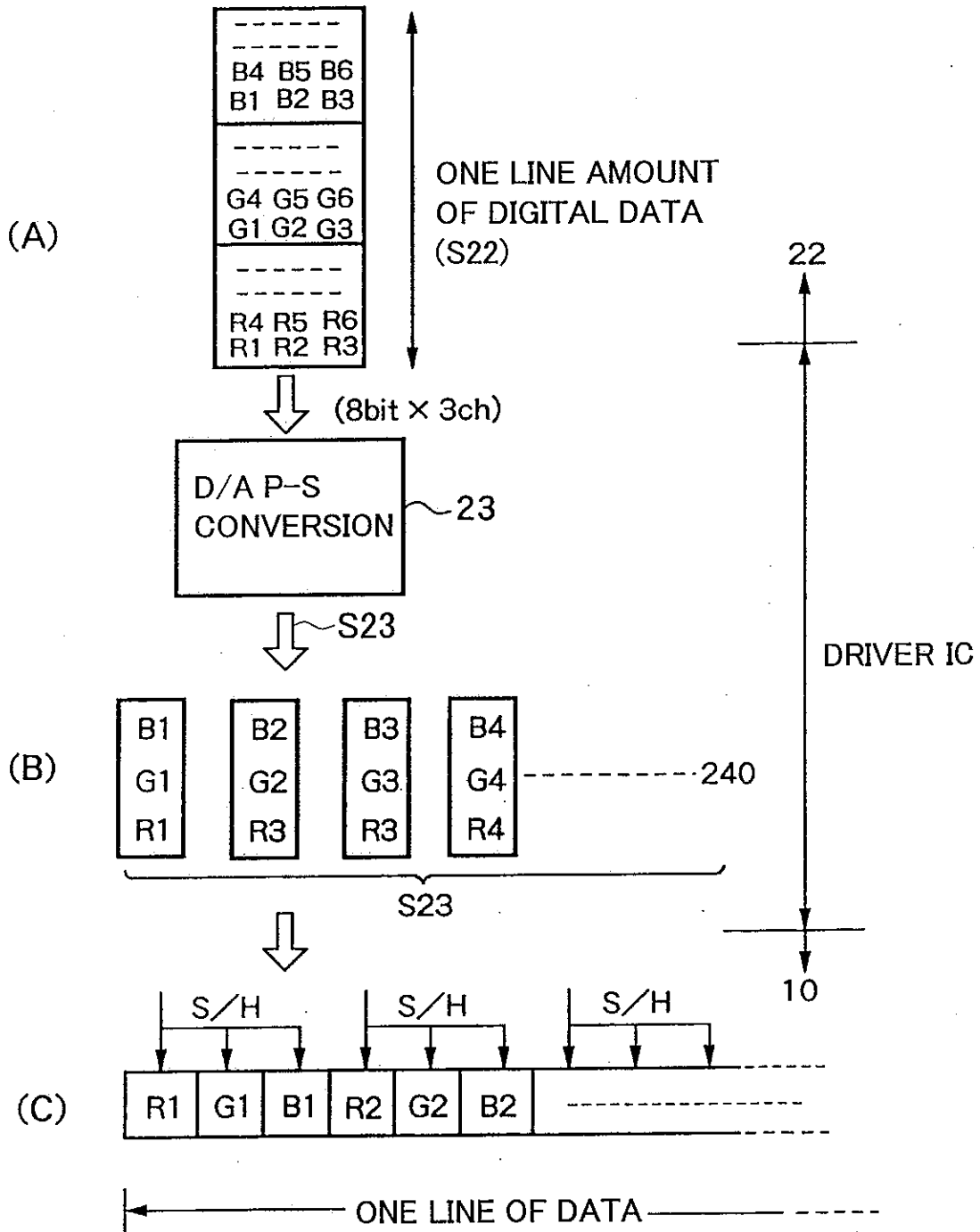


FIG. 10

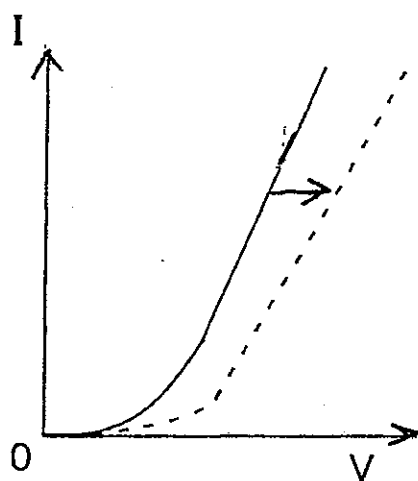


FIG. 11

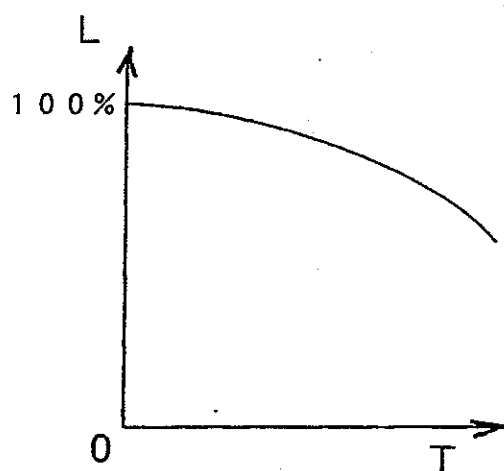
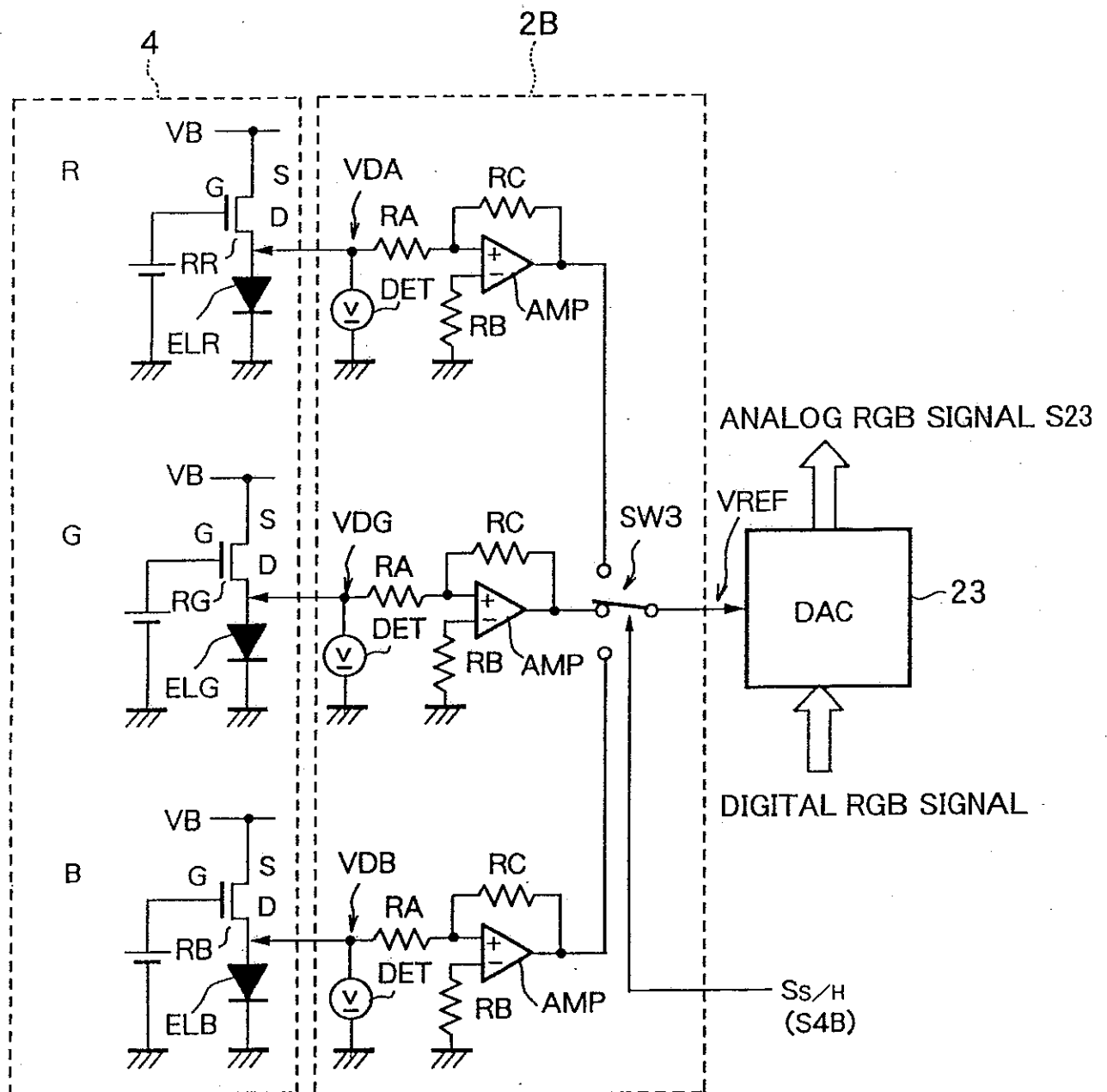


FIG. 12

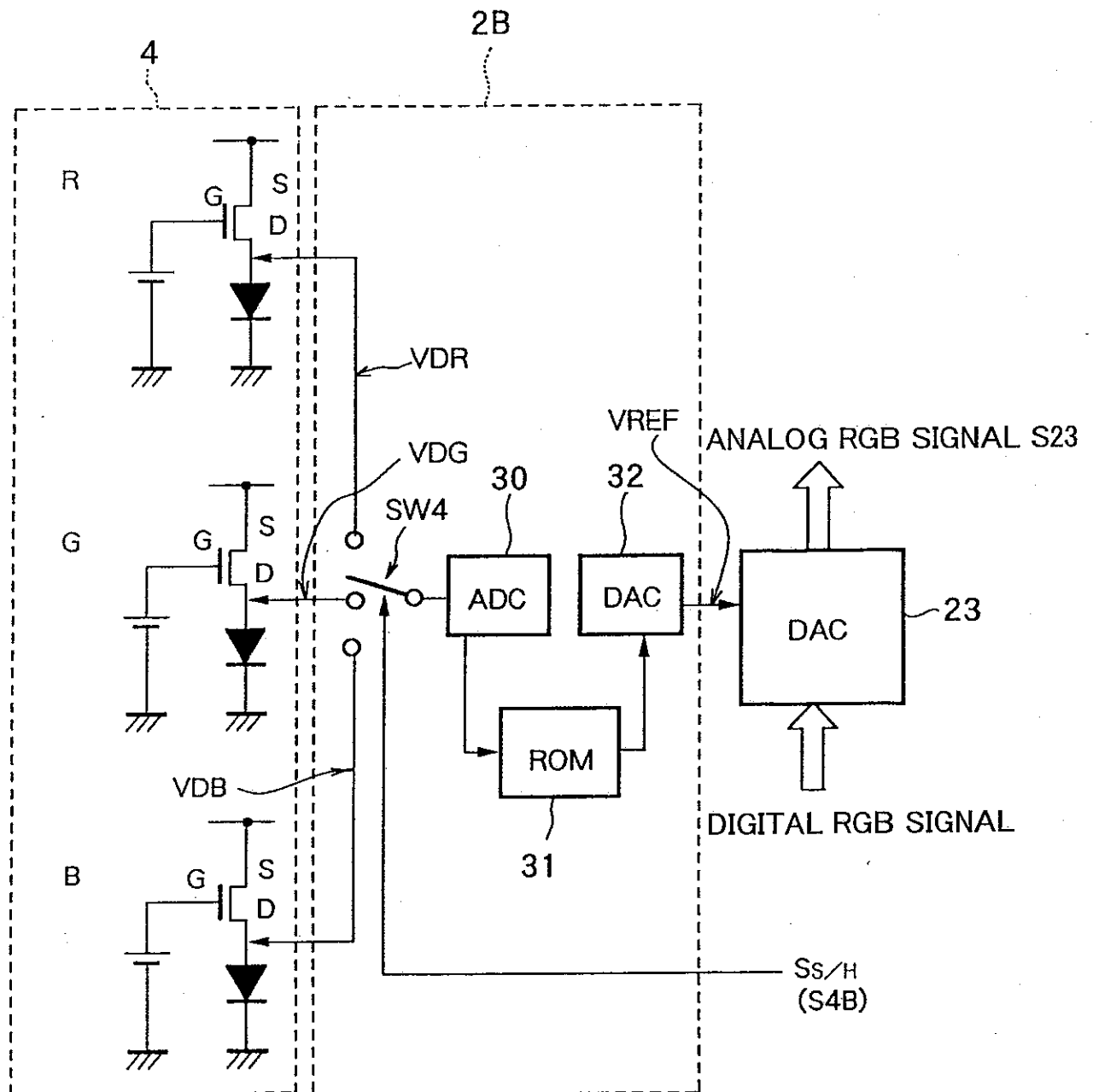


2B: LEVEL ADJUSTMENT CIRCUIT

4: ADJUSTMENT INFORMATION RETRIEVE MEANS

23: D/A CONVERTER

FIG. 13



2B: LEVEL ADJUSTMENT CIRCUIT
 4: ADJUSTMENT INFORMATION RETRIEVE MEANS
 23: D/A CONVERTER

FIG. 14

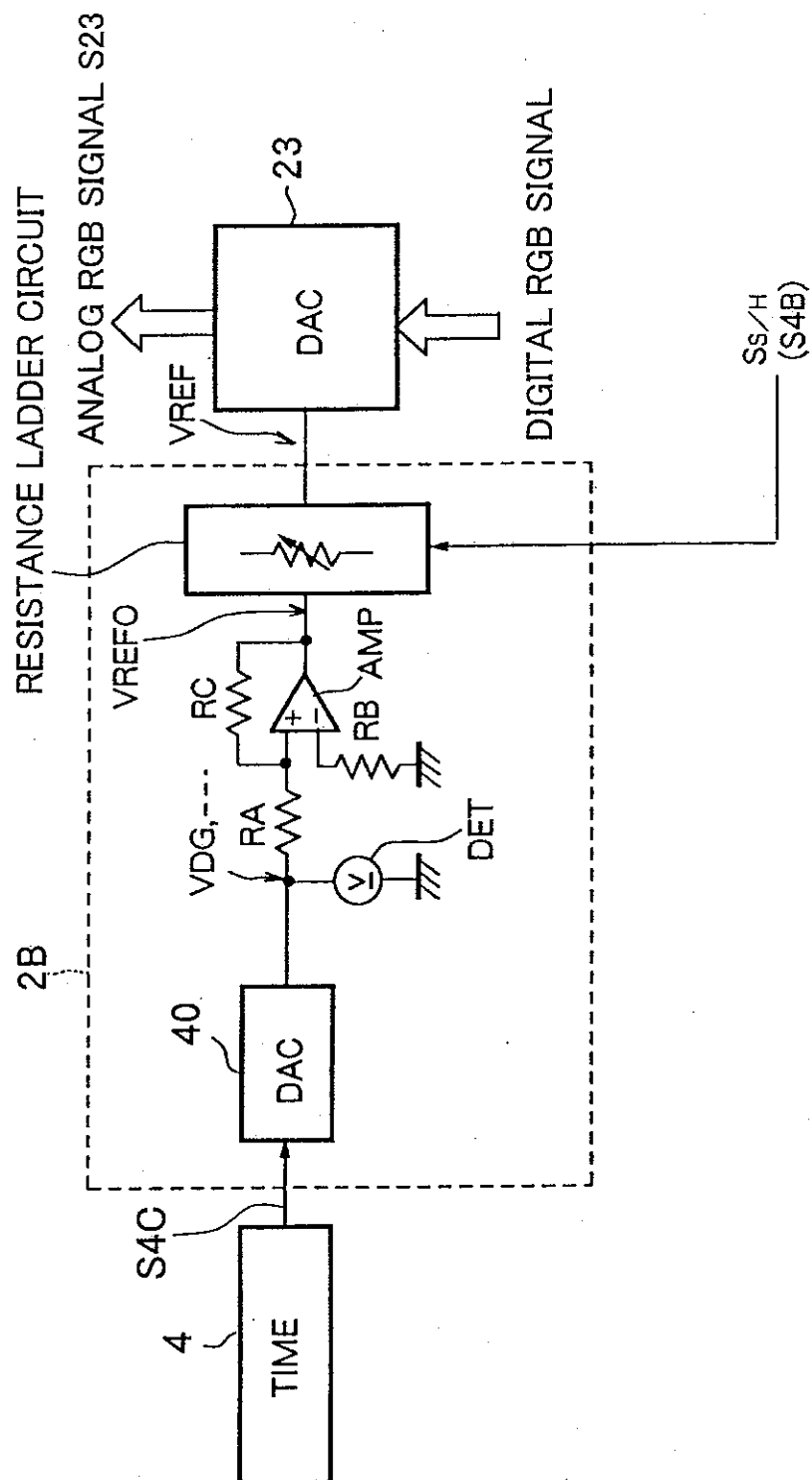


FIG. 15

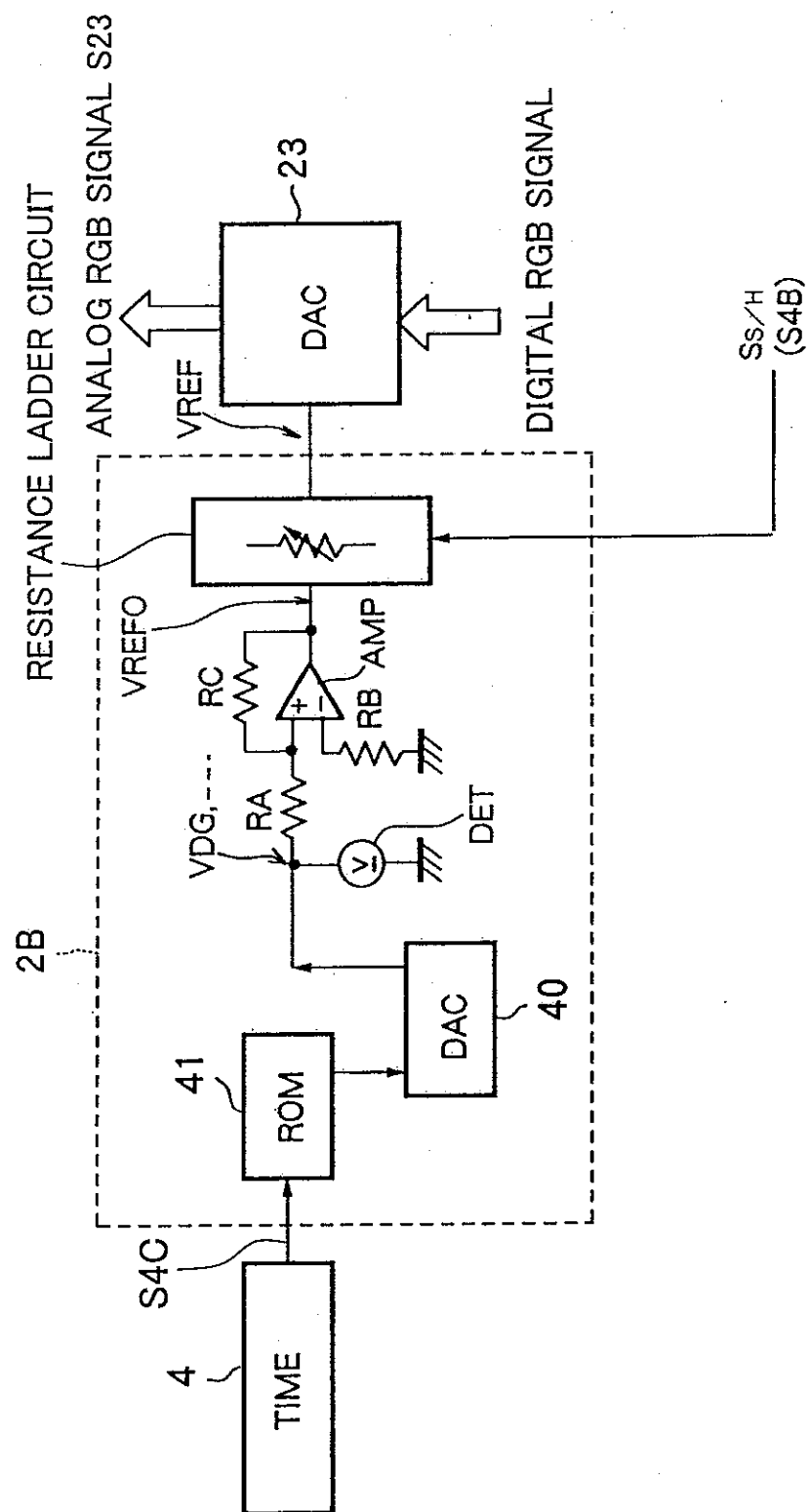


FIG. 16

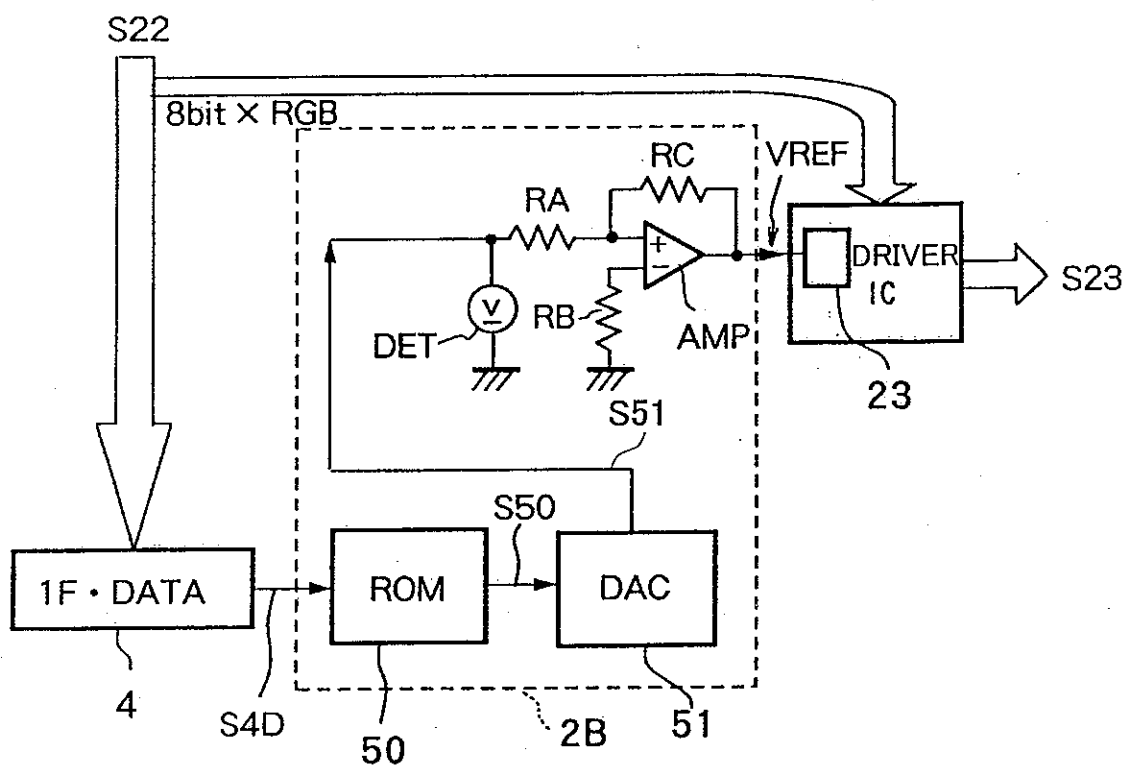


FIG. 17

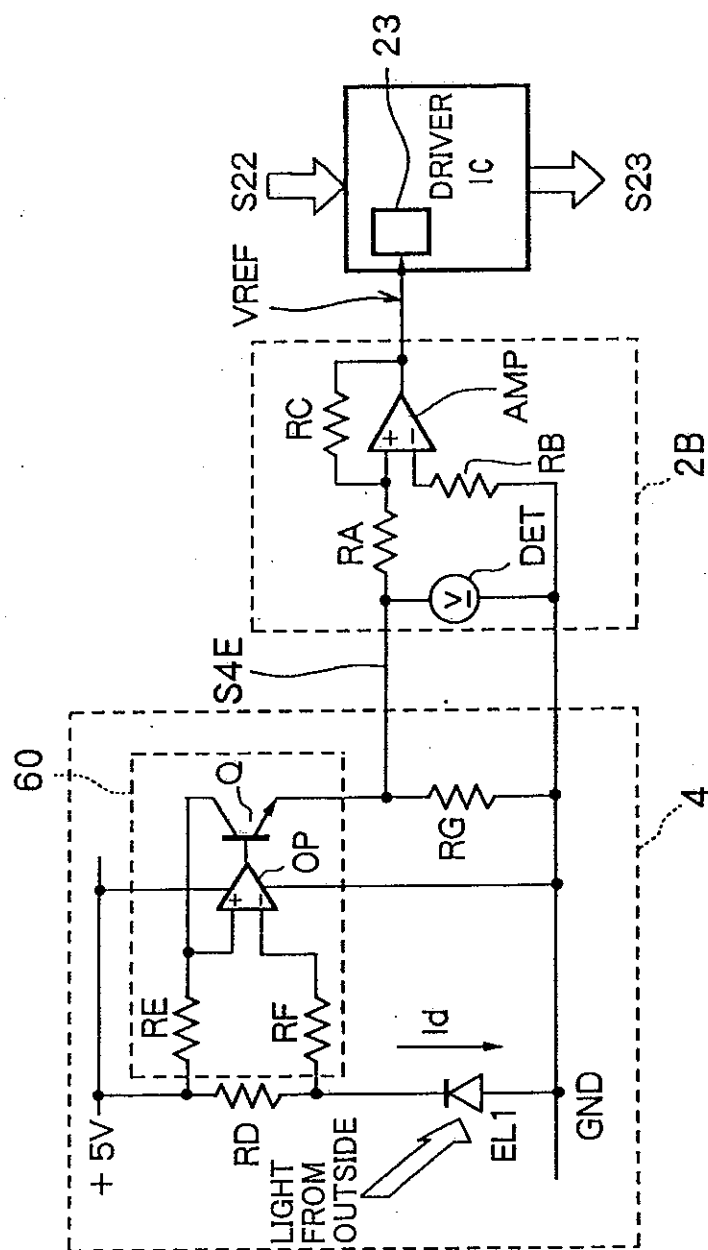


FIG. 18

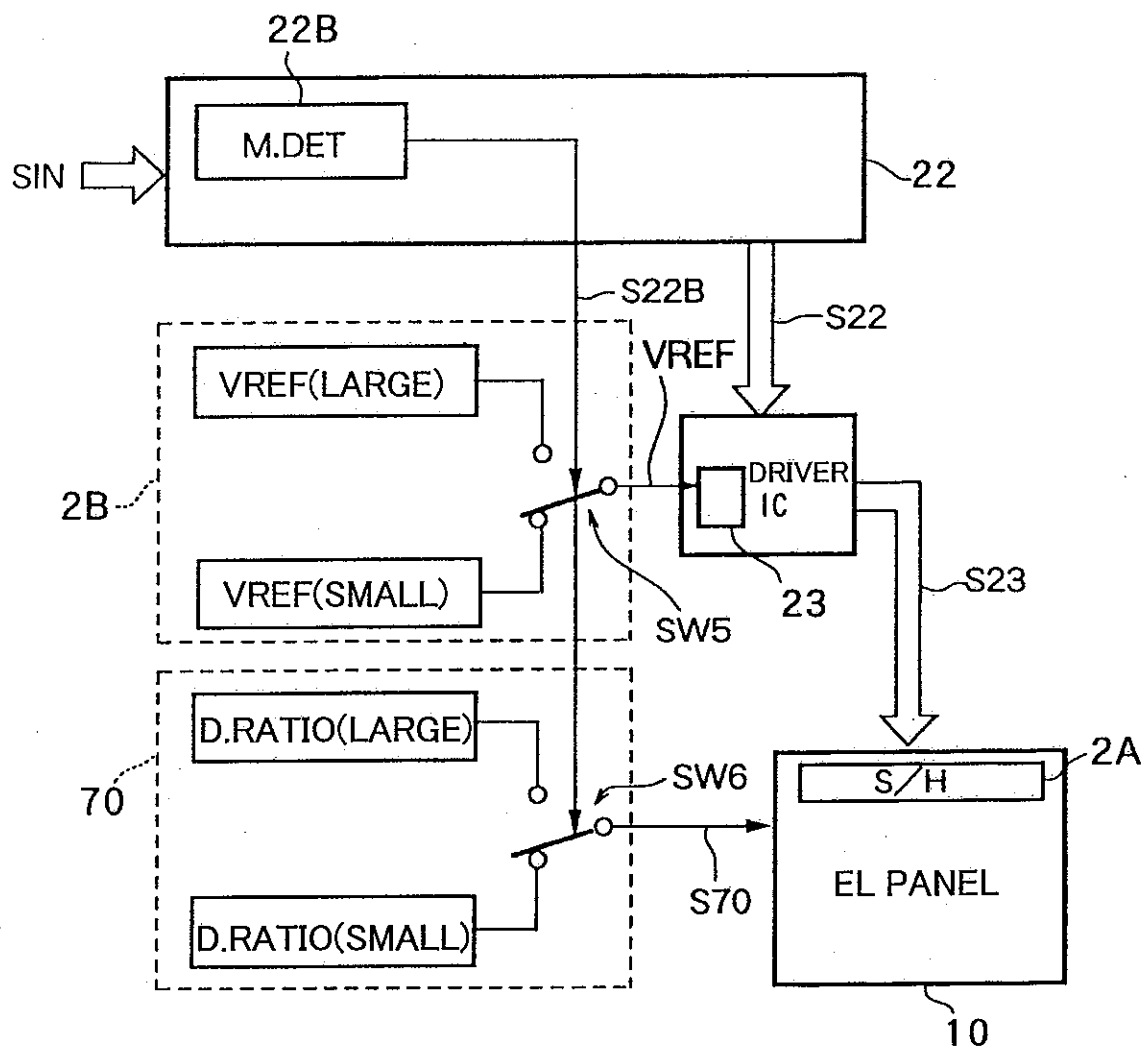
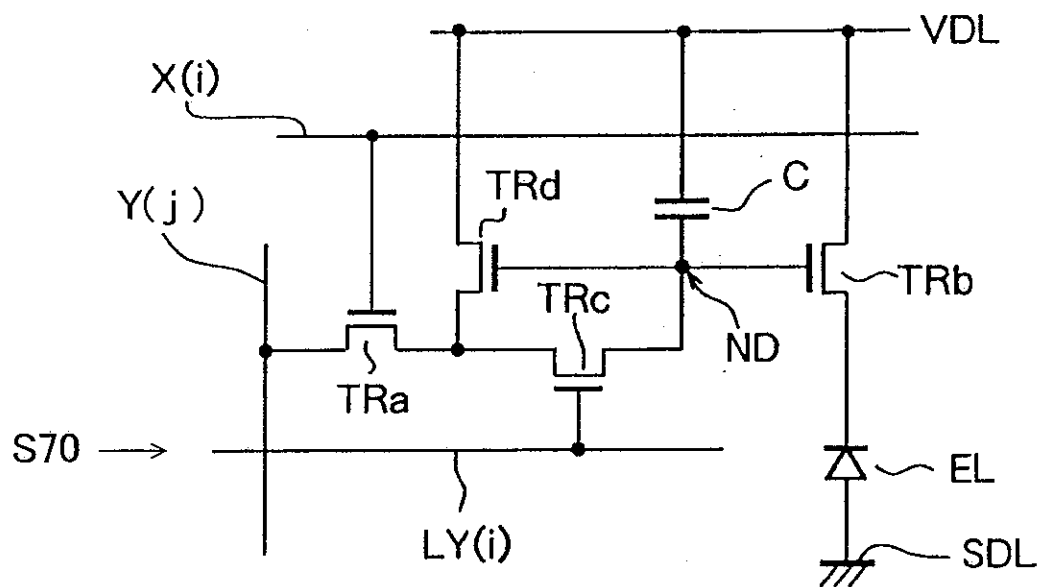


FIG. 19



Explanation of References

- 1: CELL ARRAY
- 1a: VALID SCREEN DISPLAY REGION
- 2: .CIRCUIT FOR GENERATING DRIVE SIGNAL FROM IMAGE SIGNAL
- 5 2A: SAMPLE HOLD CIRCUIT
- 2B: LEVEL ADJUSTMENT CIRCUIT
- 3: V-SCAN CIRCUIT
- 4: ADJUSTMENT INFORMATION RETRIEVE MEANS
- 10: ORGANIC EL PANEL
- 10 21: SIGNAL SENDING CIRCUIT
- 22: SIGNAL PROCESSING IC
- 22a: CPU
- 22B: MOTION DETECTION CIRCUIT
- 23, 40, 51: D/A CONVERTER
- 15 41, 50: ROM
- 60: PIXEL CURRENT DETECTION CIRCUIT
- 70: DUTY RATIO ADJUSTMENT CIRCUIT