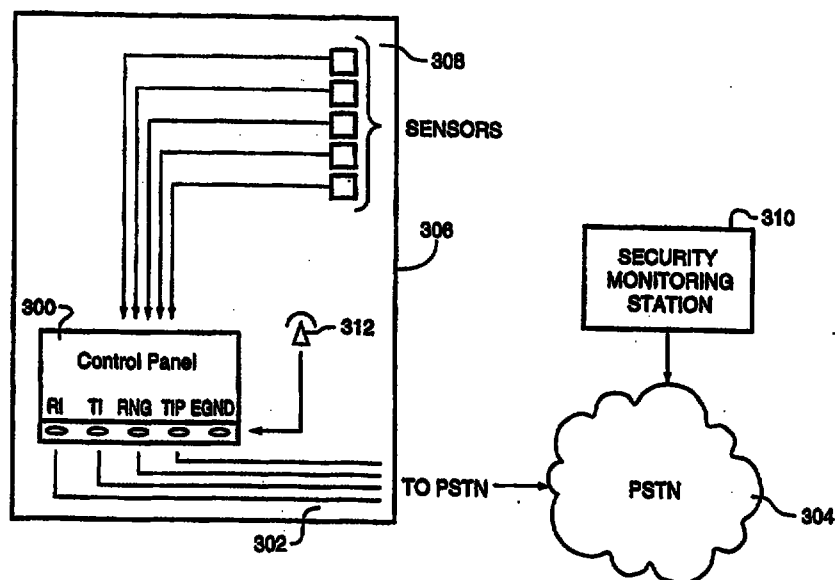




INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(51) International Patent Classification ⁶ : H04M 11/04, G08B 29/08	A1	(11) International Publication Number: WO 99/05846 (43) International Publication Date: 4 February 1999 (04.02.99)
(21) International Application Number: PCT/CA98/00708 (22) International Filing Date: 21 July 1998 (21.07.98) (30) Priority Data: 08/903,219 22 July 1997 (22.07.97) US (71) Applicant: DIGITAL SECURITY CONTROLS LTD. [CA/CA]; 1645 Flint Road, Downsview, Ontario M3J 2J6 (CA). (72) Inventor: PILDNER, Reinhart, Karl; 44 Madoc Drive, Brampton, Ontario L6V 1Z9 (CA). (74) Agents: HALL, Warren, S. et al.; Suite 301, 133 Richmond Street West, Toronto, Ontario M5H 2L7 (CA).		(81) Designated States: AL, AM, AT, AZ, BA, BB, BG, BR, BY, CH, CN, CU, CZ, DE, DK, EE, ES, FI, GB, GE, GH, GM, HR, HU, ID, IL, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MD, MG, MK, MN, MW, MX, NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM, TR, TT, UA, UG, UZ, VN, YU, ZW, ARIPO patent (GH, GM, KE, LS, MW, SD, SZ, UG, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GW, ML, MR, NE, SN, TD, TG). Published <i>With international search report.</i>

(54) Title: SECURITY SYSTEM ALARM PANEL



(57) Abstract

The invention provides an alarm panel connected to a telephone line. The alarm panel comprises a microprocessor connected to a circuit which operates in two states: passive and active. In the passive state, the alarm panel monitors the line for a ring signal and tests the integrity of the telephone line. In the active state, the alarm panel seizes the telephone line and provides full bi-directional communication with a receiver through the telephone line. The circuit is designed such that the individual subcircuits for each state operate independently of each other and can be modified to meet various regulatory requirements without affecting the other circuits. The circuit draws low current in the passive state. Finally, the circuit is separated from the microprocessor using only 2 or 3 optocouplers,