~~**What is claimed is:**~~

CLAIMS

1. A method for decoding video data, the method comprising:

determining a size of a non-zeroed out region of a transform unit~~, wherein the transform unit comprises N coefficient groups and each coefficient group comprises M coefficients, wherein M and N are integer values~~;

~~determining~~defining a regular bin count threshold for the transform unit ~~based on the size~~as a number of bins equal to 1.75 multiplied by a total number of coefficients in the non-zeroed out region of the transform unit;

context decoding syntax elements in groups of 4 for the transform unit until the ~~first number of~~ regular ~~coded bins~~ bin count threshold is reached, wherein:

the context decoded syntax elements comprise one or more significance flags~~,~~ and one or more parity level flags~~, and one or more first flags,~~

each of the one or more significance flags indicate if an absolute level for a corresponding coefficient is equal to zero,

each of the one or more parity level flags indicates if a corresponding coefficient has an absolute level that is even or odd,

~~each of the one or more first flags indicates if a corresponding coefficient has an absolute level that is greater than 2;~~

in response to the context decoded syntax elements reaching the ~~first number of~~ regular ~~coded bins~~bin count threshold, bypass decoding additional syntax elements of the transform unit;

determining values for a first set of coefficients of the transform unit based on the context decoded syntax elements; and

determining values for a second set of coefficients of the transform unit based on the additional syntax elements.

2. The method of claim 1, wherein ~~determining the regular bin count threshold for the transform unit based on the size of~~ the transform unit comprises ~~setting the regular bin count threshold for the transform unit equal to N*28.~~

~~3. The method of claim 1, wherein determining the regular bin count threshold for the transform unit based on the size of the transform unit comprises setting the regular bin count threshold for the transform unit equal to 1.75*N*M.~~

~~4. The method of claim 1, wherein determining the size of the transform unit comprises determining a size of a non-zeroed out region of the transform unit, and wherein determining the regular bin count threshold for the transform unit based on the size of the transform unit comprises setting the regular bin count threshold for the transform unit equal to 28 multiplied by a number of~~N coefficient groups ~~in the non-zeroed out region of the transform unit.~~

~~5. The method of claim 1, wherein determining the size of the transform unit comprises determining a size of a non-zeroed out region of the transform unit, and wherein determining the regular bin count threshold for the transform unit based on the size of the transform unit comprises setting the regular bin count threshold for the transform unit equal to 1.75 multiplied by a total number of~~and each coefficient group comprises M coefficients ~~in the non-zeroed out region of the transform unit.~~

~~6. The method of claim 1, wherein the transform unit comprises a chroma transform unit and determining the regular bin count threshold for the transform unit based on the size of the transform unit comprises setting the regular bin count threshold for the transform unit equal to N*7.~~

~~7. The method of claim 1~~, wherein M and N are integer values, wherein the transform unit comprises a luma transform unit and M equals 16.

~~8. The method of claim 1, wherein the transform unit comprises a chroma transform unit and M equals 4.~~

~~9~~3. The method of claim 1, wherein bypass decoding the additional syntax elements comprises decoding the additional syntax elements using one or both of Golomb-Rice coding or Exponential-Golomb coding.

~~10~~4. The method of claim 1, further comprising:

determining a decoded transform block based on the values for the first set of coefficients and the values for the second set of coefficients;

adding the decoded transform block to a prediction block to determine a reconstructed block;

performing one or more filtering operations on the reconstructed block to determine a decoded block of video data; and

outputting a decoded picture of video data that includes the decoded block of video data.

~~11~~5. A method for encoding video data, the method comprising:

determining a size of a non-zeroed out region of a transform unit~~, wherein the transform unit comprises N coefficient groups and each coefficient group comprises M coefficients, wherein M and N are integer values~~;

~~determining~~defining a regular bin count threshold for the transform unit ~~based on the size~~as a number of bins equal to 1.75 multiplied by a total number of coefficients in the non-zeroed out region of the transform unit;

context encoding syntax elements in groups of 4 for the transform unit until the ~~first number of~~ regular ~~coded bins~~ bin count threshold is reached, wherein:

the context encoded syntax elements comprises one or more significance flags~~,~~ and one or more parity level flags, ~~and one or more first flags,~~

each of the one or more significance flags indicate if an absolute level for a corresponding coefficient is equal to zero,

each of the one or more parity level flags indicates if a corresponding coefficient has an absolute level that is even or odd, and

~~each of the one or more first flags indicates if a corresponding coefficient has an absolute level that is greater than 2, and~~

the context encoded syntax elements represent values for a first set of coefficients of the transform unit; and

in response to the context encoded syntax elements reaching the ~~first number of~~ regular ~~coded bins~~bin count threshold, bypass encoding additional syntax elements for the transform unit, wherein the additional syntax elements represent values for a second set of coefficients of the transform unit.

~~12~~6. The method of claim ~~11~~5, wherein ~~determining the regular bin count threshold for the transform unit based on the size of t~~he transform unit comprises ~~setting the regular bin count threshold for the transform unit equal to N*28.~~

~~13. The method of claim 11, wherein determining the regular bin count threshold for the transform unit based on the size of the transform unit~~N coefficient groups and each coefficient group comprises ~~setting the regular bin count threshold for the transform unit equal to 1.75 multiplied by a total number of~~ M coefficients ~~in the transform unit.~~

~~14. The method of claim 11, wherein determining the size of the transform unit comprises determining a size of a non-zeroed out region of the transform unit, and wherein determining the regular bin count threshold for the transform unit based on the size of the transform unit comprises setting the regular bin count threshold for the transform unit equal to 28 multiplied by a number of coefficient groups in the non-zeroed out region of the transform unit.~~

~~15. The method of claim 11, wherein determining the size of the transform unit comprises determining a size of a non-zeroed out region of the transform unit, ~~, wherein M and ~~wherein determining the regular bin count threshold for the transform unit based on the size of the transform unit comprises setting the regular bin count threshold for the transform unit equal to 1.75 multiplied by a total number of coefficients in the non-zeroed out region of the transform unit.~~

~~16. The method of claim 11, wherein the transform unit comprises a chroma transform unit and determining the regular bin count threshold for the transform unit based on the size of the transform unit comprises setting the regular bin count threshold for the transform unit equal to N*~~7.~~

~~17. The method of claim 11~~ are integer values, wherein the transform unit comprises a luma transform unit and M equals 16.

~~18~~7. The method of claim ~~11, wherein the transform unit comprises a chroma transform unit and M equals 4.~~

~~19. The method of claim 11~~5, wherein bypass encoding the additional syntax elements comprises encoding the additional syntax elements using one or both of Golomb-Rice coding or Exponential-Golomb coding.

~~20~~8. A device for decoding video data, the device comprising:

a memory configured to store video data; and

one or more processors implemented in circuitry and configured to:

determine a size of a non-zeroed out region of a transform unit ~~of the video data, wherein the transform unit comprises N coefficient groups and each coefficient group comprises M coefficients, wherein M and N are integer values~~;

~~determine~~ define a regular bin count threshold for the transform unit ~~based on the size~~as a number of bins equal to 1.75 multiplied by a total number of coefficients in the non-zeroed out region of the transform unit;

context decode syntax elements in groups of 4 for the transform unit until the ~~first number of~~ regular ~~coded bins~~ bin count threshold is reached, wherein:

the context decoded syntax elements comprise one or more significance flags~~,~~ and one or more parity level flags, ~~and one or more first flags,~~

each of the one or more significance flags indicate if an absolute level for a corresponding coefficient is equal to zero, and

each of the one or more parity level flags indicates if a corresponding coefficient has an absolute level that is even or odd, ~~and~~

~~each of the one or more first flags indicates if a corresponding coefficient has an absolute level that is greater than 2;~~

in response to the context decoded syntax elements reaching the ~~first number of~~ regular ~~coded bins~~bin count threshold, bypass decode additional syntax elements of the transform unit;

determine values for a first set of coefficients of the transform unit based on the context decoded syntax elements; and

determine values for a second set of coefficients of the transform unit based on the additional syntax elements.

~~21~~9. The device of claim ~~20, wherein to determine the regular bin count threshold for the transform unit based on the size of the transform unit, the one or more processors are configured to set the regular bin count threshold for the transform unit equal to N*28.~~

~~22. The device of claim 20, wherein to determine the regular bin count threshold for the transform unit based on the size of the transform unit, the one or more processors are configured to set the regular bin count threshold for the transform unit equal to 1.75*N*M.~~

~~23. The device of claim 20, wherein to determine the size of the transform unit the one or more processors are configured to determine a size of a non-zeroed out region of the transform unit, and wherein to determine the regular bin count threshold for the transform unit based on the size of the transform unit, the one or more processors are configured to set the regular bin count threshold for the transform unit equal to 28 multiplied by a number of coefficient groups in the non-zeroed out region of the transform unit.~~

~~24. The device of claim 20, wherein to determine the size of the transform unit, the one or more processors are configured to determine a size of a non-zeroed out region of the transform unit, and wherein to determine the regular bin count threshold for the transform unit based on the size of the transform unit, the one or more processors are configured to set the regular bin count threshold for the transform unit equal to 1.75 multiplied by a total number of coefficients in the non-zeroed out region of the transform unit.~~

~~25. The device of claim 20~~8, wherein the transform unit comprises ~~a chroma transform unit and wherein to determine the regular bin count threshold for the transform unit based on the size of the transform unit, the one or more processors are configured to set the regular bin count threshold for the transform unit equal to N*7.~~

~~26. The device of claim 20~~N coefficient groups and each coefficient group comprises M coefficients, wherein M and N are integer values, wherein the transform unit comprises a luma transform unit and M equals 16.

~~27~~10. The device of claim ~~20, wherein the transform unit comprises a chroma transform unit and M equals 4.~~

~~28. The device of claim 20~~8, wherein to bypass decode the additional syntax elements, the one or more processors are configured to decode the additional syntax elements using one or both of Golomb-Rice coding or Exponential-Golomb coding.

~~29~~11. The device of claim ~~20~~8, wherein the one or more processors are further configured to:

determine a decoded transform block based on the values for the first set of coefficients and the values for the second set of coefficients;

add the decoded transform block to a prediction block to determine a reconstructed block;

perform one or more filtering operations on the reconstructed block to determine a decoded block of video data; and

output a decoded picture of video data that includes the decoded block of video data.

~~30~~12. A device for encoding video data, the device comprising:

a memory configured to store video data; and

one or more processors implemented in circuitry and configured to:

determine a size of a non-zeroed out region of a transform unit~~, wherein the transform unit comprises N coefficient groups and each coefficient group comprises M coefficients, wherein M and N are integer values~~;

~~determine~~define a regular bin count threshold for the transform unit ~~based on the size~~as a number of bins equal to 1.75 multiplied by a total number of coefficients in the non-zeroed out region of the transform unit;

context encode syntax elements in groups of 4 for the transform unit until the ~~first number of~~ regular ~~coded bins~~ bin count threshold is reached, wherein:

the context encoded syntax elements comprises one or more significance flags~~,~~ and one or more parity level flags, ~~and one or more first flags,~~

each of the one or more significance flags indicate if an absolute level for a corresponding coefficient is equal to zero,

each of the one or more parity level flags indicates if a corresponding coefficient has an absolute level that is even or odd, and

~~each of the one or more first flags indicates if a corresponding coefficient has an absolute level that is greater than 2, and~~

the context encoded syntax elements represent values for a first set of coefficients of the transform unit; and

in response to the context encoded syntax elements reaching the ~~first number of~~ regular ~~coded bins~~bin count threshold, bypass encode additional syntax elements for the transform unit, wherein the additional syntax elements represent values for a second set of coefficients of the transform unit.

~~31~~13. The device of claim ~~30, wherein to determine the regular bin count threshold for the transform unit based on the size of the transform unit, the one or more processors are configured to set the regular bin count threshold for the transform unit equal to N*28.~~

~~32. The device of claim 30, wherein to determine the regular bin count threshold for the transform unit based on the size of the transform unit, the one or more processors are configured to set the regular bin count threshold for the transform unit equal to 1.75 multiplied by a total number of coefficients in the transform unit.~~

~~33. The device of claim 30, wherein to determine the size of the transform unit, the one or more processors are configured to determine a size of a non-zeroed out region of the transform unit, and wherein to determine the regular bin count threshold for the transform unit based on the size of the transform unit, the one or more processors are configured to set the regular bin count threshold for the transform unit equal to 28 multiplied by a number of coefficient groups in the non-zeroed out region of the transform unit.~~

~~34. The device of claim 30, wherein to determine the size of the transform unit, the one or more processors are configured to determine a size of a non-zeroed out region of the transform unit, and wherein to determine the regular bin count threshold for the transform unit based on the size of the transform unit, the one or more processors are configured to set the regular bin count threshold for the transform unit equal to 1.75 multiplied by a total number of coefficients in the non-zeroed out region of the transform unit.~~

~~35. The device of claim 30~~12, wherein the transform unit comprises ~~a chroma transform unit and to determine the regular bin count threshold for the transform unit based on the size of the transform unit, the one or more processors are configured to set the regular bin count threshold for the transform unit equal to N*7.~~

~~36. The device of claim 30~~N coefficient groups and each coefficient group comprises M coefficients, wherein M and N are integer values, wherein the transform unit comprises a luma transform unit and M equals 16.

~~37~~14. The device of claim ~~30, wherein the transform unit comprises a chroma transform unit and M equals 4.~~

~~38. The device of claim 30~~12, wherein to bypass encode the additional syntax elements, the one or more processors are configured to encode the additional syntax elements using one or both of Golomb-Rice coding or Exponential-Golomb coding.