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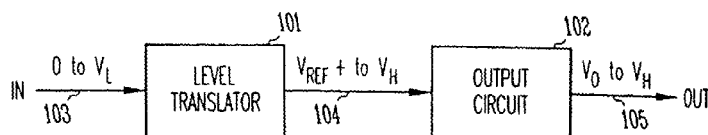
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(54) Title: HIGH VOLTAGE CMOS OUTPUT DRIVER IN LOW VOLTAGE PROCESS



(57) Abstract: A high-voltage output buffer is implemented in a low-voltage semiconductor process. The buffer comprises a level translator circuit, the level translator operable to receive a signal varying between ground and a low voltage level, and to output a corresponding signal varying between a reference voltage level and a high voltage level. The reference voltage level is an intermediate voltage level between half of the low voltage level and the high voltage level. The buffer further comprises an output circuit operable to receive via an input the output of the level translator circuit, and to output a high voltage level when the input is a high voltage level or a zero voltage level when the input is at the reference voltage level.



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