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SUBSTITUTE ABSTRACT

-98-

Abstract of the Disclosure

A method and apparatus for the analysis of a computer process is disclosed. The method consists of the steps of analyzing a target process and constructing a model and repository which are representative of the control and non-control components contained within the original target process respectively. The resultant model and repository are executed, and their execution is representative of the original target process. Structures created during the execution of the model and repository assist a user to identify failures which occur during the execution, and then locate the fault(s) responsible for the failures as well as places within the target source where modifications due to new requirements are needed. An alternative form of object code is proposed which consists of two components, control and non-control components. Previously described model representing the control component constantly increases the intelligence of the resulting object code in respect to the target process being executed through the processes of propagation of elementary knowledge about the correctness or uncertainty of the target process elements execution.

This Sheet Has Been Replaced



REPLACED SHEET