

METHOD AND APPARATUS FOR INTEGRATING A DEDICATED SELECTIVE CALL DECODER IN A CONTROLLER

Abstract of the Disclosure

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A memory 220 comprising address register 305, control register 310, status register 315, message register 320, and receive address register information register 325, are coupled to a decoder 240 and a microcontroller 250 via a parallel bus 235 and 230. The microcontroller 250
10 controlling the operation of the decoder 240 to receive and decode a selective call signal from the receiver circuitry 102, the microcontroller 250 communicating with the decoder 240 by storing and retrieving information in the registers in the memory 220. The decoder 240 communicating with the microcontroller 240 by storing and retrieving information in the registers
15 in the memory 220.

Figure 2 accompanies this abstract.