

AbstractMemory device which comprises a multi-layer capacitor

- 5 A memory device comprising memory cells having at least three stacked electrodes 33, 34, 35, spaced apart pairwise by dielectric material 37, so that the pairs of electrodes form respective capacitor layers. The capacitors are connected electrically in parallel to each other. The dielectric material is optionally ferroelectric material, in which case the capacitors are ferrocapacitors.

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[Fig 3]



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