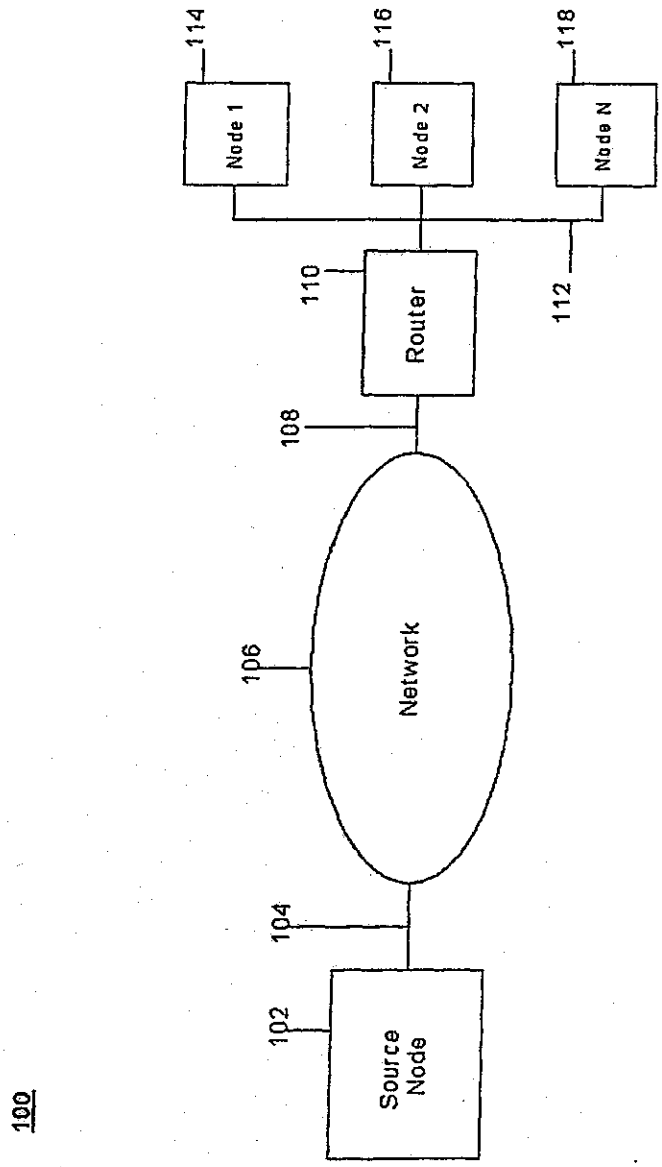

100
Source Node
102
104
Network
106
108
Router
110
112
Node 1
114
Node 2
116
Node N
118

FIG. 1

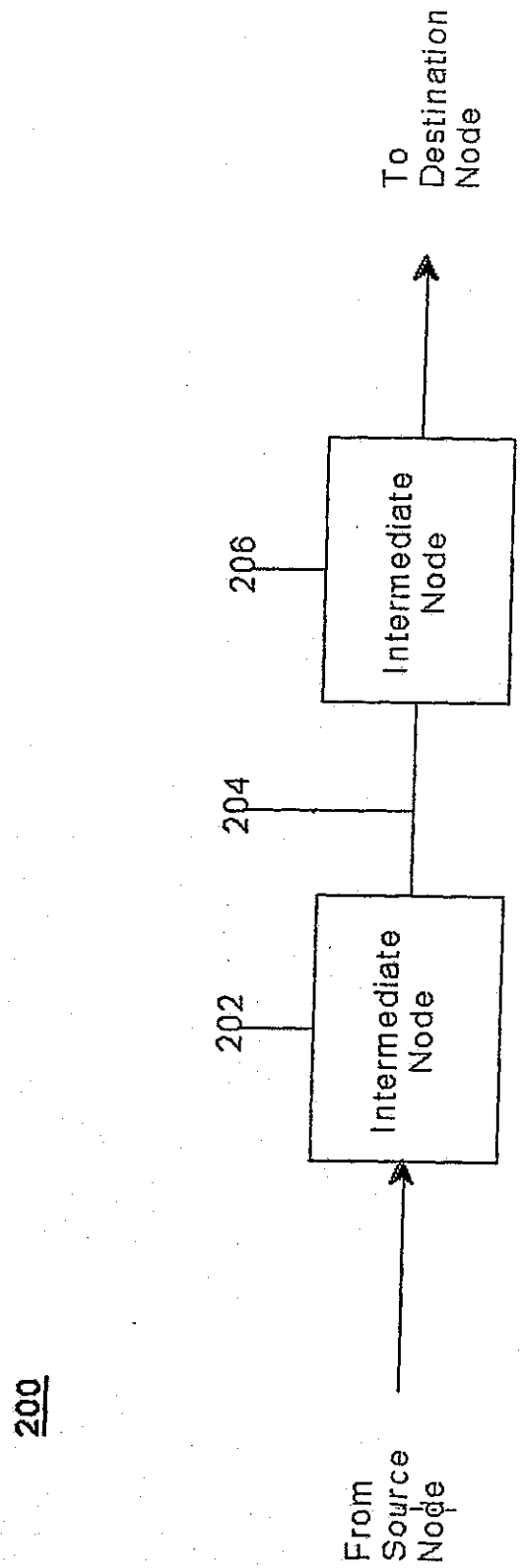

200
From Source Node
202
Intermediate Node
204
206
Intermediate Node
To Destination Node

FIG. 2

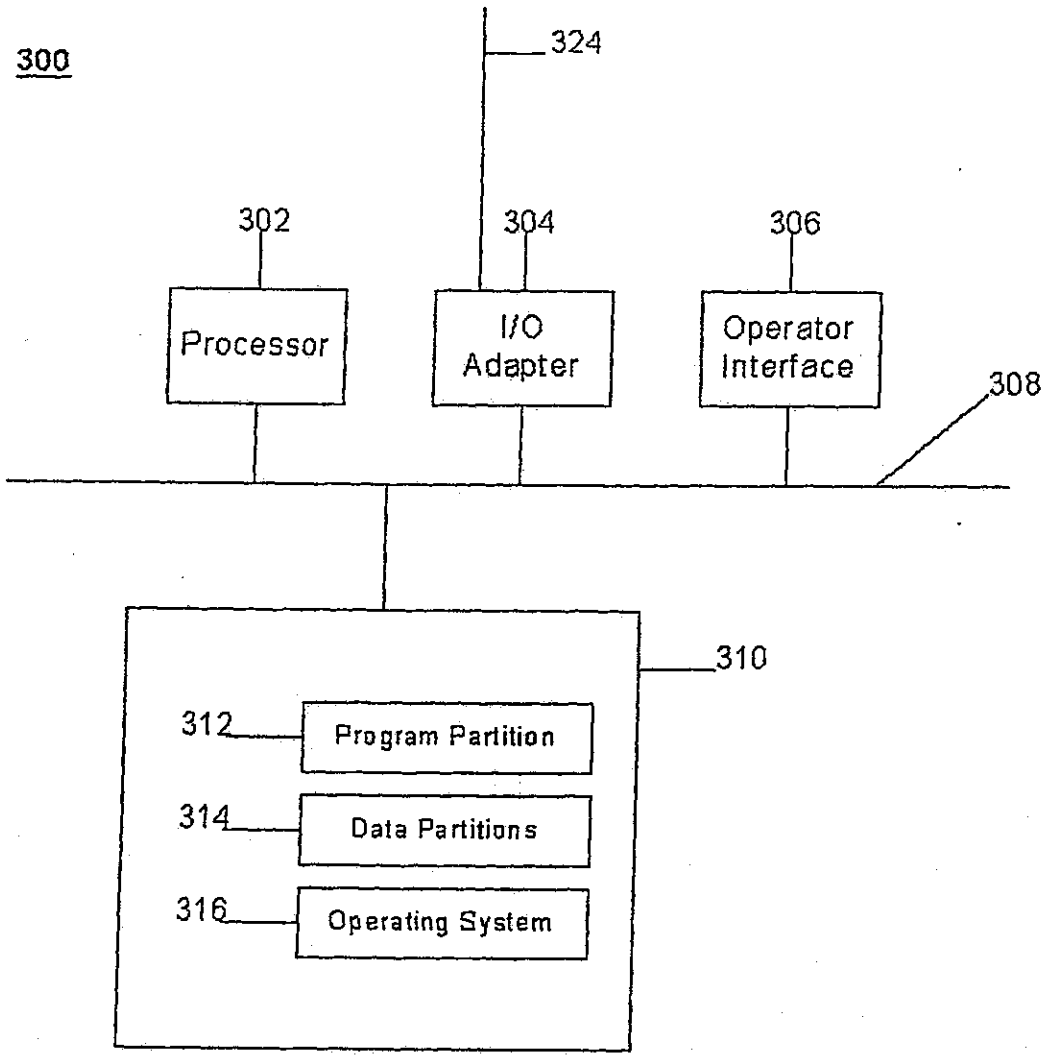

300
324
302
304
306
Processor
I/O Adapter
Operator Interface
308
310
312
Program Partition
314
Data Partitions
316
Operating System

FIG. 3

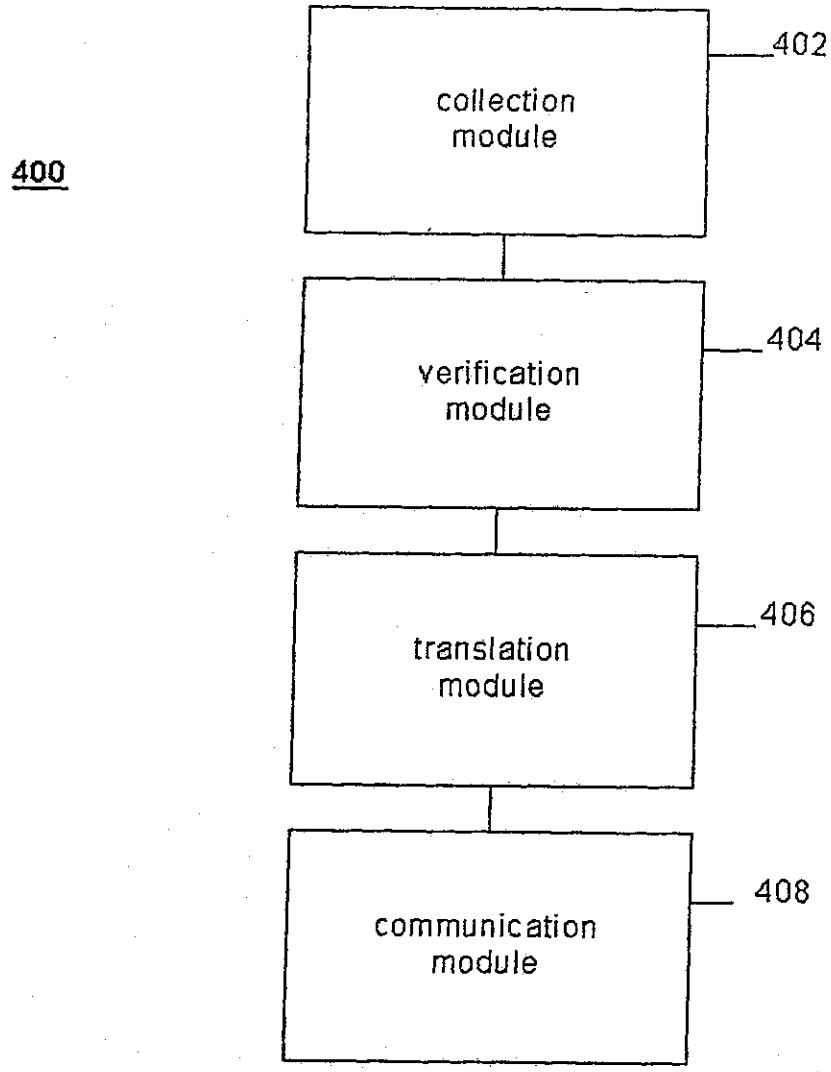

400
collection module
402
verification module
404
translation module
406
communication module
408

FIG. 4

500

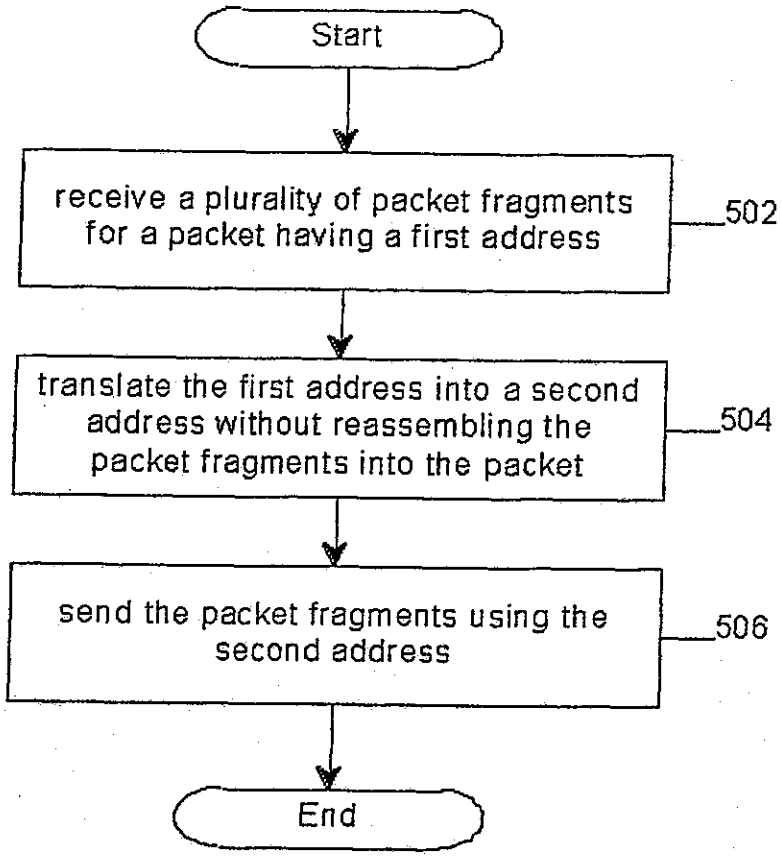

Start
receive a plurality of packet fragments for a packet having a first address
502
translate the first address into a second address without reassembling the packet fragments into the packet
504
send the packet fragments using the second address
506
End

FIG. 5

600

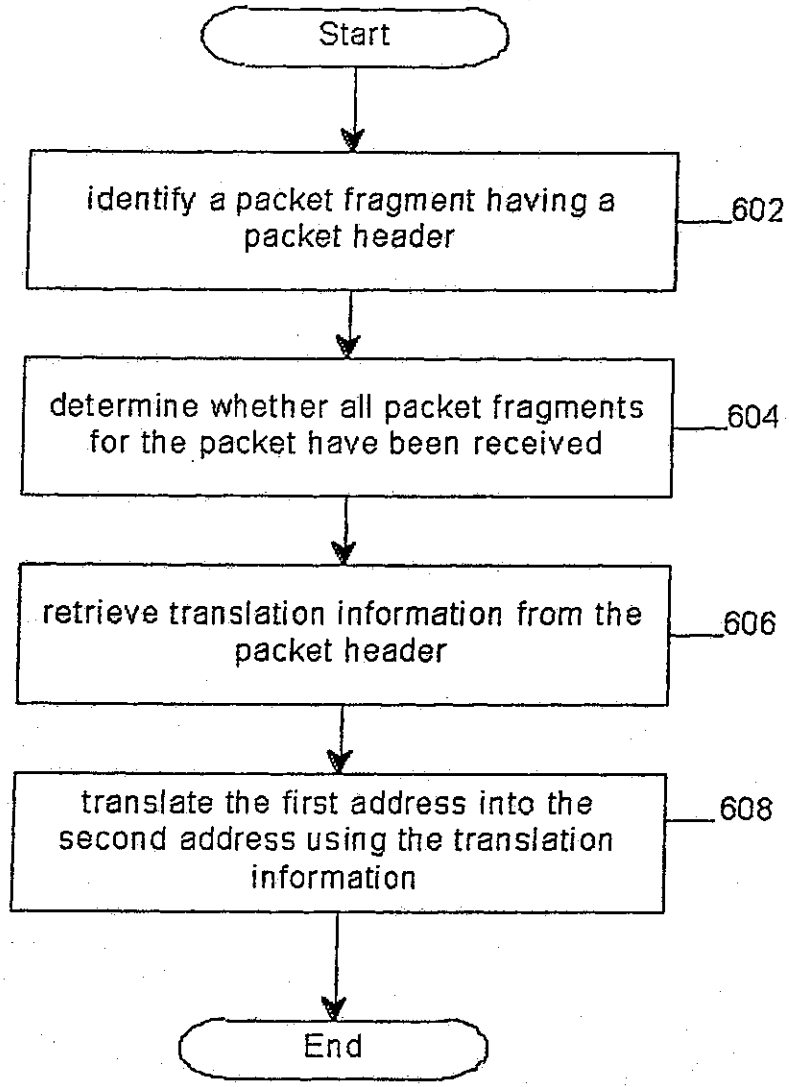

Start
identify a packet fragment having a packet header
602
determine whether all packet fragments for the packet have been received
604
retrieve translation information from the packet header
606
translate the first address into the second address using the translation information
608
End

FIG. 6

700

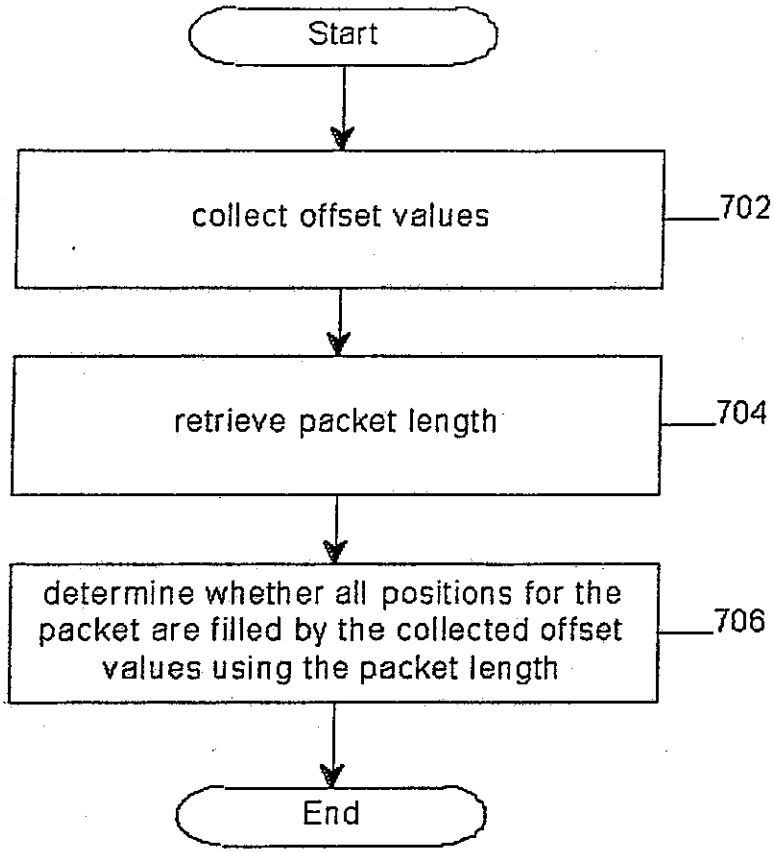

Start
collect offset values
702
retrieve packet length
704
determine whether all positions for the packet are filled by the collected offset values using the packet length
706
End

FIG. 7