

A B S T R A C T

A substrate (10) includes a valid area (6) and a
invalid area (7) located outside the valid area, the
valid area providing an array substrate, and a colored
5 layers (24R) is formed in the valid area and the
invalid area. A conductive film is formed on the
colored layers in the valid area (6) for forming a
plurality of pixel electrodes (26), and the conductive
film (26a) is simultaneously is formed on the colored
10 layer (24R) in the invalid area (7). The substrate
(10) with the conductive film formed thereon is divided
along the periphery (e) of the valid area (6), thereby
cutting the array substrate.



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