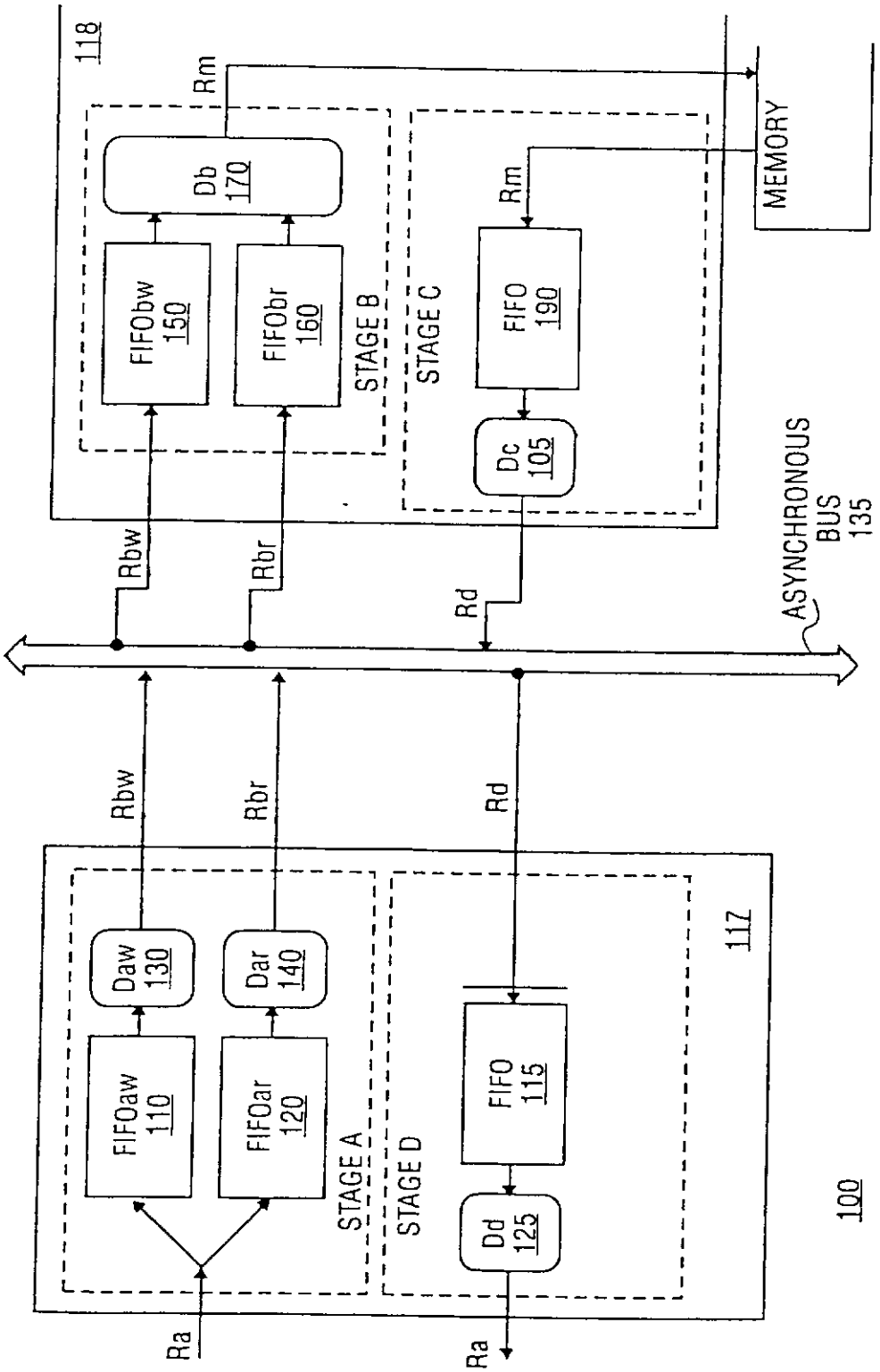

100
117
118
Ra
Ra
FIFOaw 110
FIFOar 120
Daw 130
Dar 140
STAGE A
STAGE D
FIFO 115
Dd 125
Rbw
Rbr
Rd
FIFObw 150
FIFObr 160
Db 170
STAGE B
STAGE C
FIFO 190
Dc 105
Rm
MEMORY
ASYNCHRONOUS BUS 135

FIG. 1

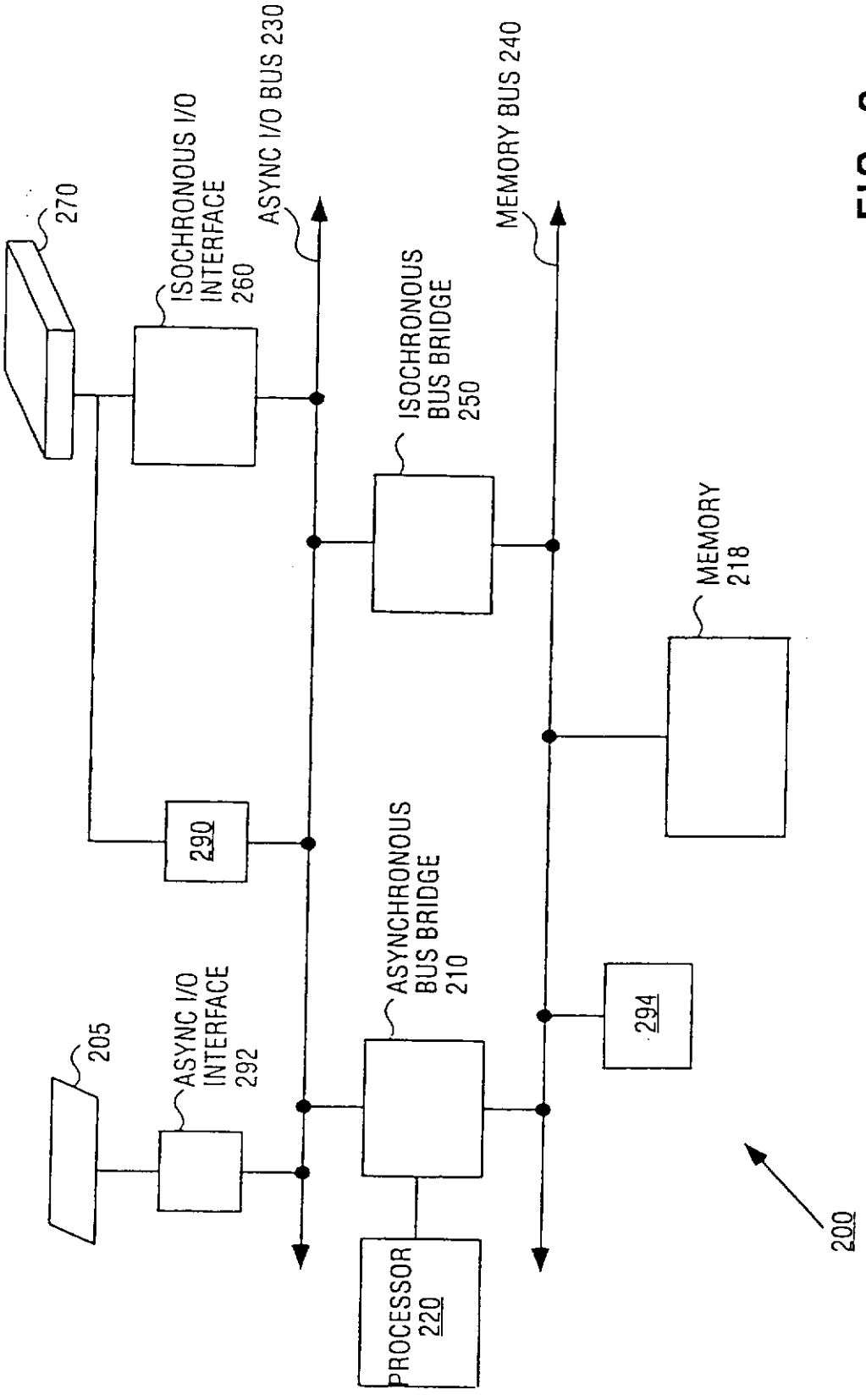

270
ISOCHRONOUS I/O INTERFACE 260
ASYNC I/O BUS 230
MEMORY BUS 240
ISOCHRONOUS BUS BRIDGE 250
MEMORY 218
290
ASYNCHRONOUS BUS BRIDGE 210
294
205
ASYNC I/O INTERFACE 292
PROCESSOR 220
200

FIG. 2