

OSCILLATION PERIOD DETECTION CIRCUIT AND METHOD, AND SEMICONDUCTOR MEMORY

ABSTRACT

The embodiments of the present disclosure provide an oscillation period detection circuit and method, and semiconductor memory. The oscillation period detection circuit includes an oscillator module, a control module, and a counting module. The oscillator module includes a target oscillator, and is configured to receive an enable signal and control the target oscillator to output an oscillation clock signal according to the enable signal; the control module is configured to receive the enable signal and the oscillation clock signal, and perform valid time reforming processing according to the oscillation clock signal and the enable signal to determine a target time; the counting module is configured to receive the enable signal and the oscillation clock signal, and perform period counting processing according to the enable signal and the oscillation clock signal to determine a target period number. The oscillation period of the target oscillator is calculated according to the target time and the target period number.

[Fig. 1]