ABSTRACT

A TEACHING APPARATUS FOR SIMULATING AN EXECUTION OF A COMPUTER PROGRAM

The present invention discloses a teaching apparatus for simulating an execution of a computer program, the apparatus comprising a plurality of function blocks (200, 300, 400, 500), each of the function block (200, 300, 400, 500) having a receptacle (201) extending therethrough that when combined with a corresponding function block (200, 300, 400, 500), the receptacles (201) form a track that simulates an execution of the computer program; and a mobile unit (100) configured to navigate along the track of the combined function blocks (200, 300, 400, 500); wherein the function blocks (200, 300, 400, 500) include decision blocks (500) which are each configured with assigned recesses (501a, 501b) to receive one or more parameter blocks (600), such that the decision blocks (500) are equipped with movable obstructions in the receptacles (201) that are actuatable to allow passage of the mobile unit (100) in response to the receipt of the parameter blocks (600).

(*The most illustrative figure:* ***FIG. 1***)