

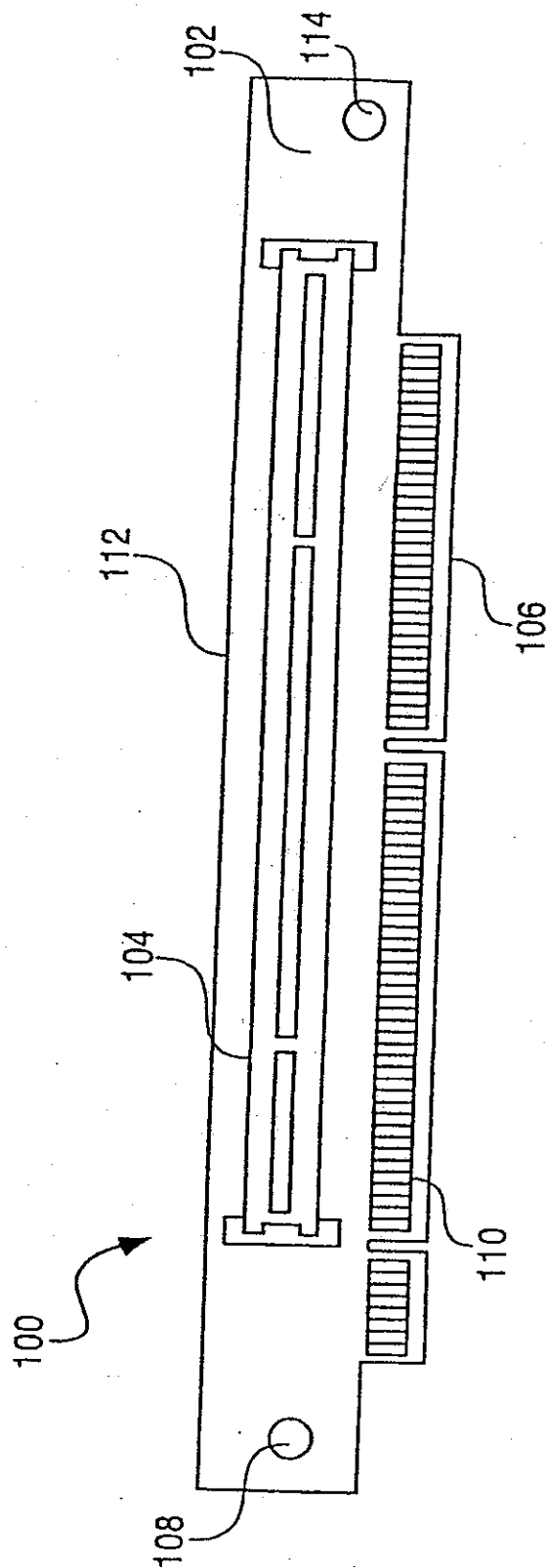


FIG. 1

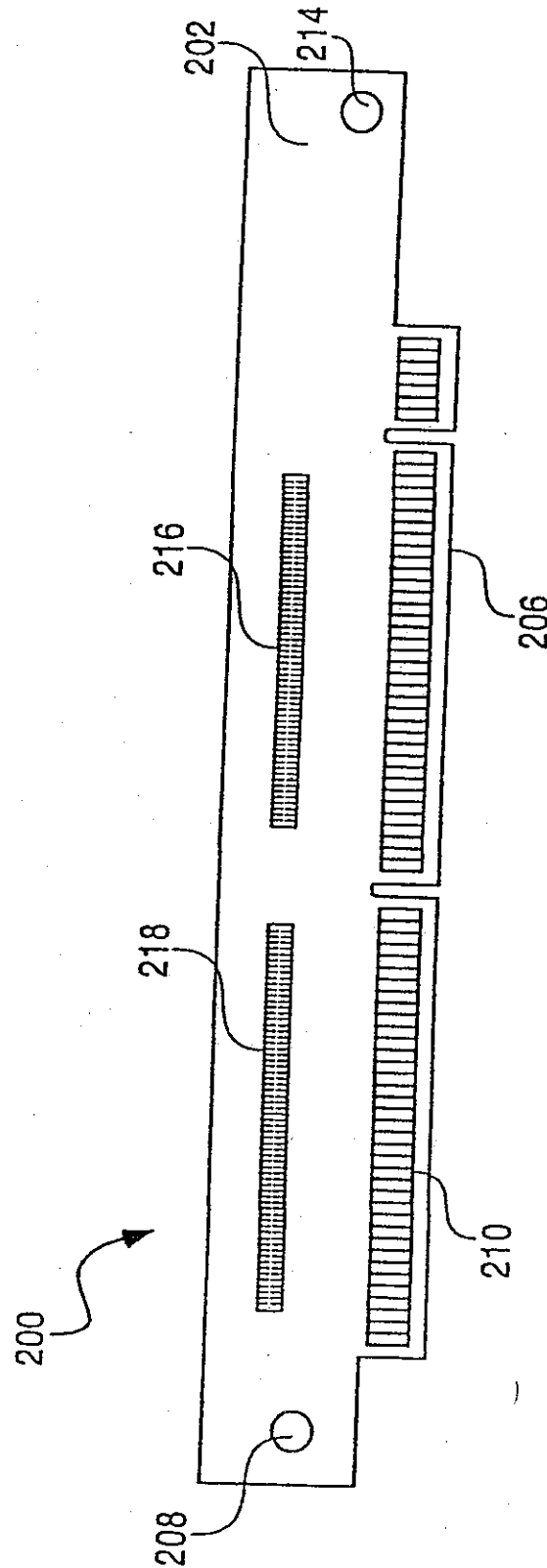


FIG. 2

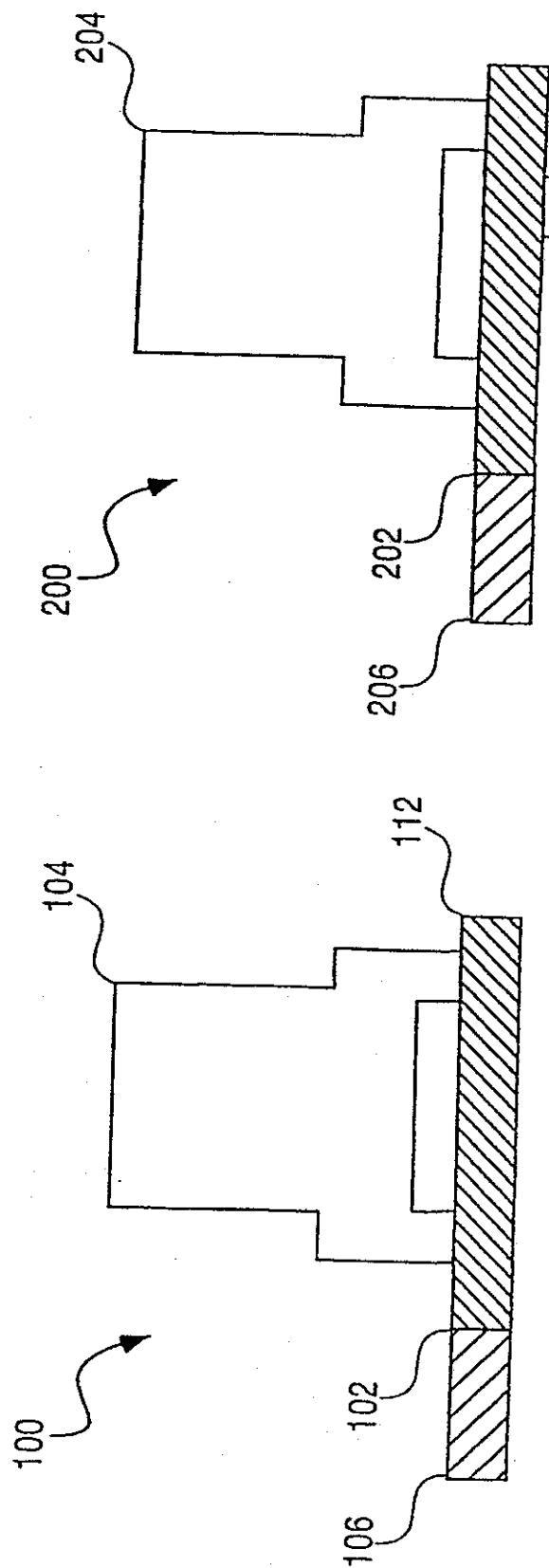


FIG. 3A

FIG. 3B

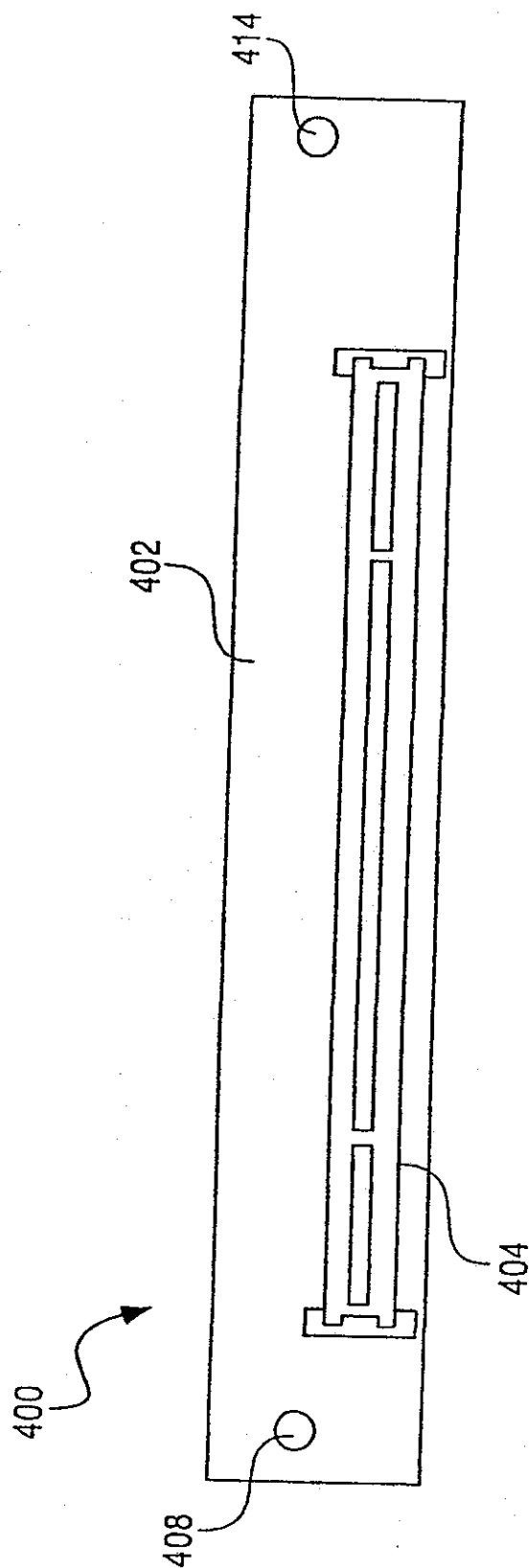


FIG. 4

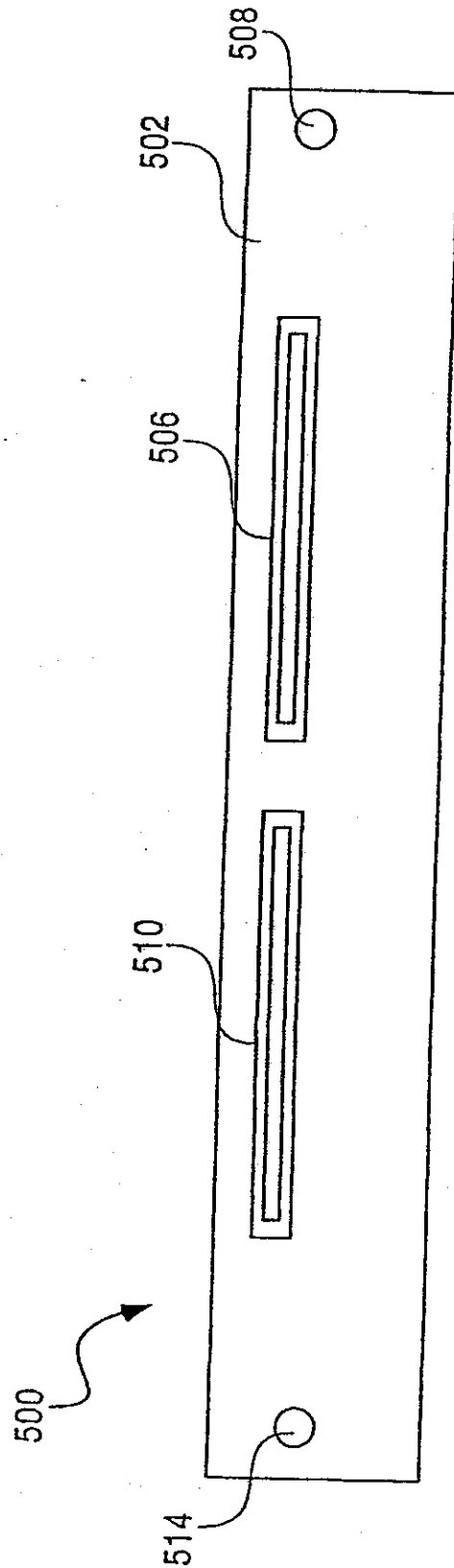


FIG. 5

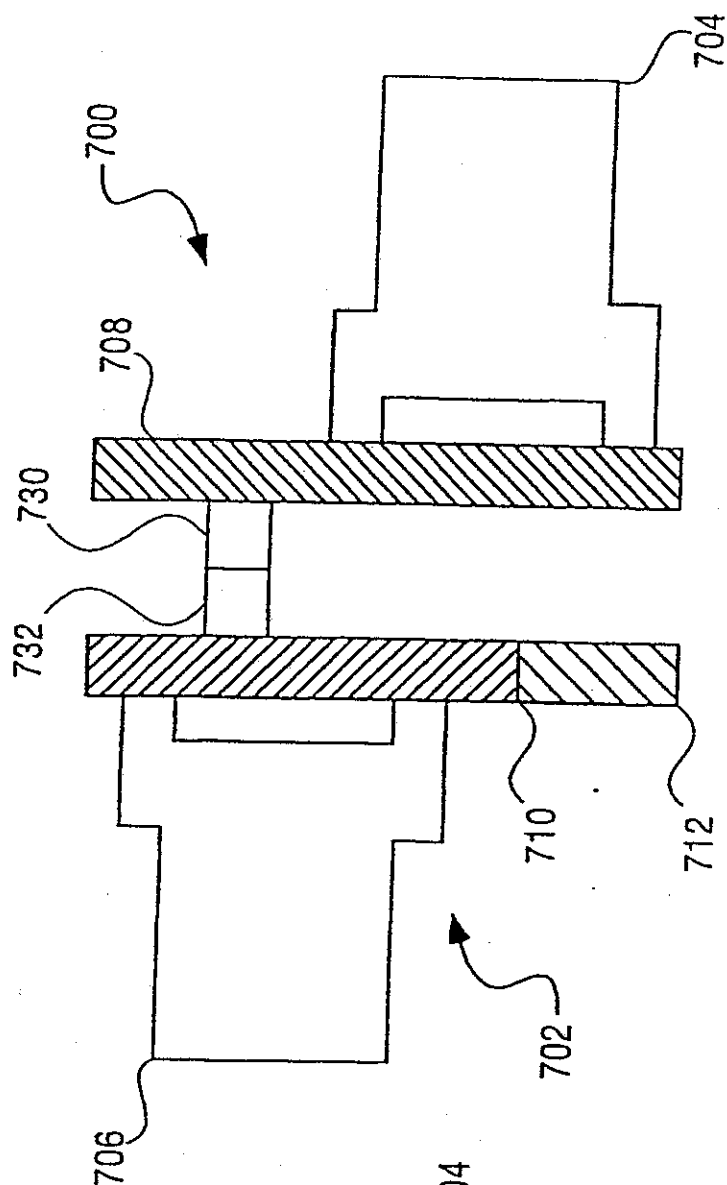


FIG. 7A

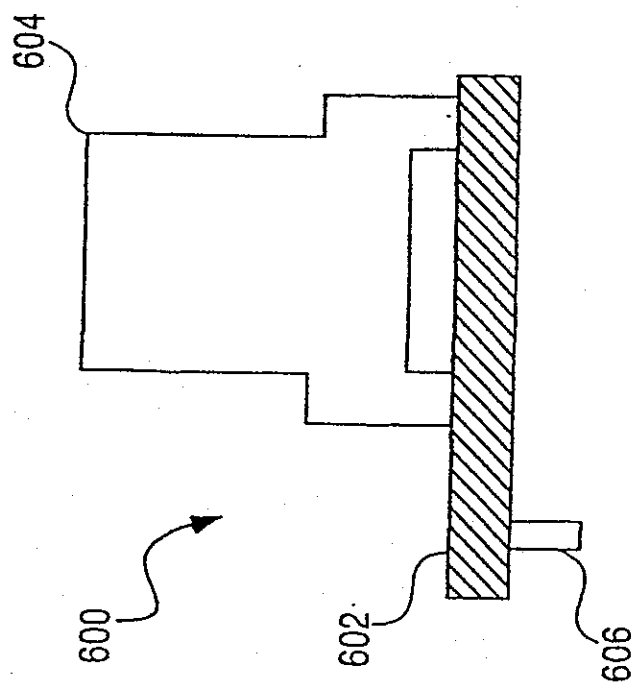


FIG. 6

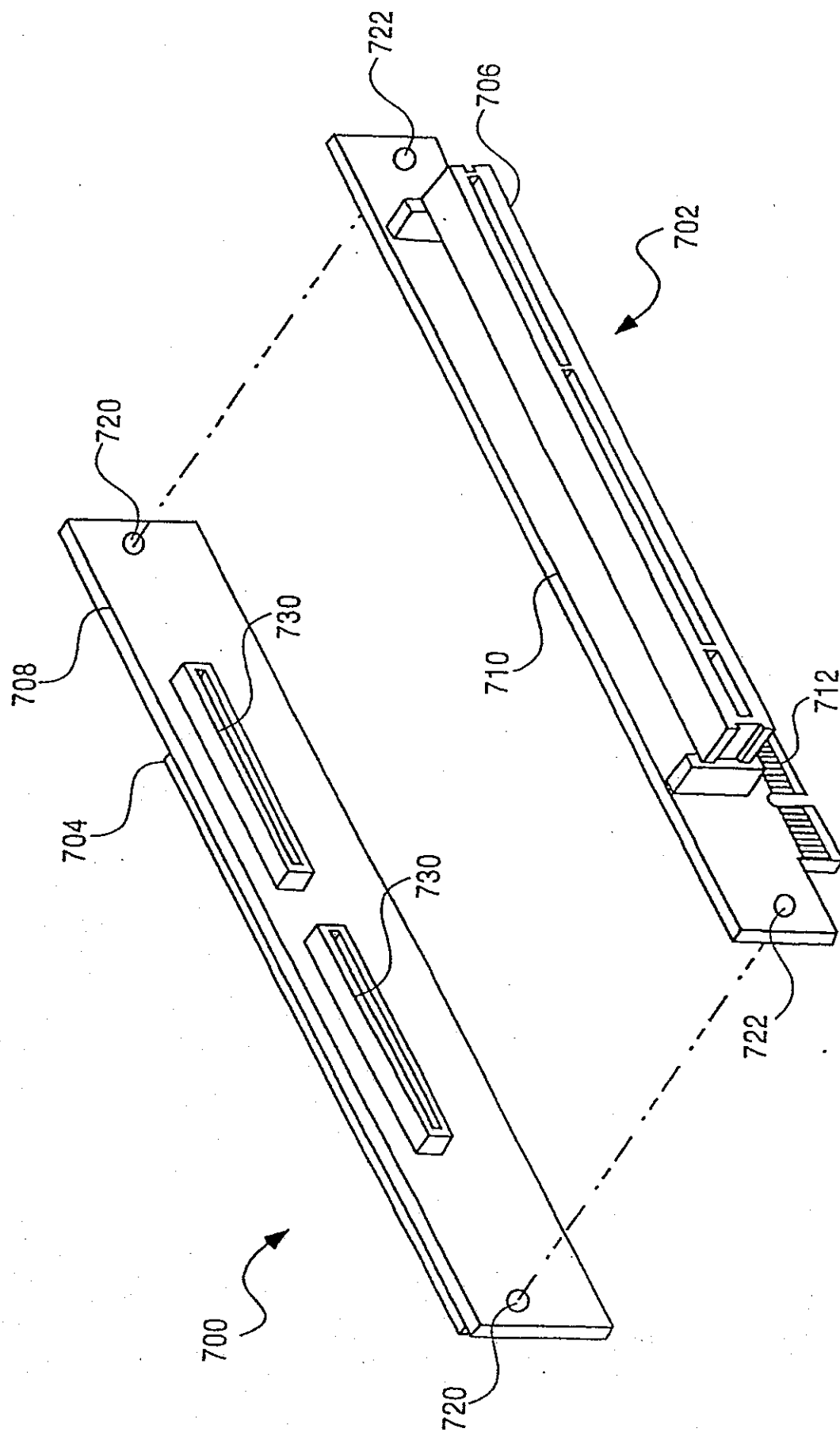


FIG. 7B

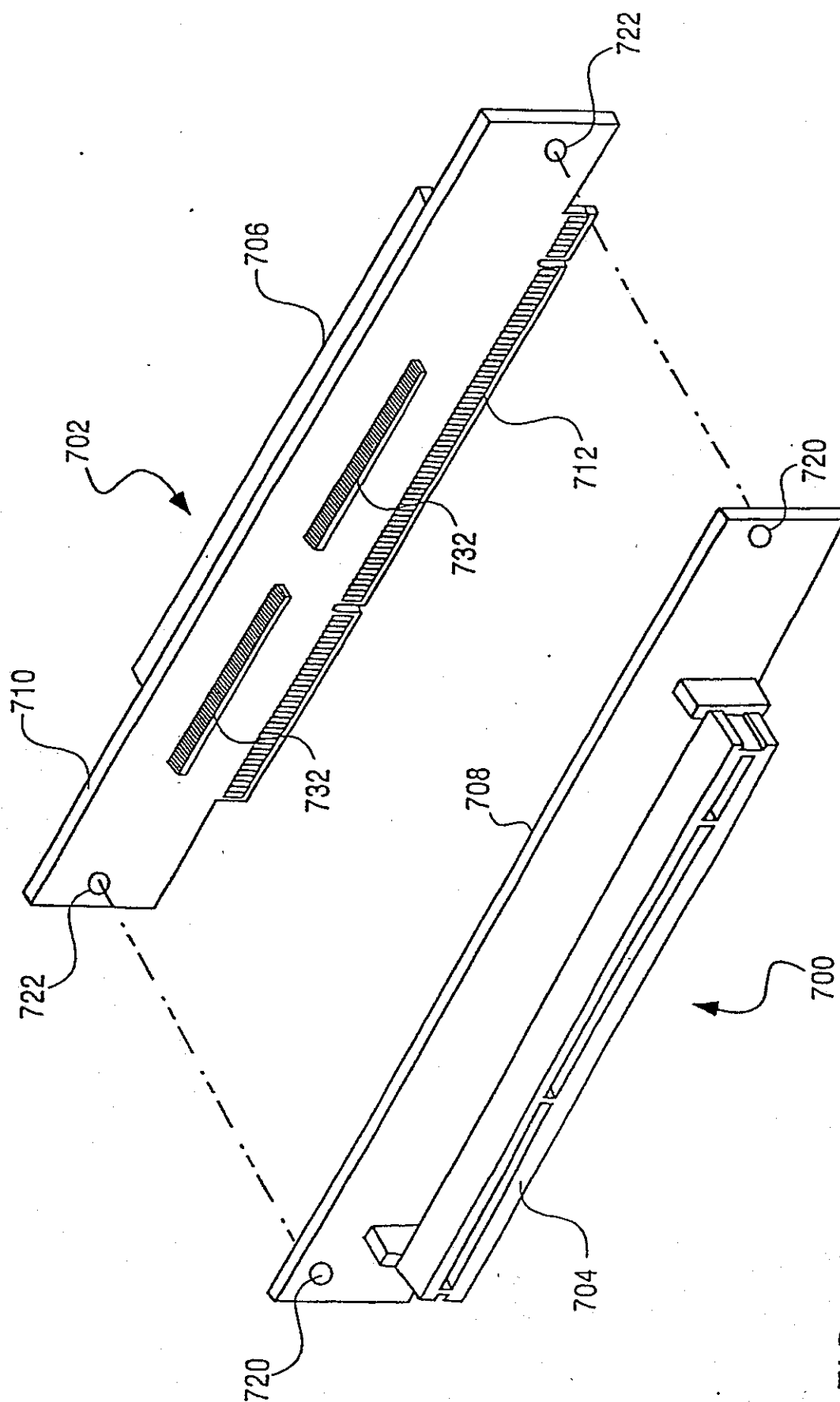


FIG. 7C

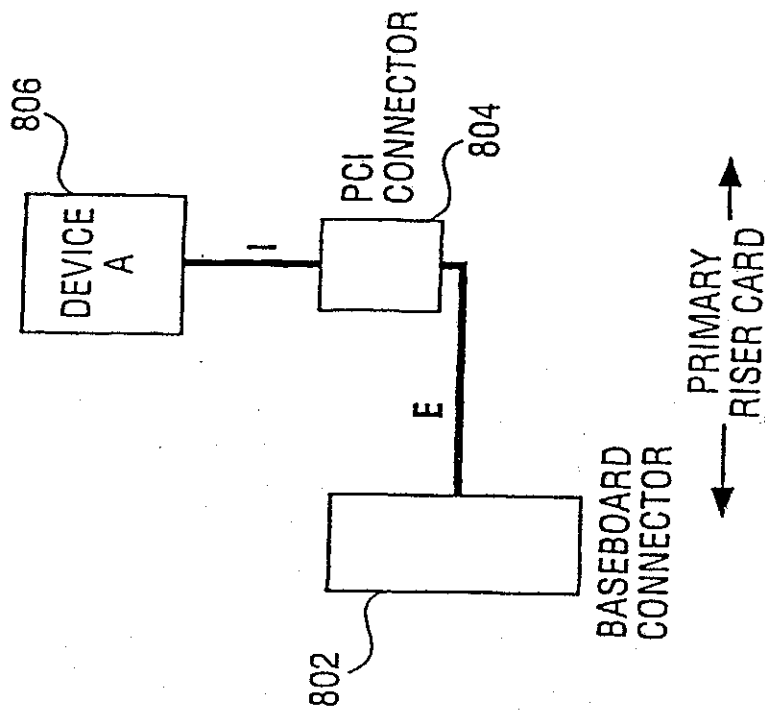


FIG. 8B

TRACE	SOURCE → DESTINATION	MAX LENGTH	MIN / MAX LENGTH
E	BASEBOARD CONN. → PCI CONN.	≤ 1.4 INCHES	0.3 - 1.1 INCHES
I	TRACE LENGTH ON DEVICE A	≤ 2.0 INCHES	1.0 - 2.0 INCHES

~~NOT FURNISHED UPON FILING~~

NO PRESENTADO(A) EN EL MOMENTO DE LA PRESENTACIÓN

NON SOUMIS(E) AU MOMENT DU DÉPÔT

Fig 3A, 3B

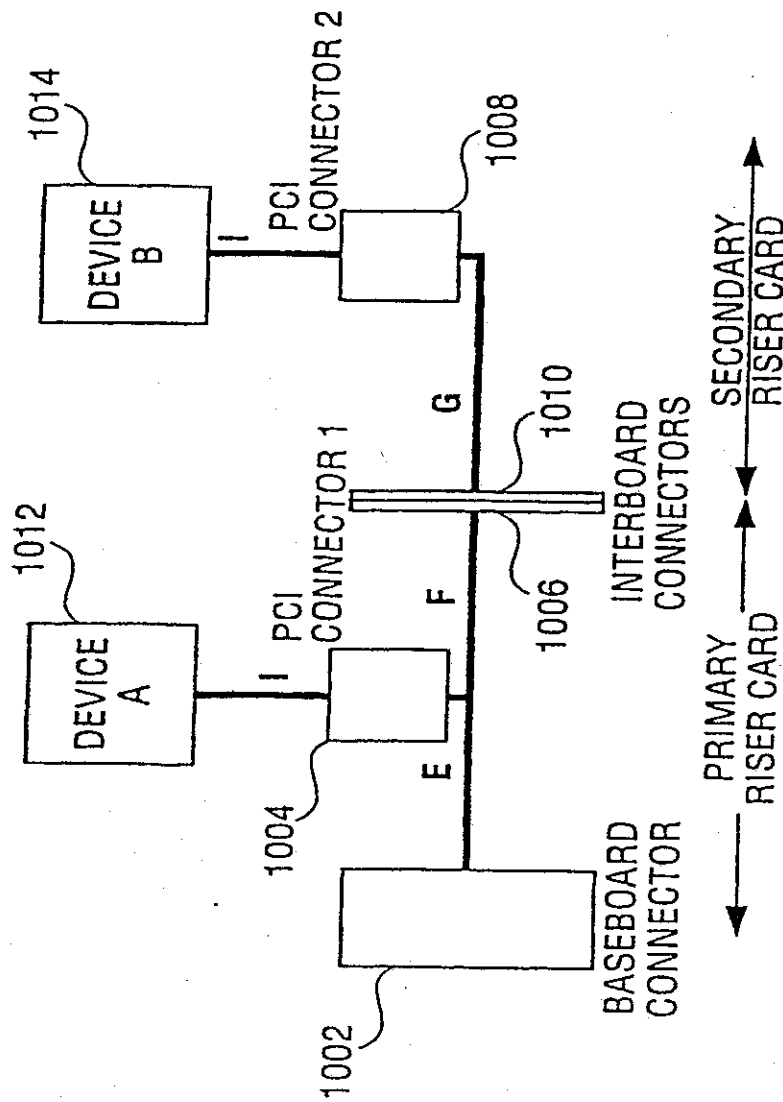


FIG. 10A

TRACE	SOURCE → DESTINATION	MAX LENGTH	MIN / MAX LENGTH
E	BASEBOARD CONN. → PCI CONN. 1	≤ 1.4 INCHES	0.3 - 1.1 INCHES
F	PCI CONN. 1 → INTERBOARD CONN.	≤ 1.5 INCHES	0.8 - 1.2 INCHES
G	INTERBOARD CONN. → PCI CONN. 2	≤ 1.9 INCHES	0.2 - 1.6 INCHES
I	TRACE LENGTHS ON DEVICES A & B	≤ 2.0 INCHES	1.0 - 2.0 INCHES

FIG. 10B

FIG. 11A

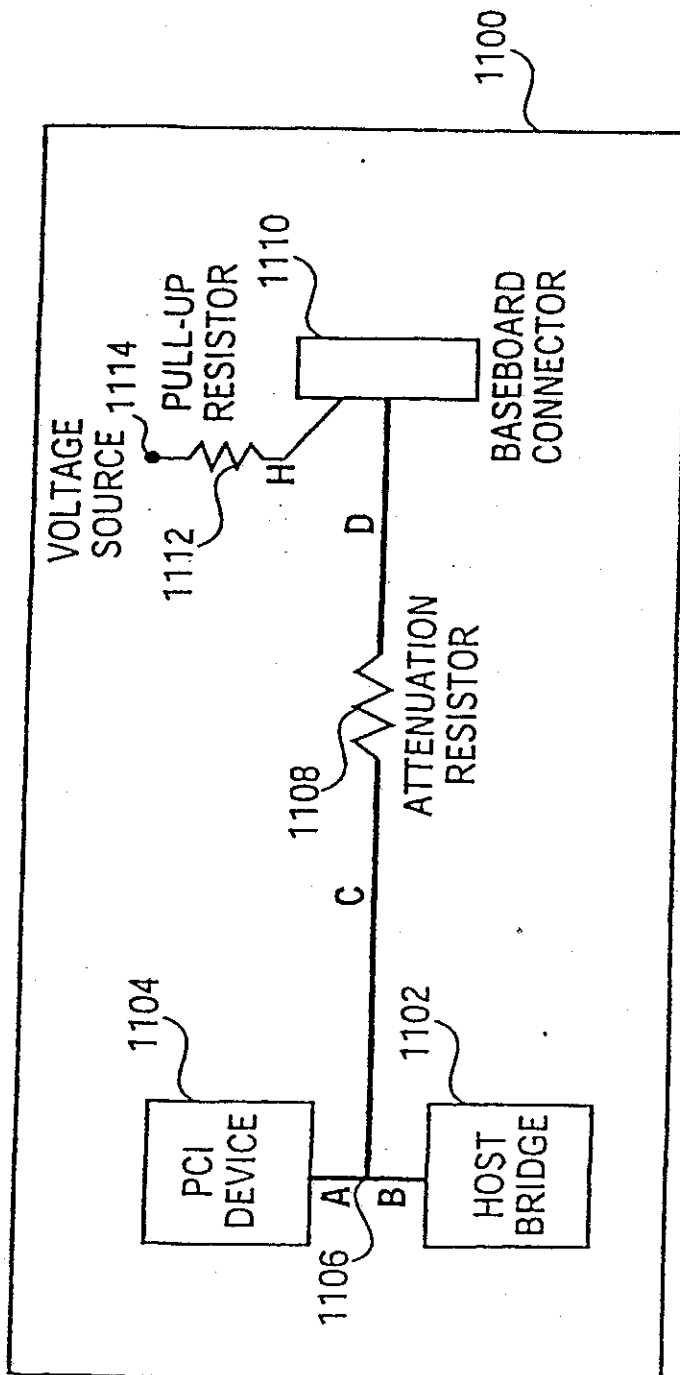


FIG. 11B

TRACE	SOURCE → DESTINATION	MAX LENGTH	MIN / MAX LENGTH
A	PCI DEVICE → T POINT	≤ 1.2 INCHES	0.01 - 1.0 INCHES
B	HOST BRIDGE → T POINT	≤ 1.2 INCHES	0.01 - 1.0 INCHES
C	T POINT → ATTENUATION RESISTOR	≤ 6 INCHES	2.0 - 5.0 INCHES
D	ATT. RESISTOR → BASEBOARD CONN.	≤ 2.5 INCHES	1.0 - 2.0 INCHES
H	PULL-UP RESISTOR STUB LENGTH	≤ 1.2 INCHES	0.01 - 1.0 INCHES

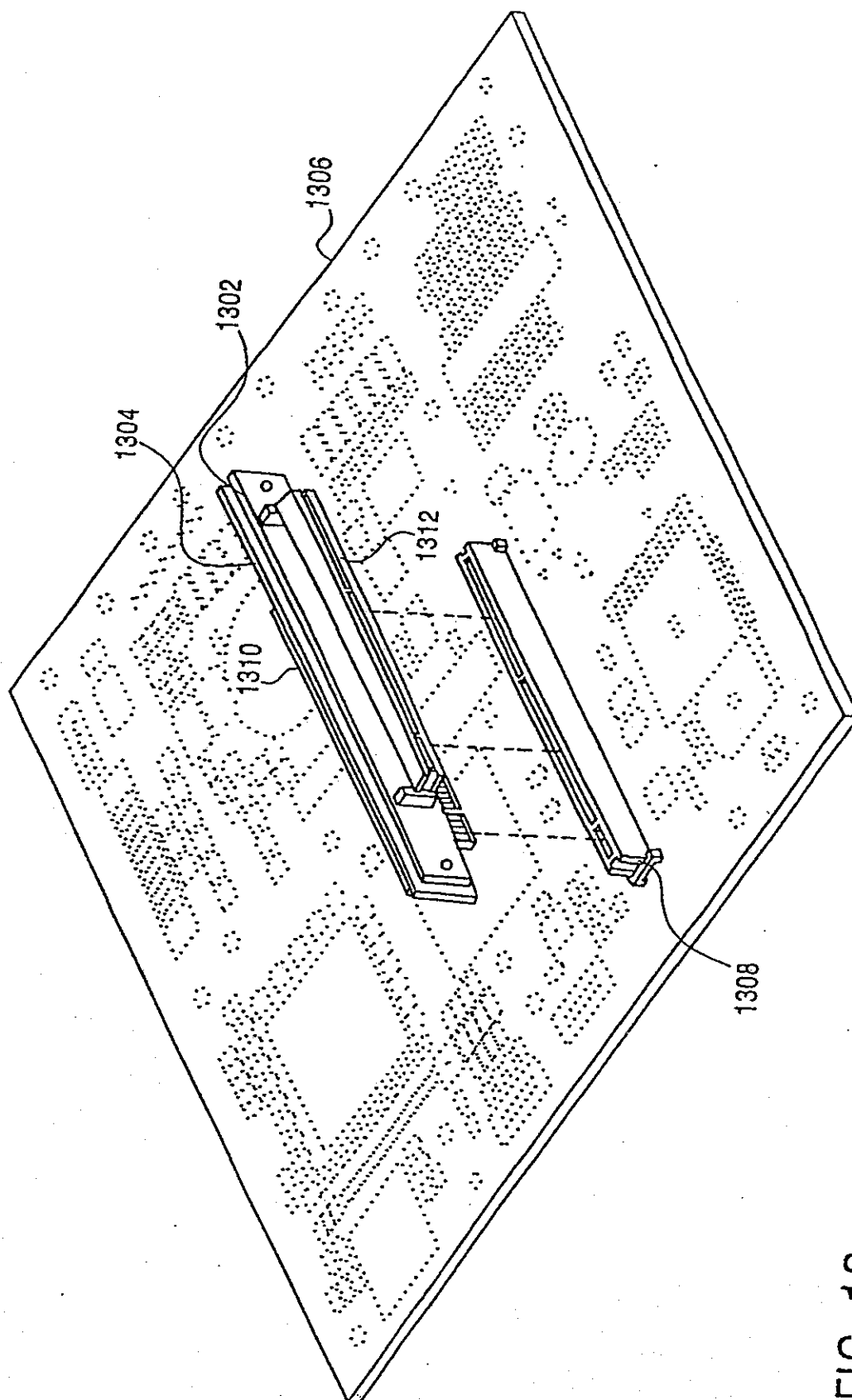


FIG. 12