

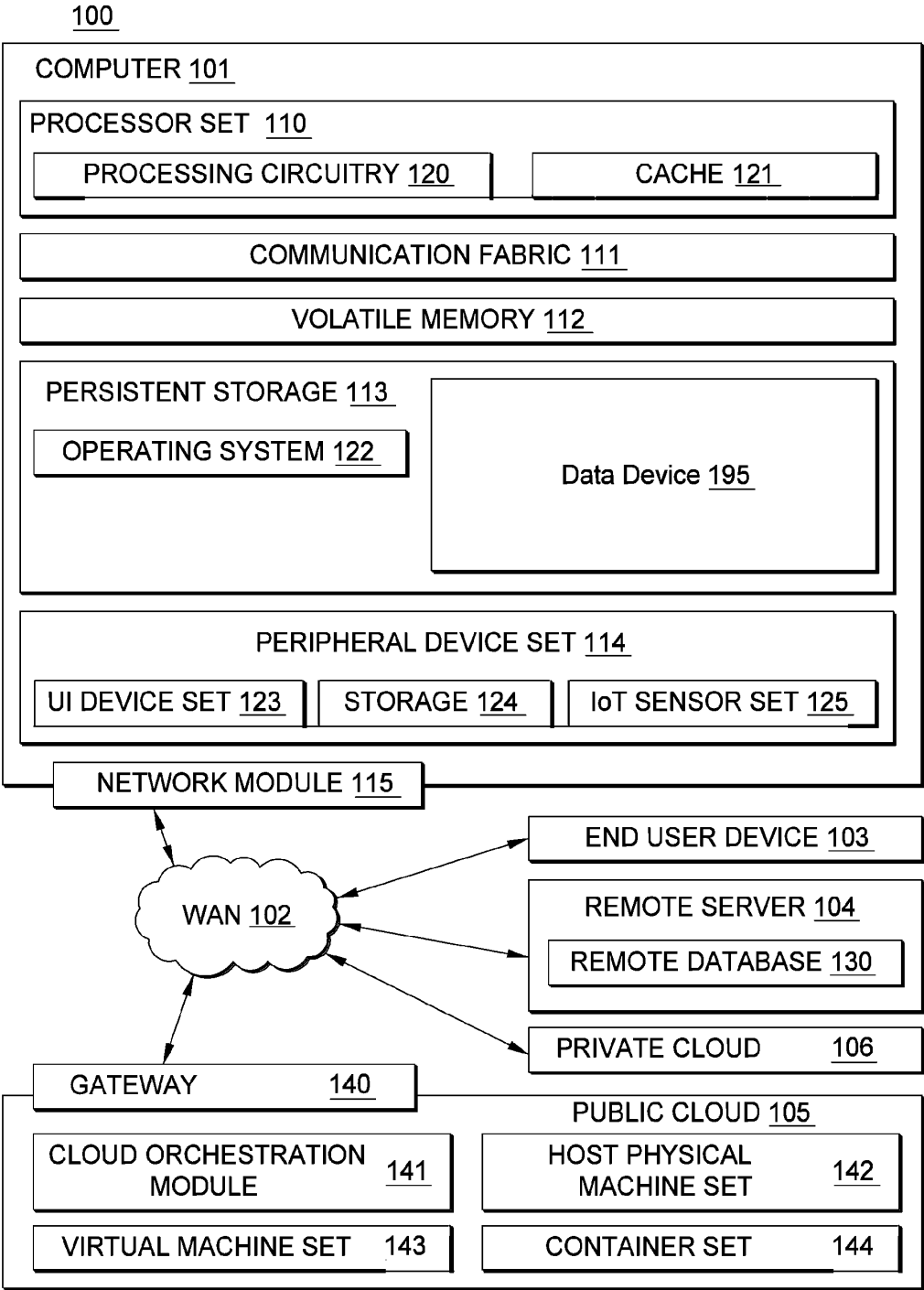


FIG. 1

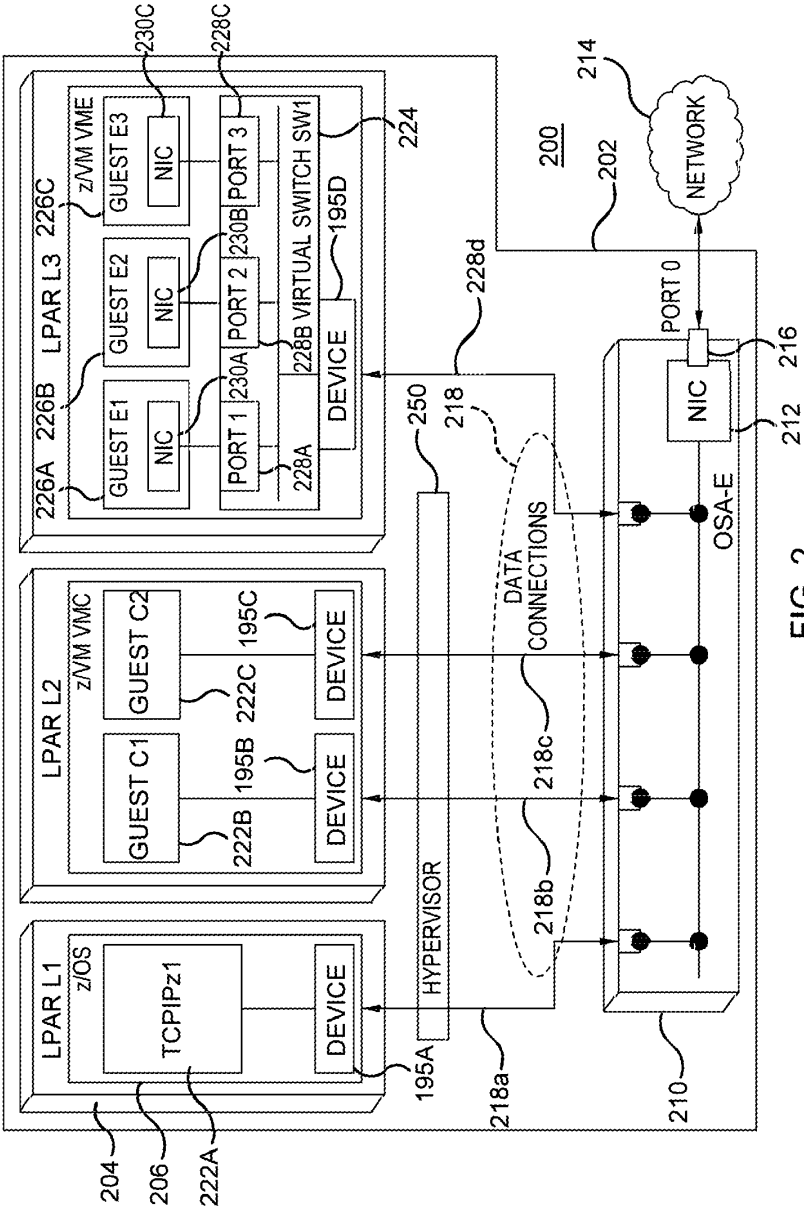
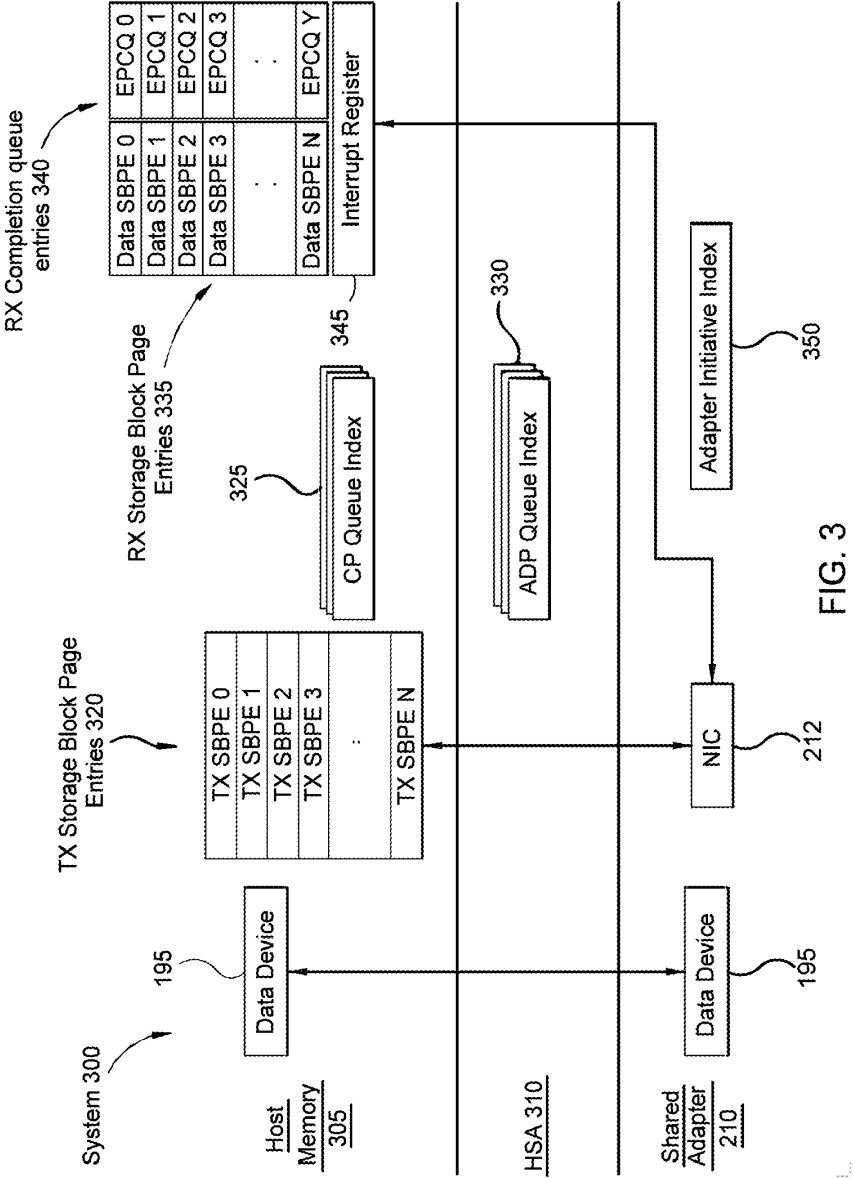


FIG. 2



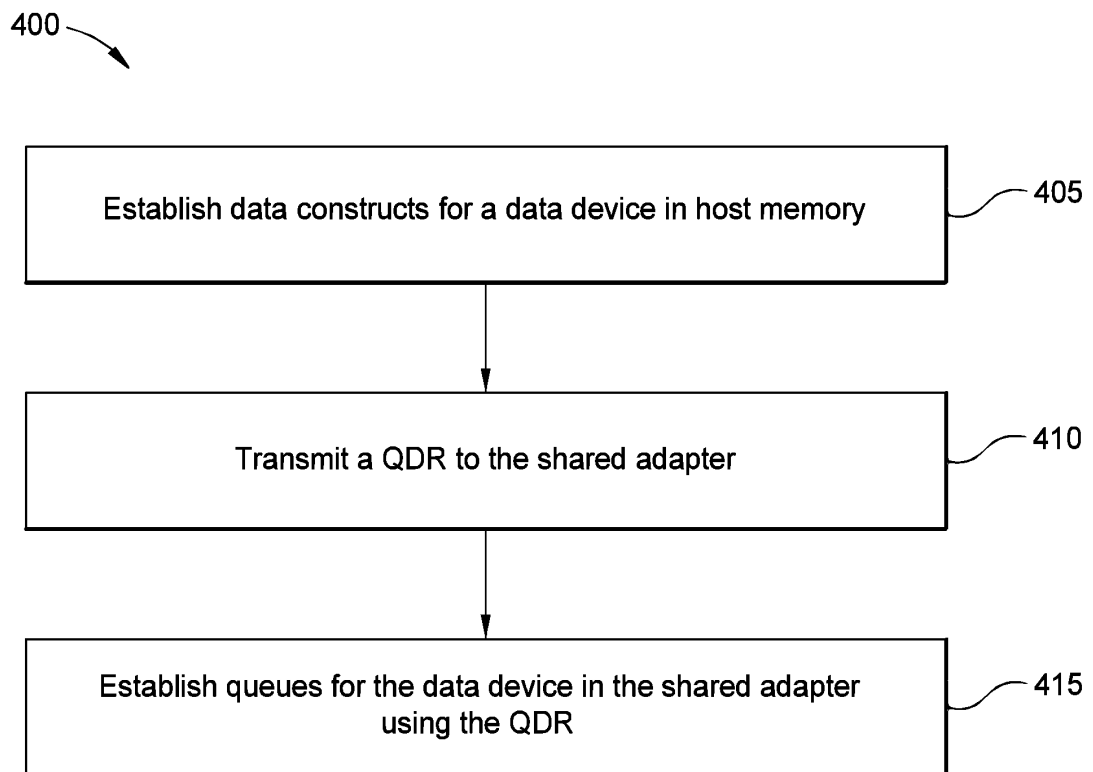


FIG. 4

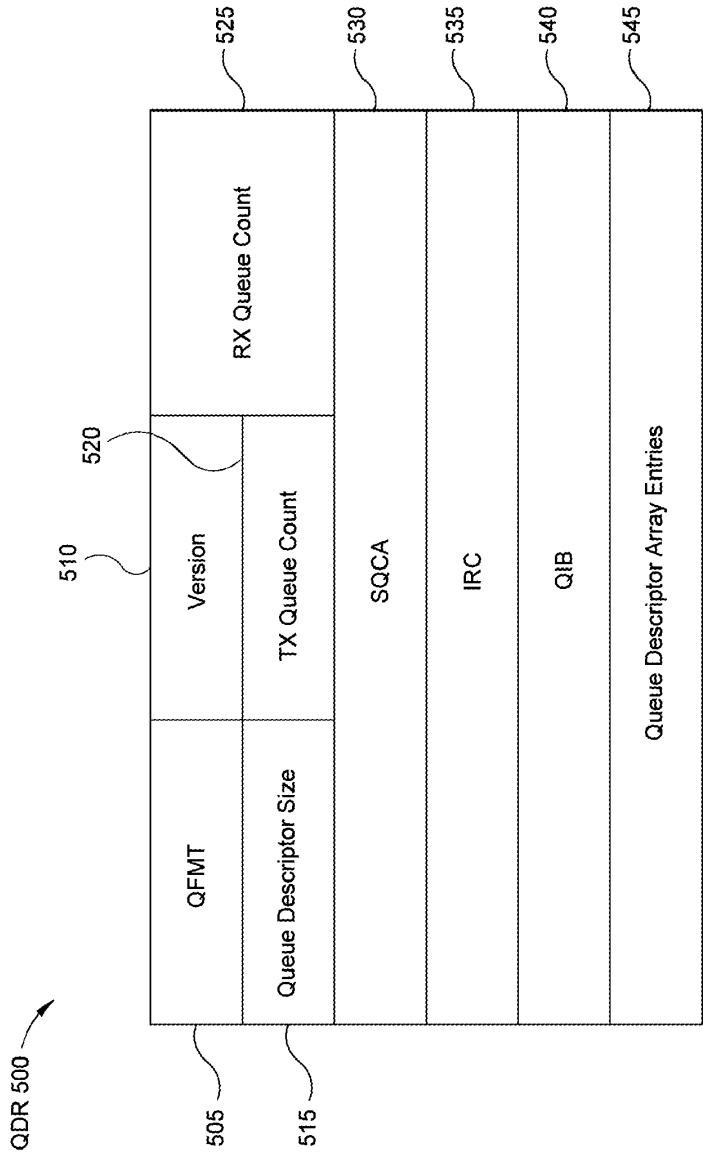


FIG. 5

Queue Descriptor Array Entry
545

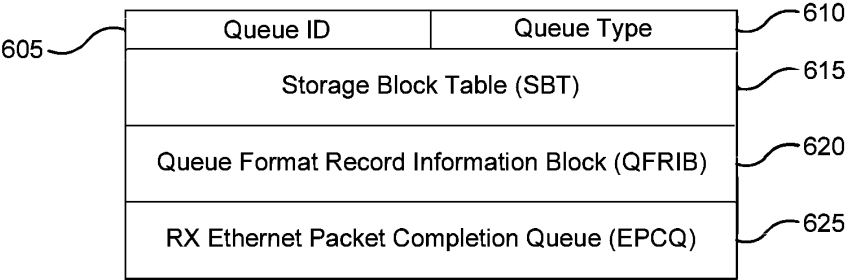


FIG. 6

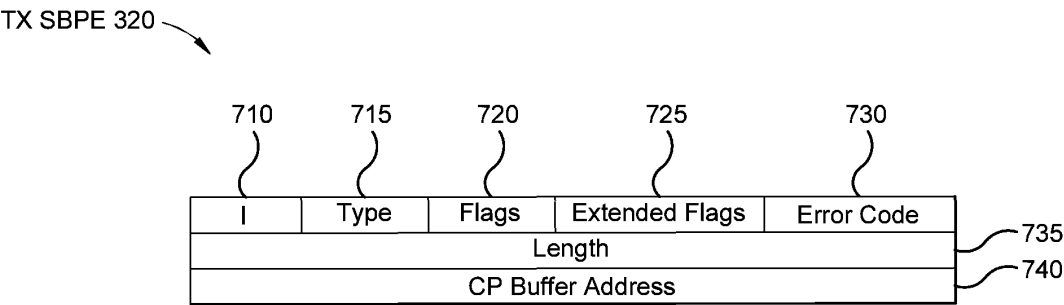


FIG. 7A

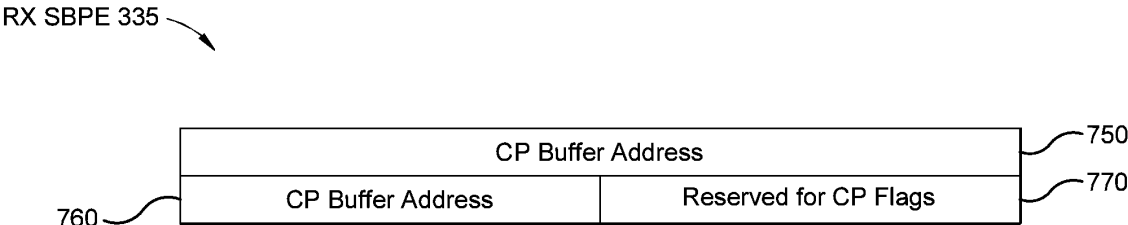


FIG. 7B

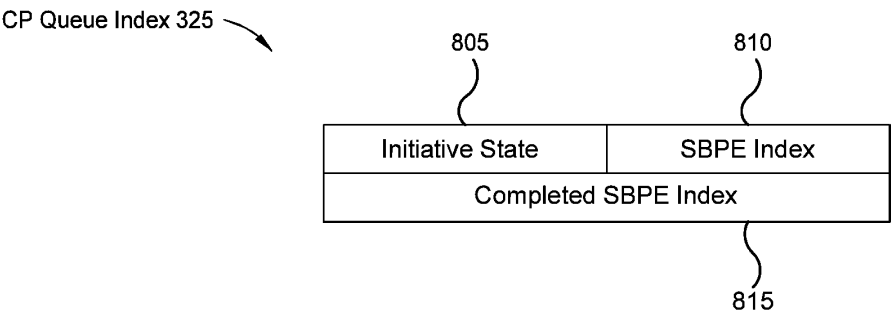


FIG. 8

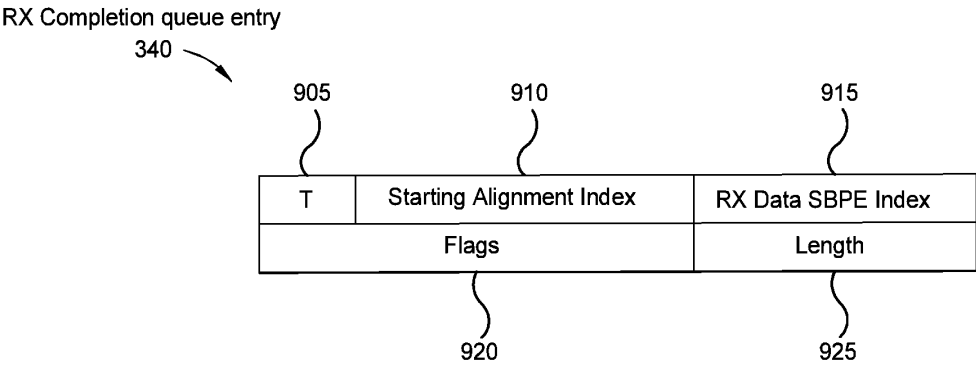


FIG. 9

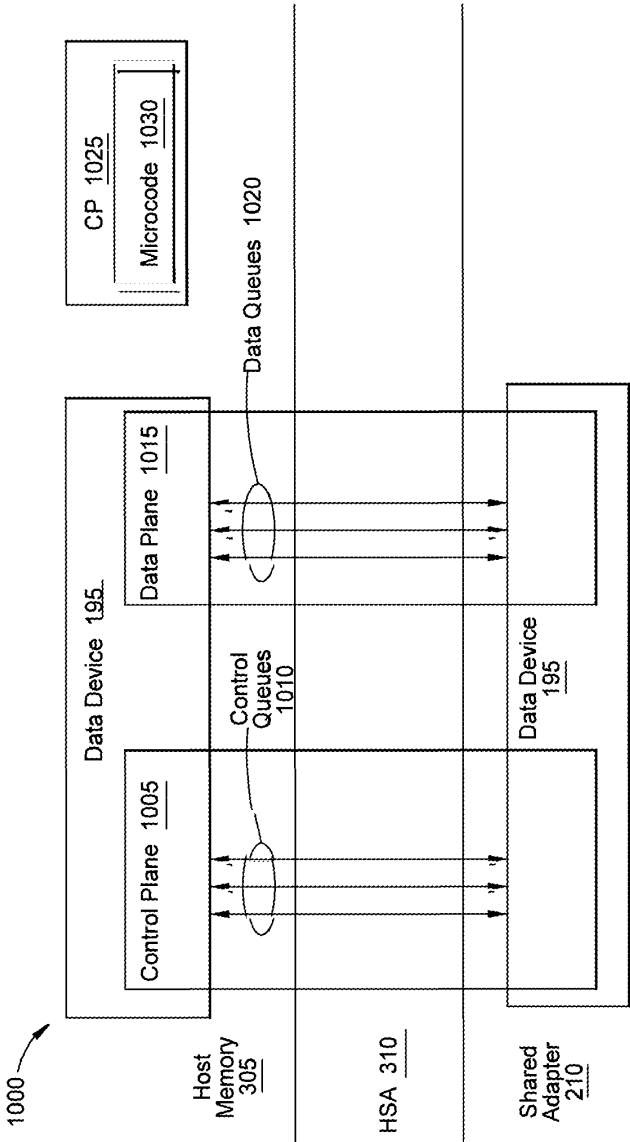


FIG. 10

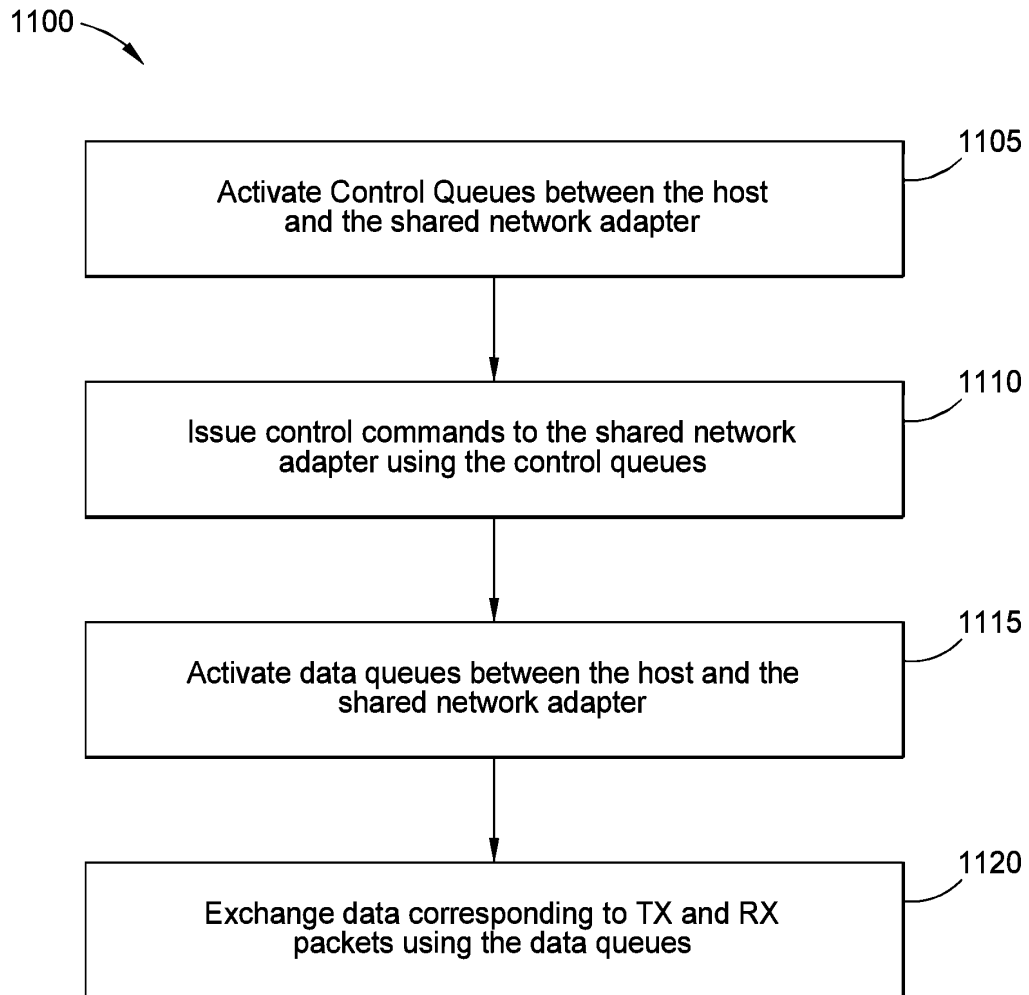
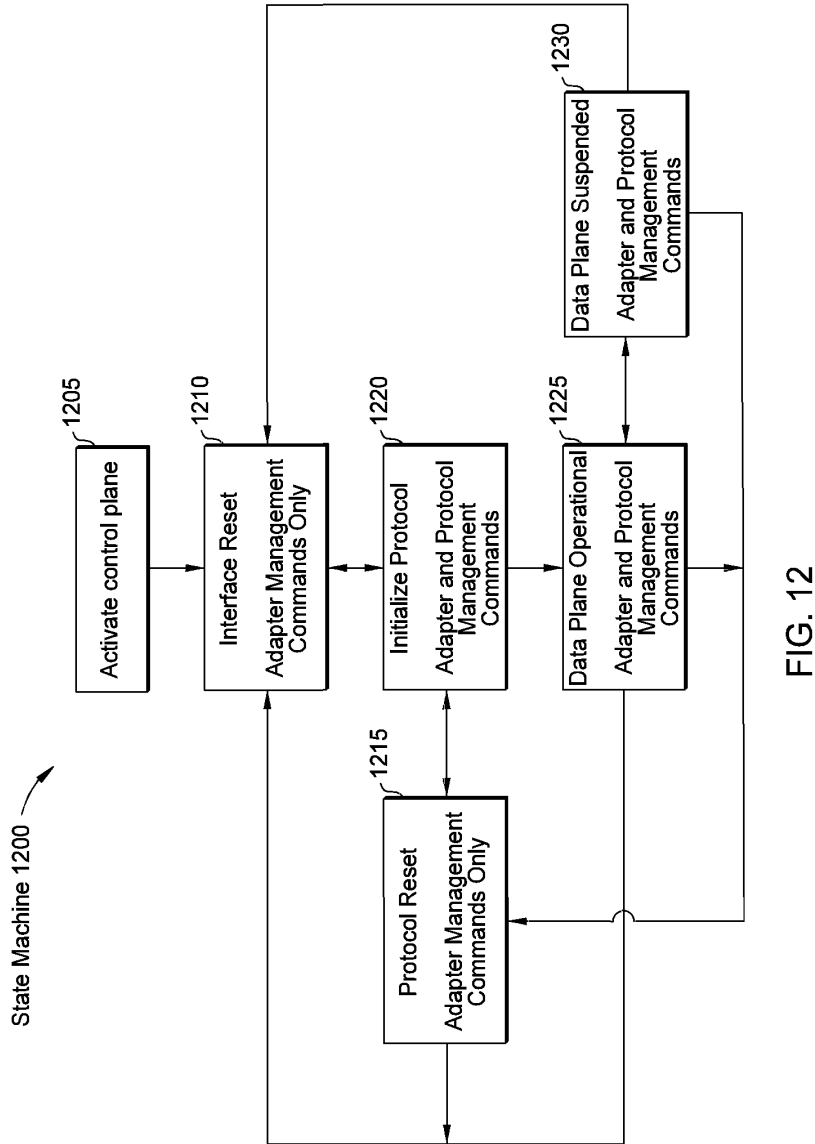


FIG. 11



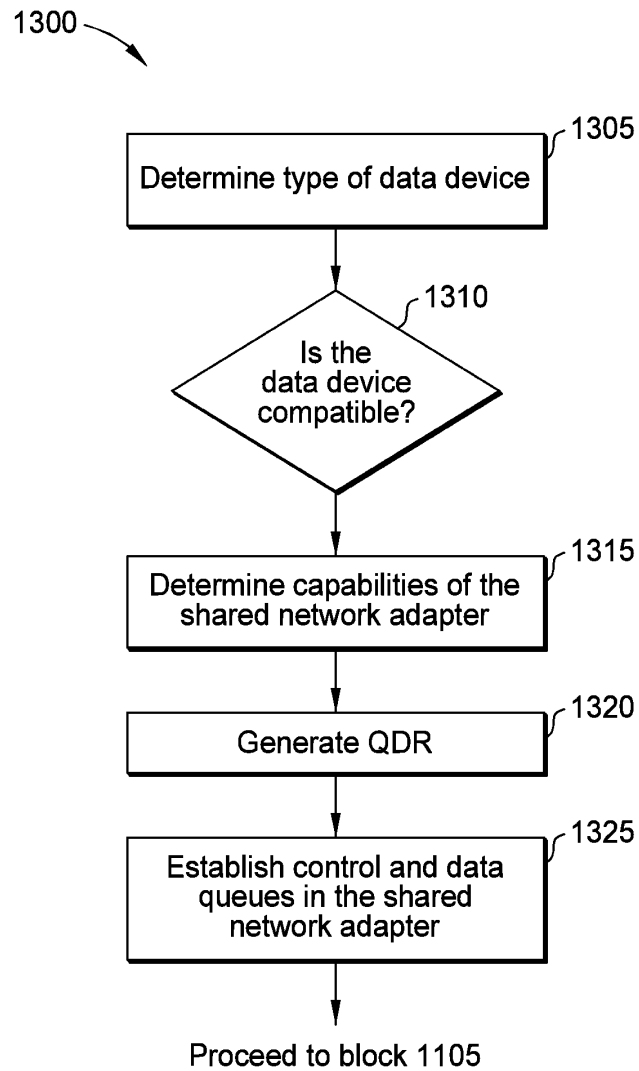


FIG. 13

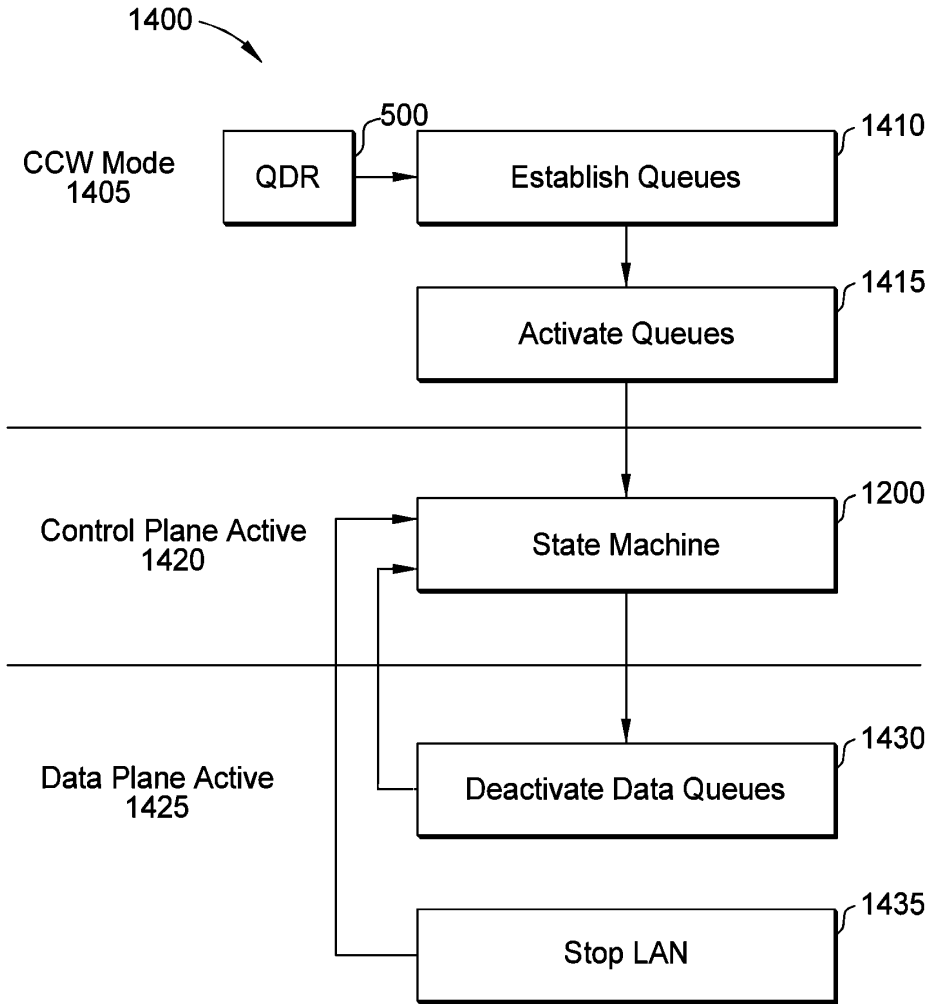


FIG. 14

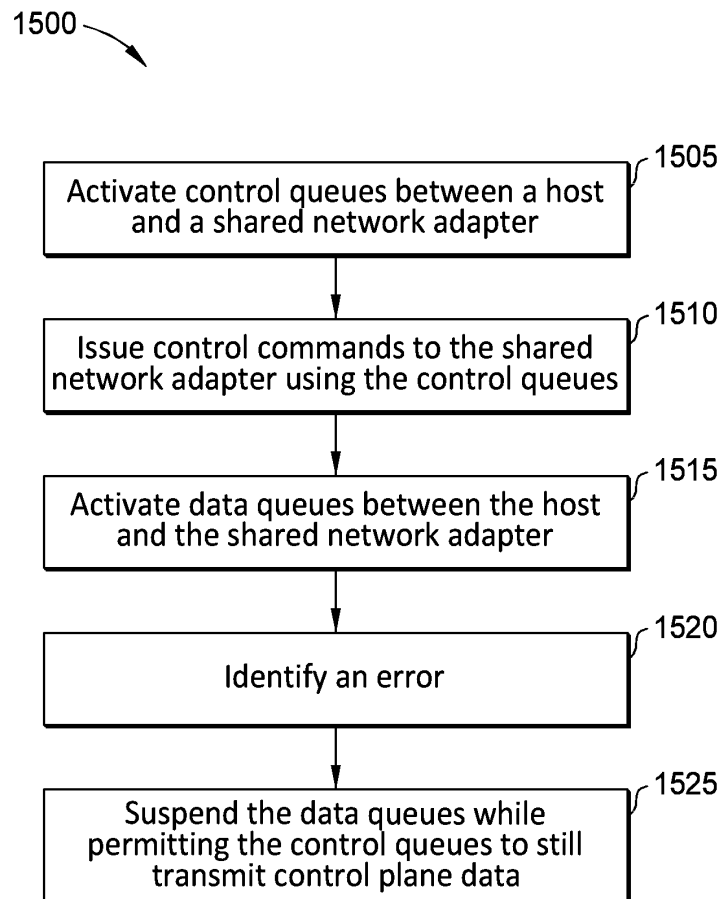


FIG. 15

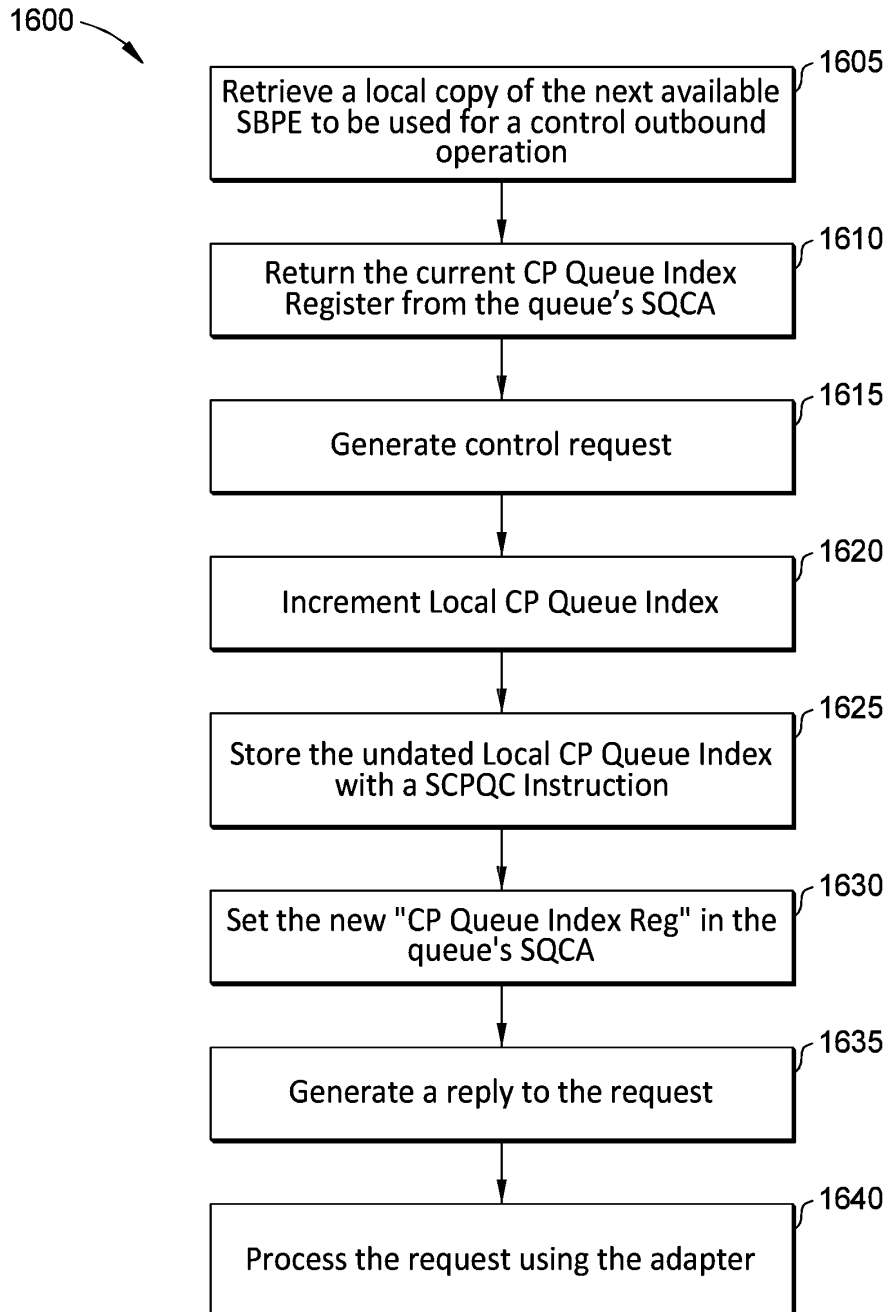


FIG. 16