TRANSISTORS HAVING SELF-ALIGNED BODY TIE

ABSTRACT

A transistor can include a source and a drain with each implemented in a first type active region, a gate implemented relative to the source and the drain, and a body implemented in the first type active region and substantially covered by the gate. The transistor can further include a body tie implemented in a second type active region and including a connecting portion substantially covered by the gate and engaging the body. The first and second active regions can be dimensioned to provide a gap therebetween on each side of the gate.

[Figure 8H]