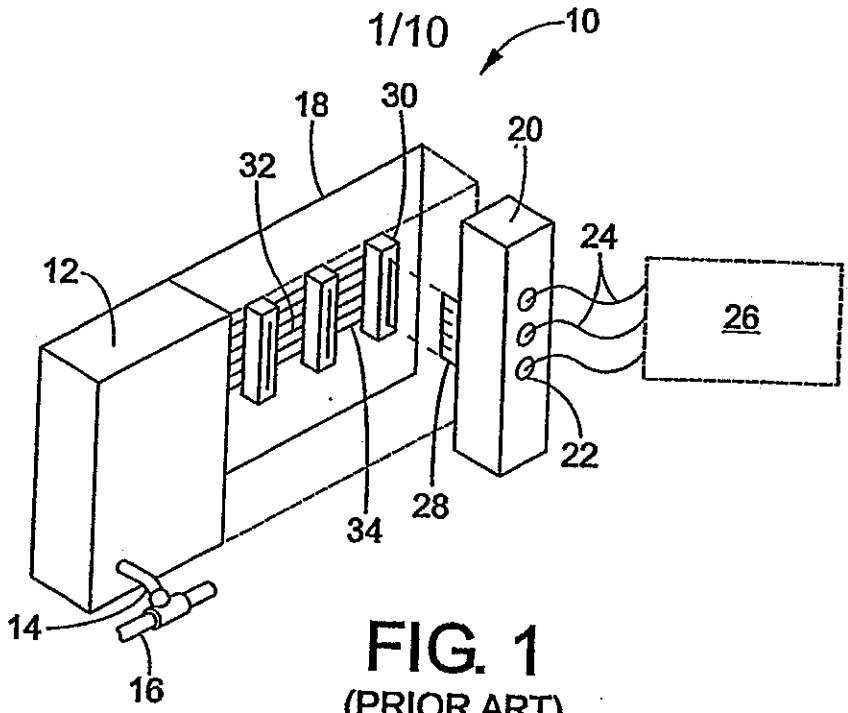



FIG. 1
(PRIOR ART)

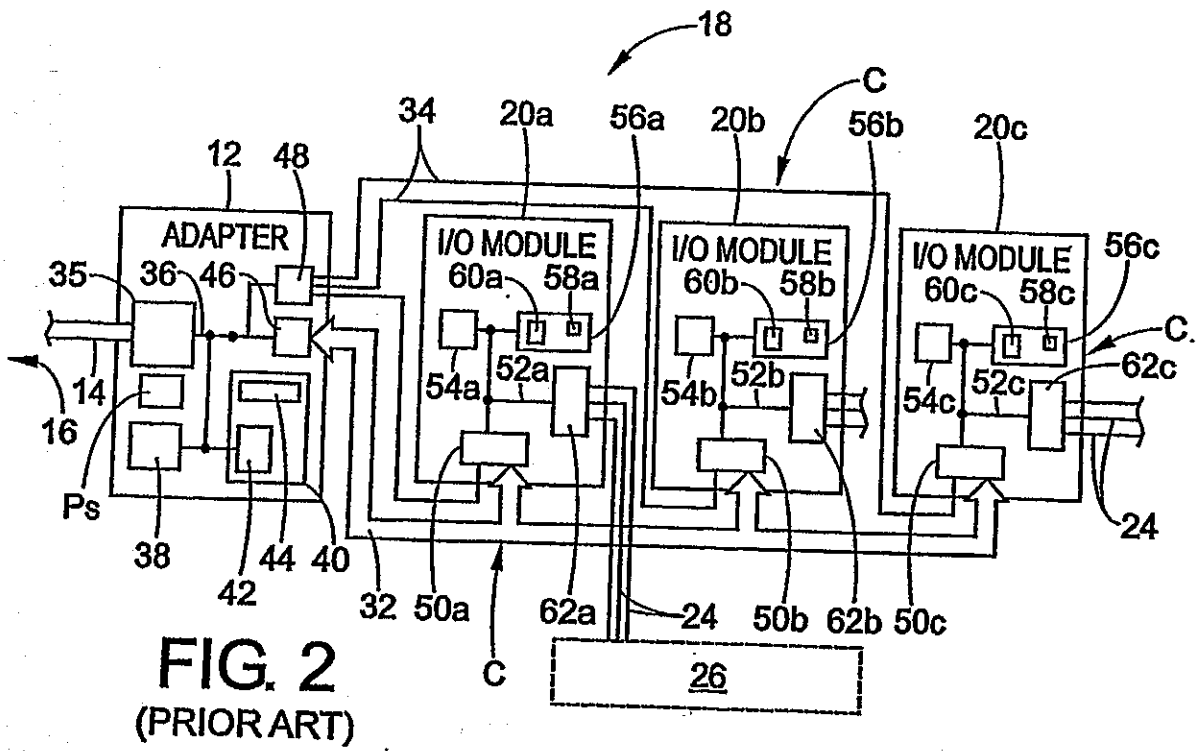



FIG. 2
(PRIOR ART)

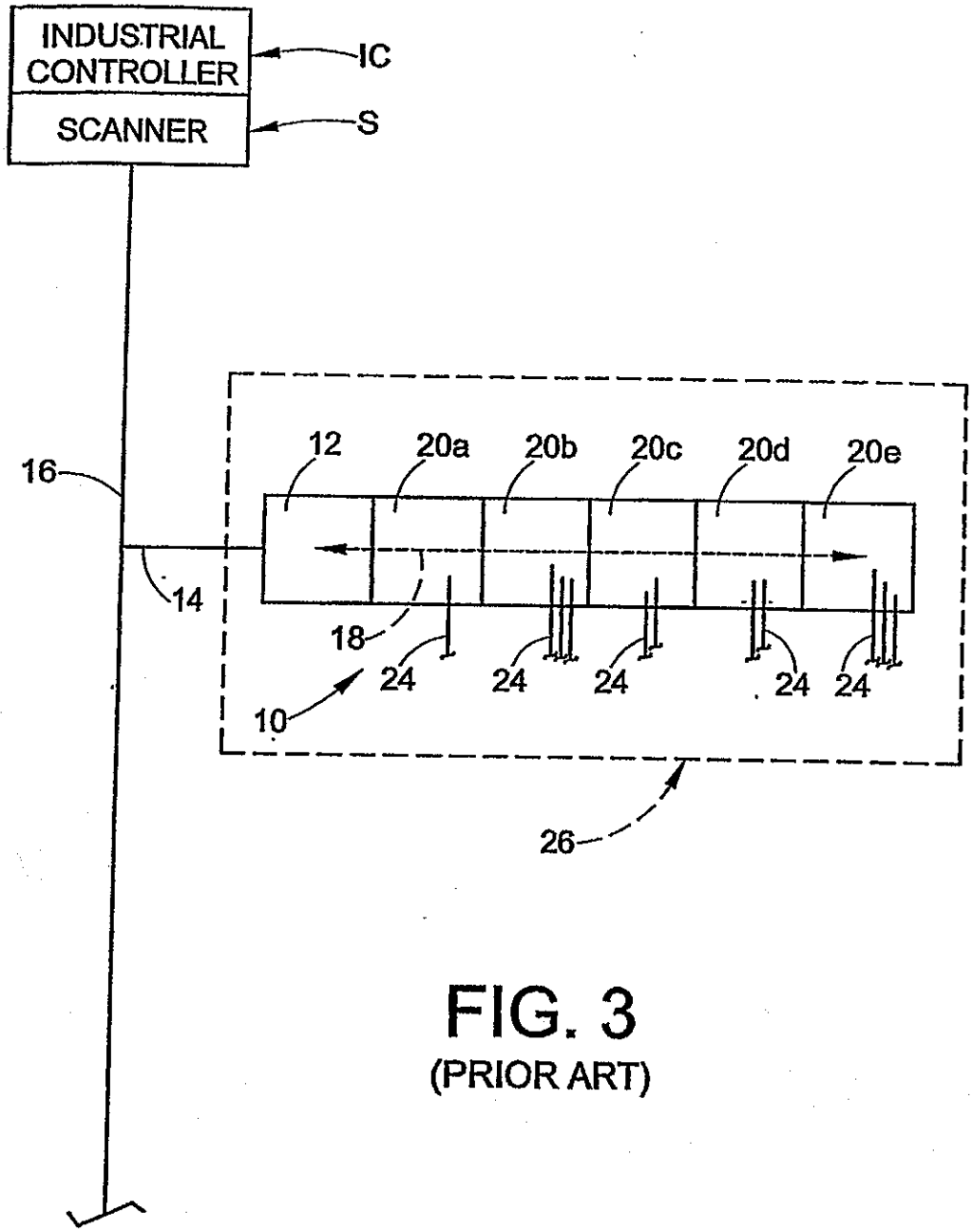



FIG. 3
(PRIOR ART)

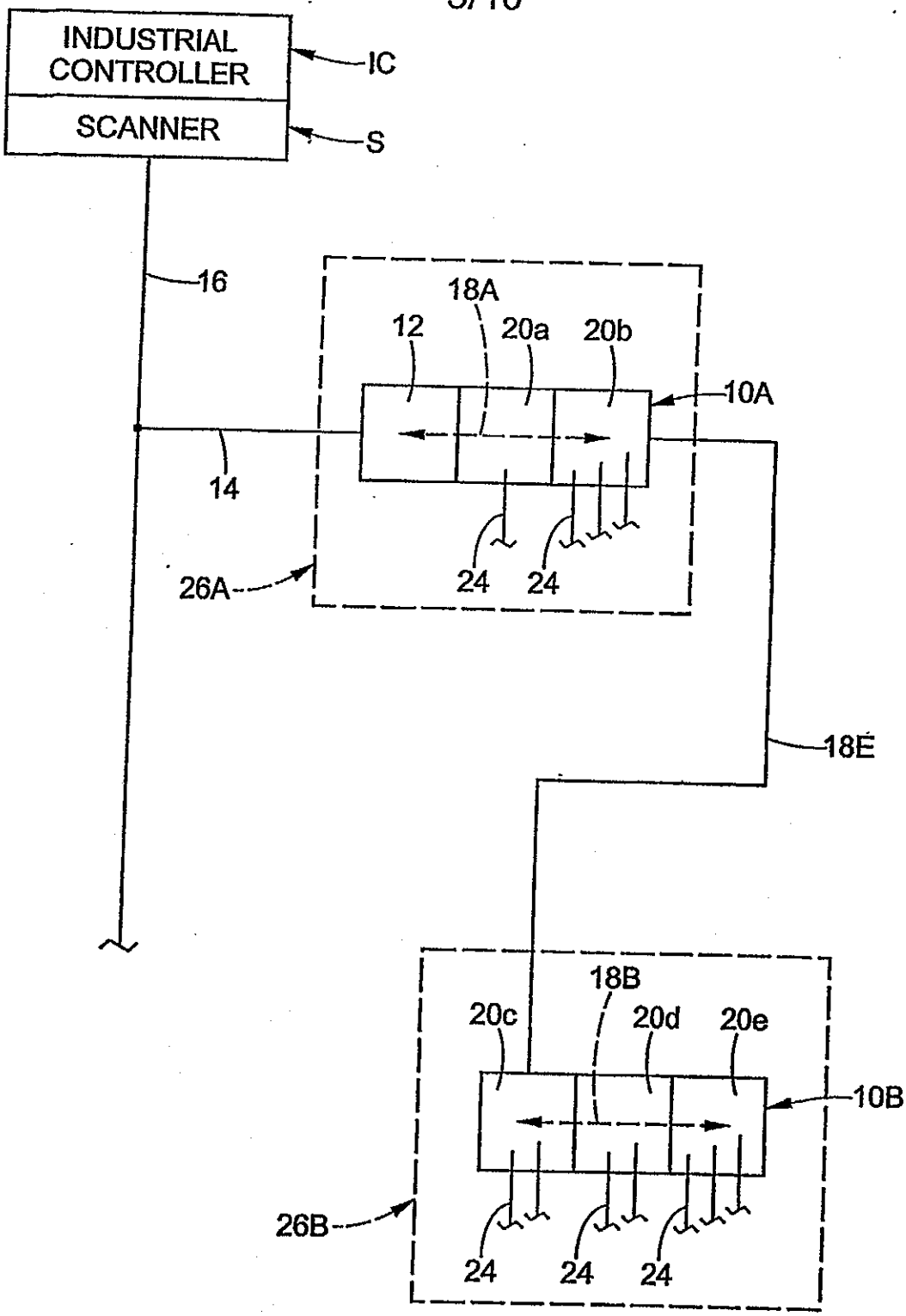



FIG. 4
(PRIOR ART)

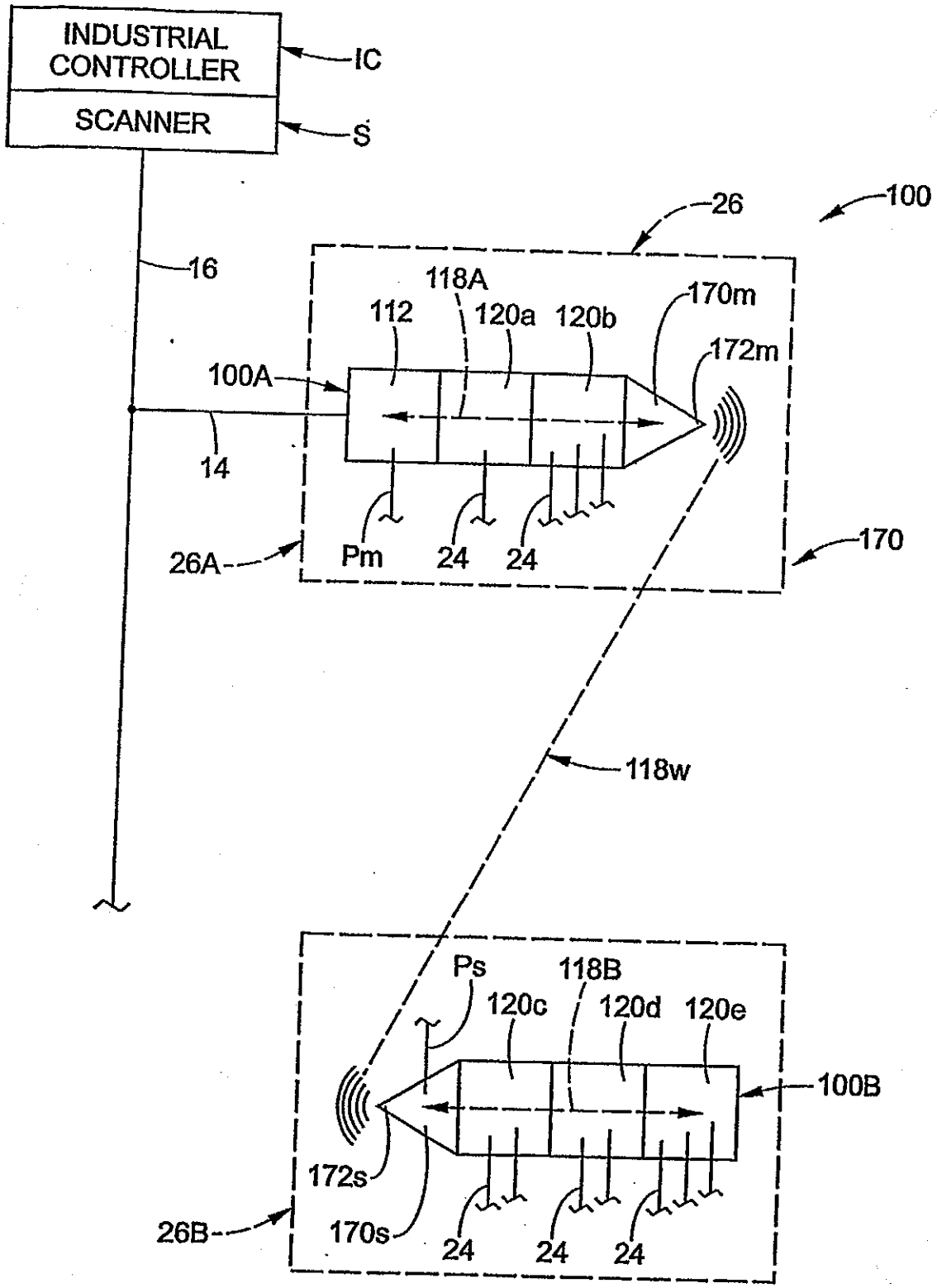

INDUSTRIAL CONTROLLER
IC
SCANNER
S
26
100
16
118A
112
120a
120b
170m
172m
100A
14
26A
Pm
24
24
170
118w
Ps
118B
120c
120d
120e
100B
172s
170s
26B
24
24
24

FIG. 5

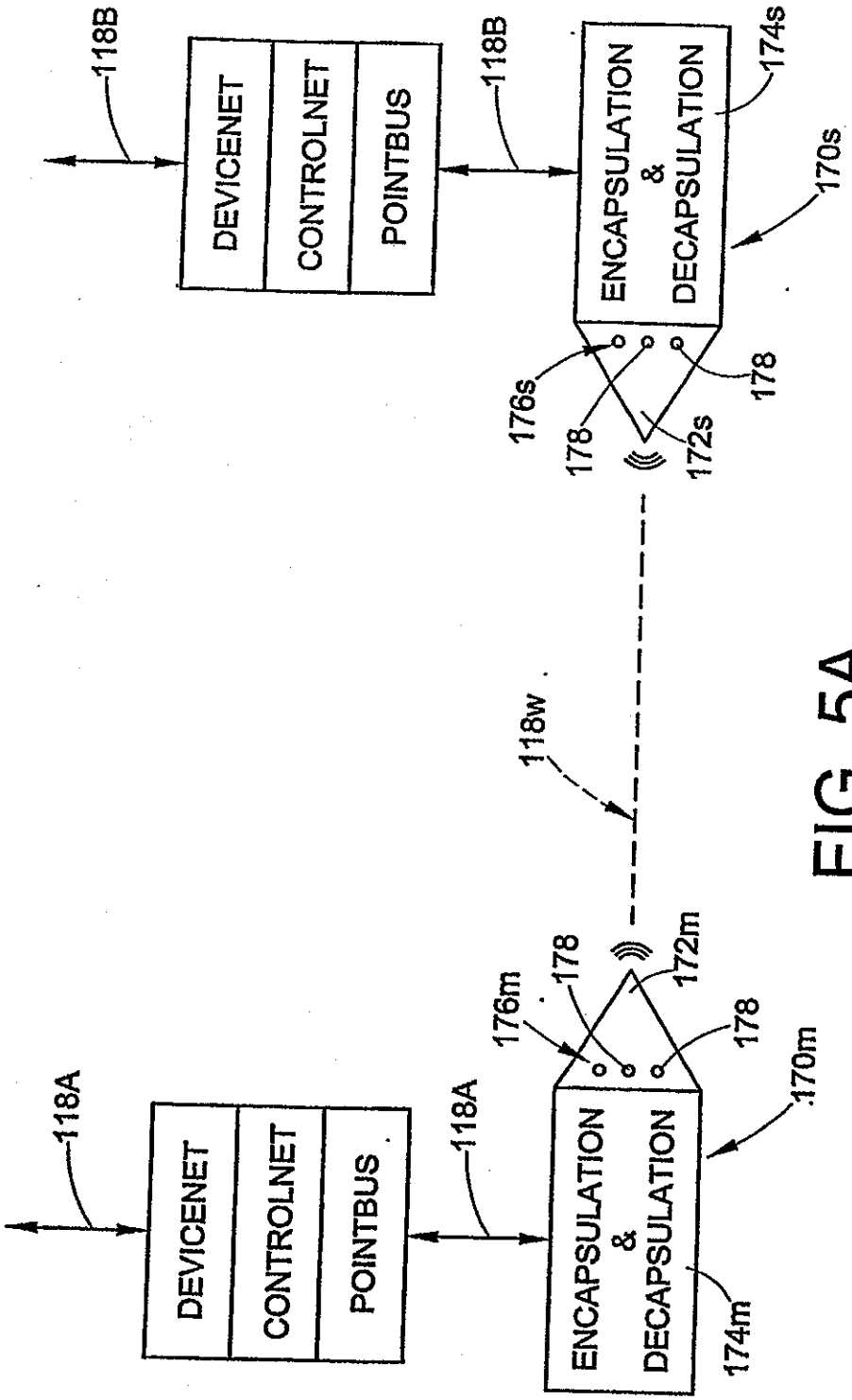
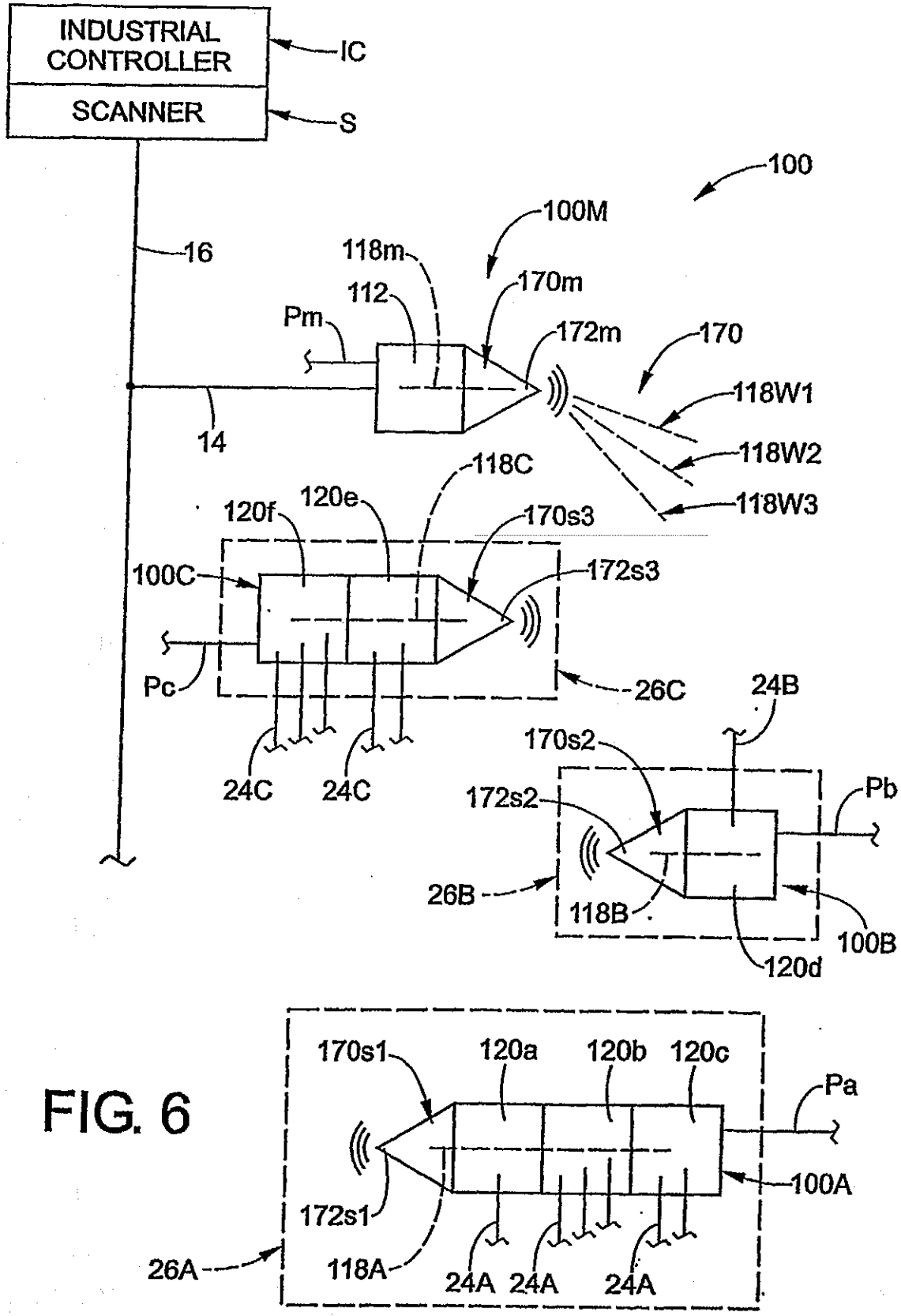

118B
DEVICENET
CONTROLNET
POINTBUS
118B
ENCAPSULATION & DECAPSULATION
174s
170s
176s
178
178
172s
118w
118A
DEVICENET
CONTROLNET
POINTBUS
118A
176m
178
172m
178
ENCAPSULATION & DECAPSULATION
170m
174m

FIG. 5A


INDUSTRIAL CONTROLLER
IC
SCANNER
S
100
100M
16
118m
112
170m
Pm
172m
170
118W1
118W2
118W3
14
118C
120f
120e
170s3
100C
172s3
Pc
26C
24B
24C
24C
170s2
172s2
Pb
26B
118B
100B
120d


170s1
120a
120b
120c
Pa
100A
172s1
26A
118A
24A
24A
24A

FIG. 6

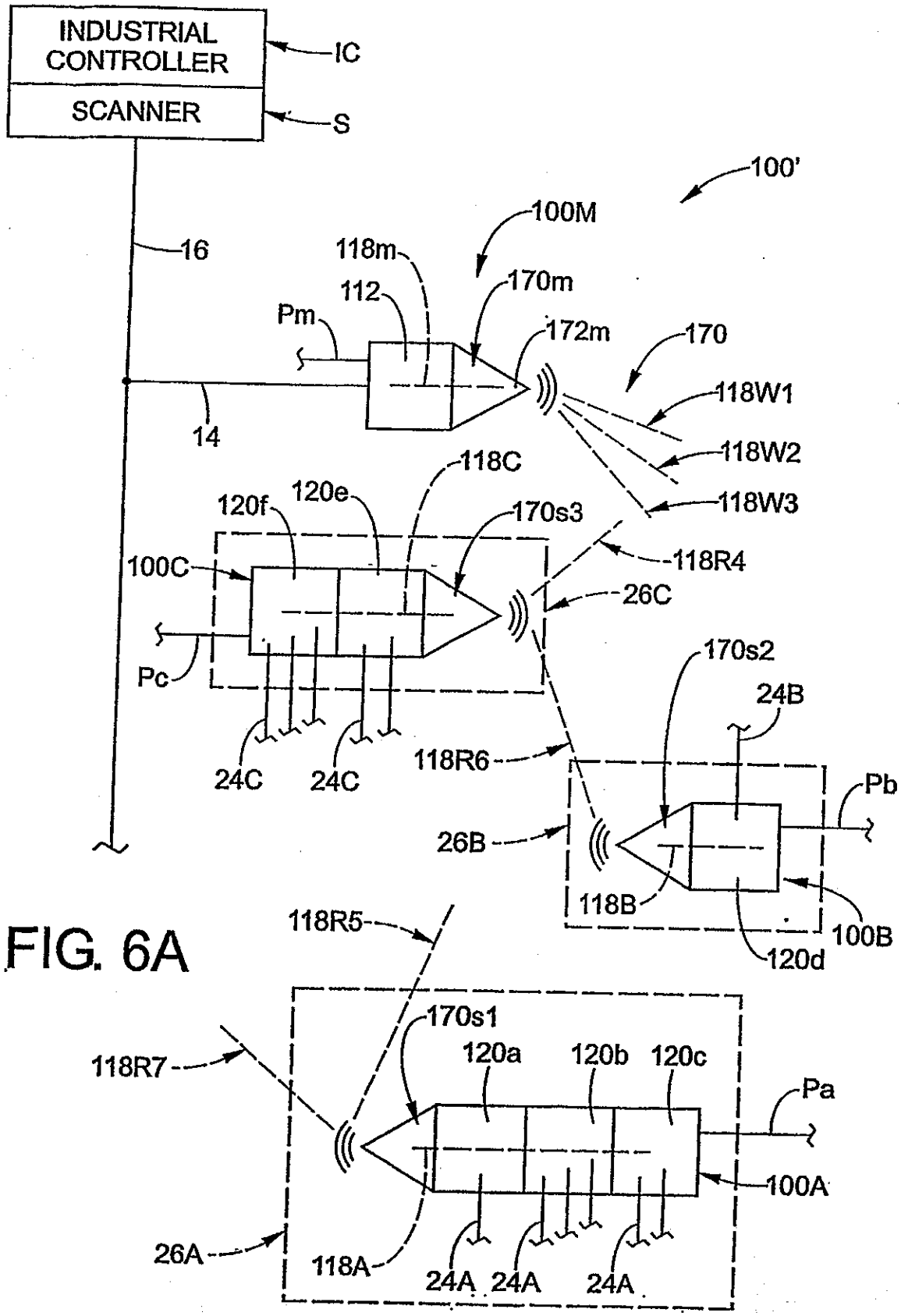

INDUSTRIAL CONTROLLER
IC
SCANNER
S
100'
100M
16
118m
112
170m
Pm
172m
170
118W1
118W2
118W3
14
118C
120f
120e
170s3
118R4
100C
26C
Pc
170s2
24B
24C
24C
118R6
Pb
26B
118B
100B
120d
118R5
170s1
118R7
120a
120b
120c
Pa
100A
26A
118A
24A
24A
24A

FIG. 6A

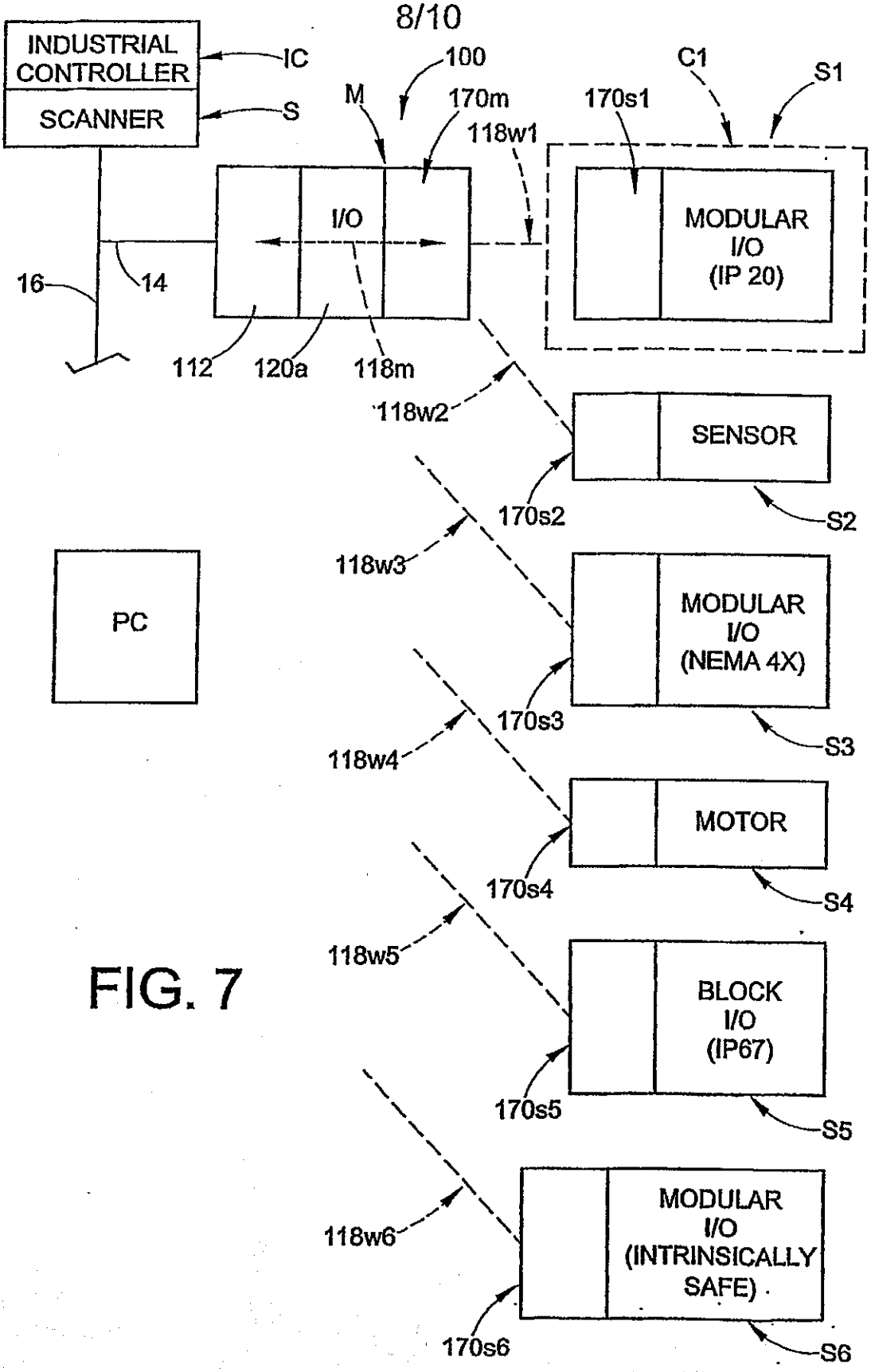

INDUSTRIAL CONTROLLER
SCANNER
IC
S
M
100
170m
118w1
170s1
C1
S1
I/O
MODULAR I/O (IP 20)
16
14
112
120a
118m
118w2
SENSOR
170s2
S2
118w3
PC
MODULAR I/O (NEMA 4X)
170s3
S3
118w4
MOTOR
170s4
S4
118w5
BLOCK I/O (IP67)
170s5
S5
118w6
MODULAR I/O (INTRINSICALLY SAFE)
170s6
S6

FIG. 7

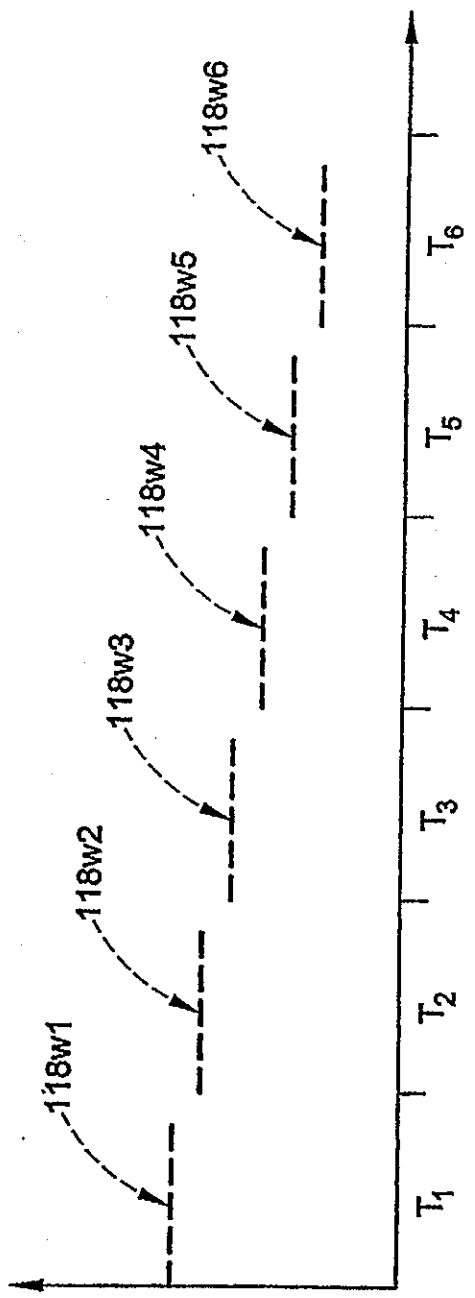



FIG. 8

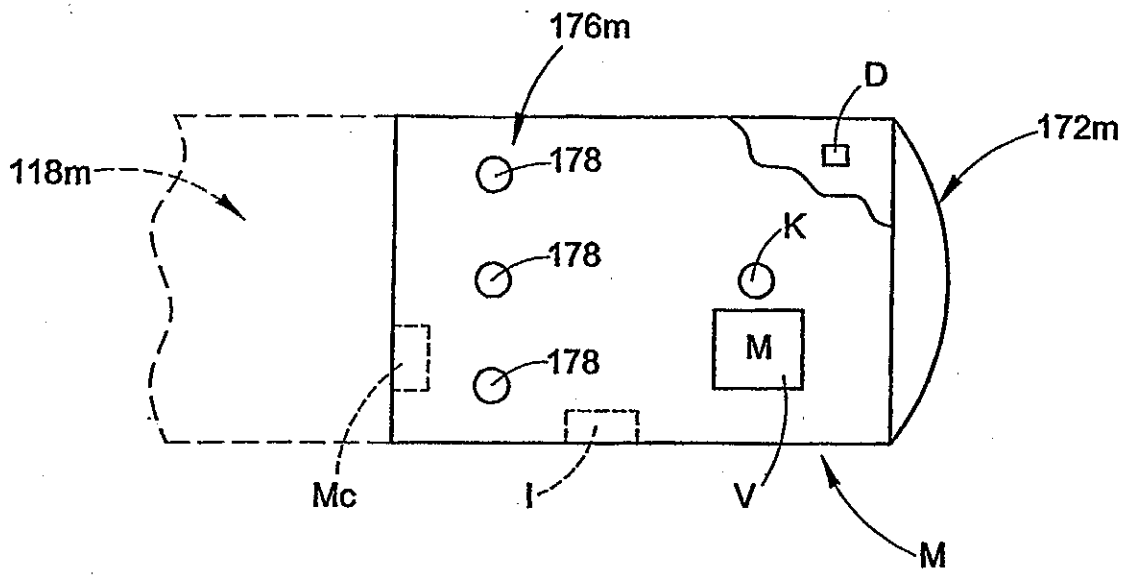

176m
D
172m
118m
178
178
178
K
M
Mc
I
V
M

FIG. 9A

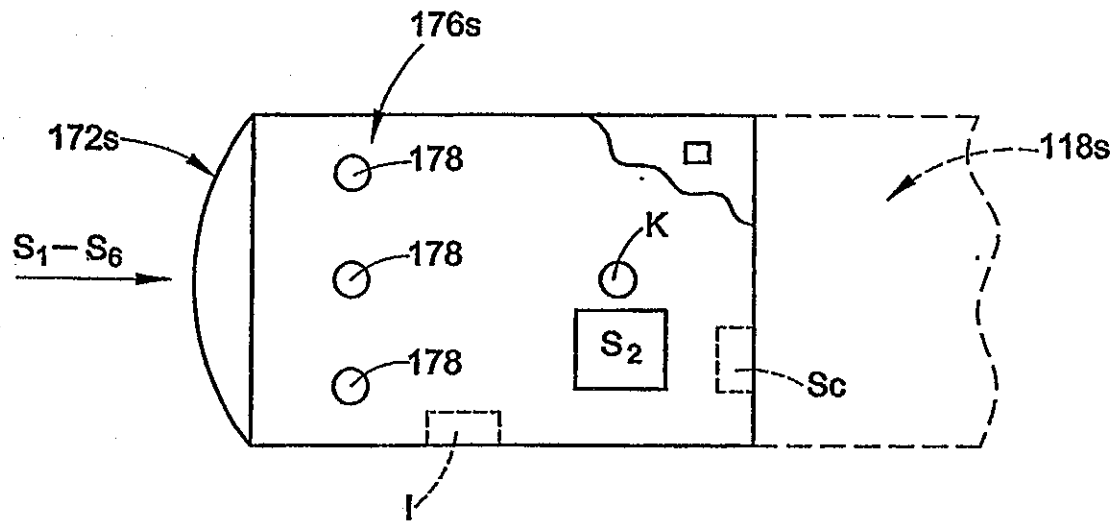

176s
172s
178
178
178
K
$S_1 - S_6$
$S_2$
Sc
118s
I

FIG. 9B