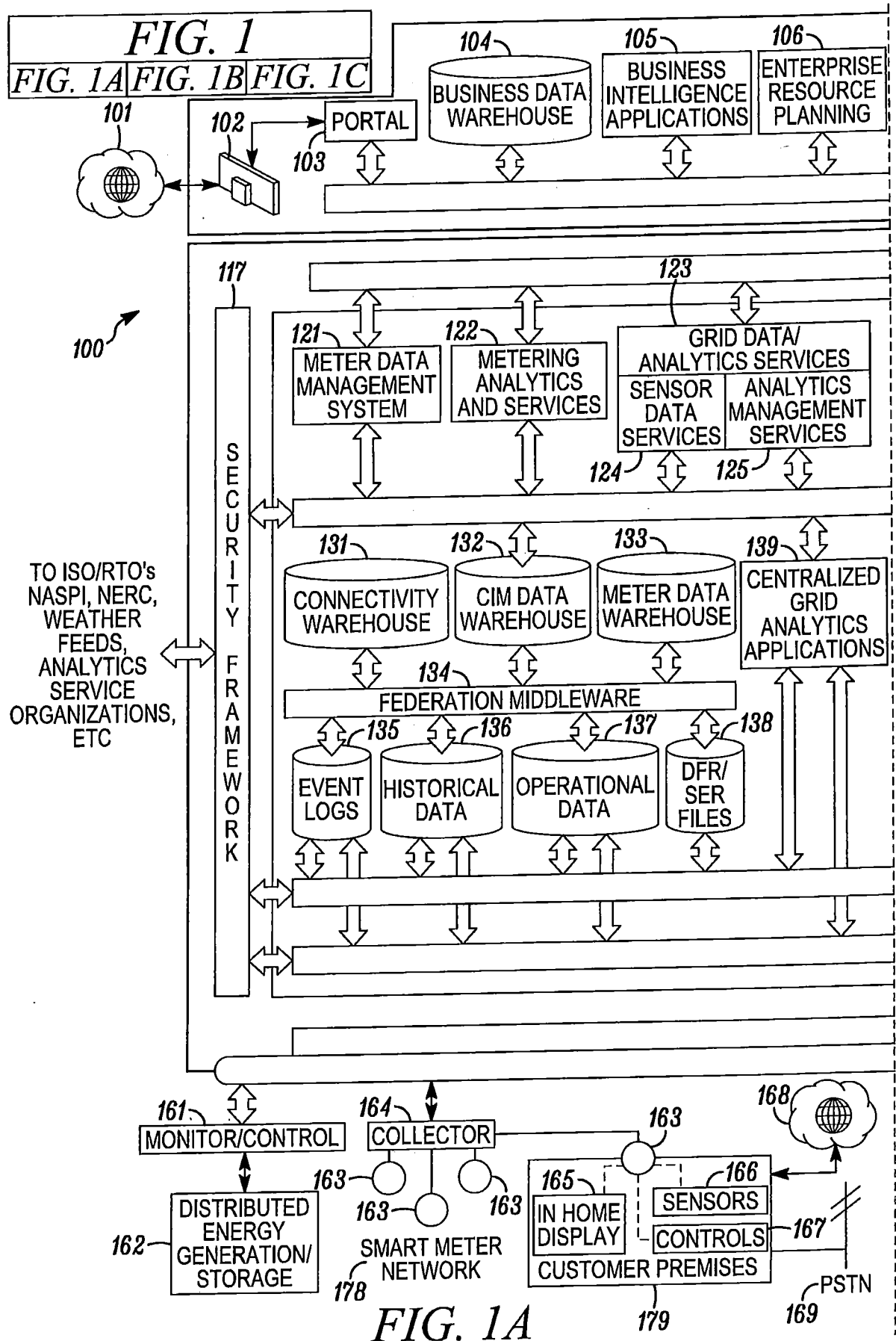
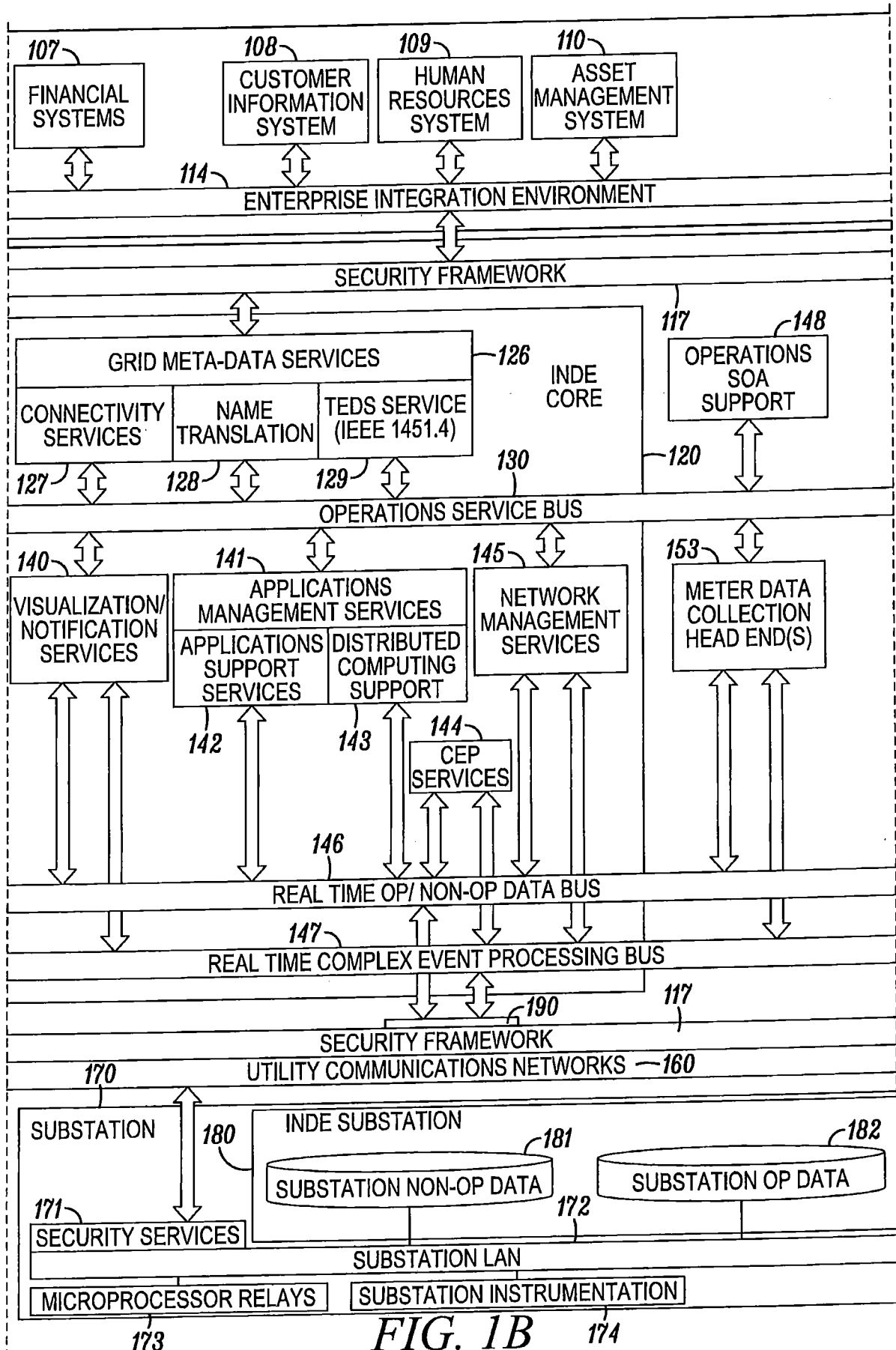
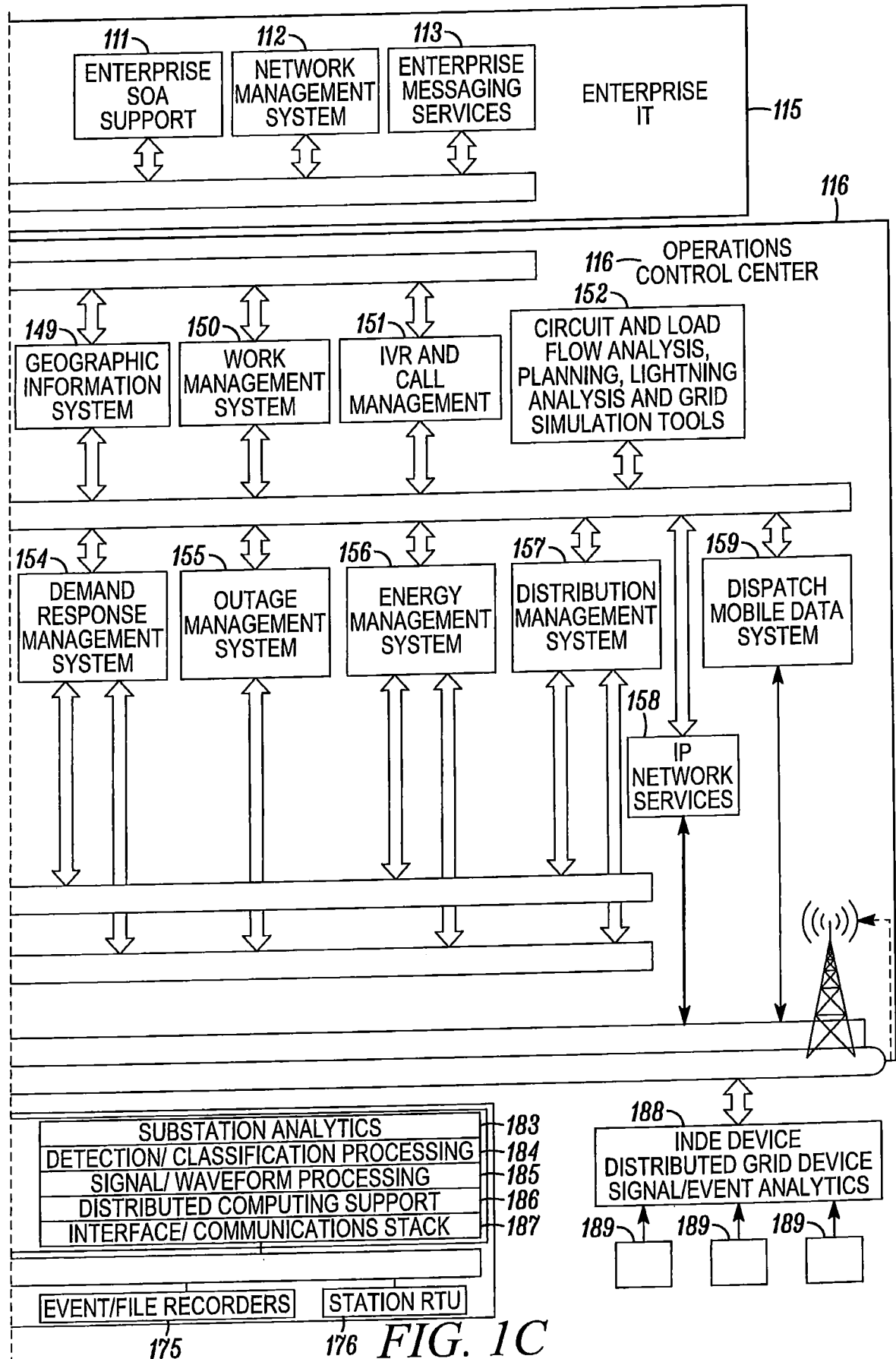








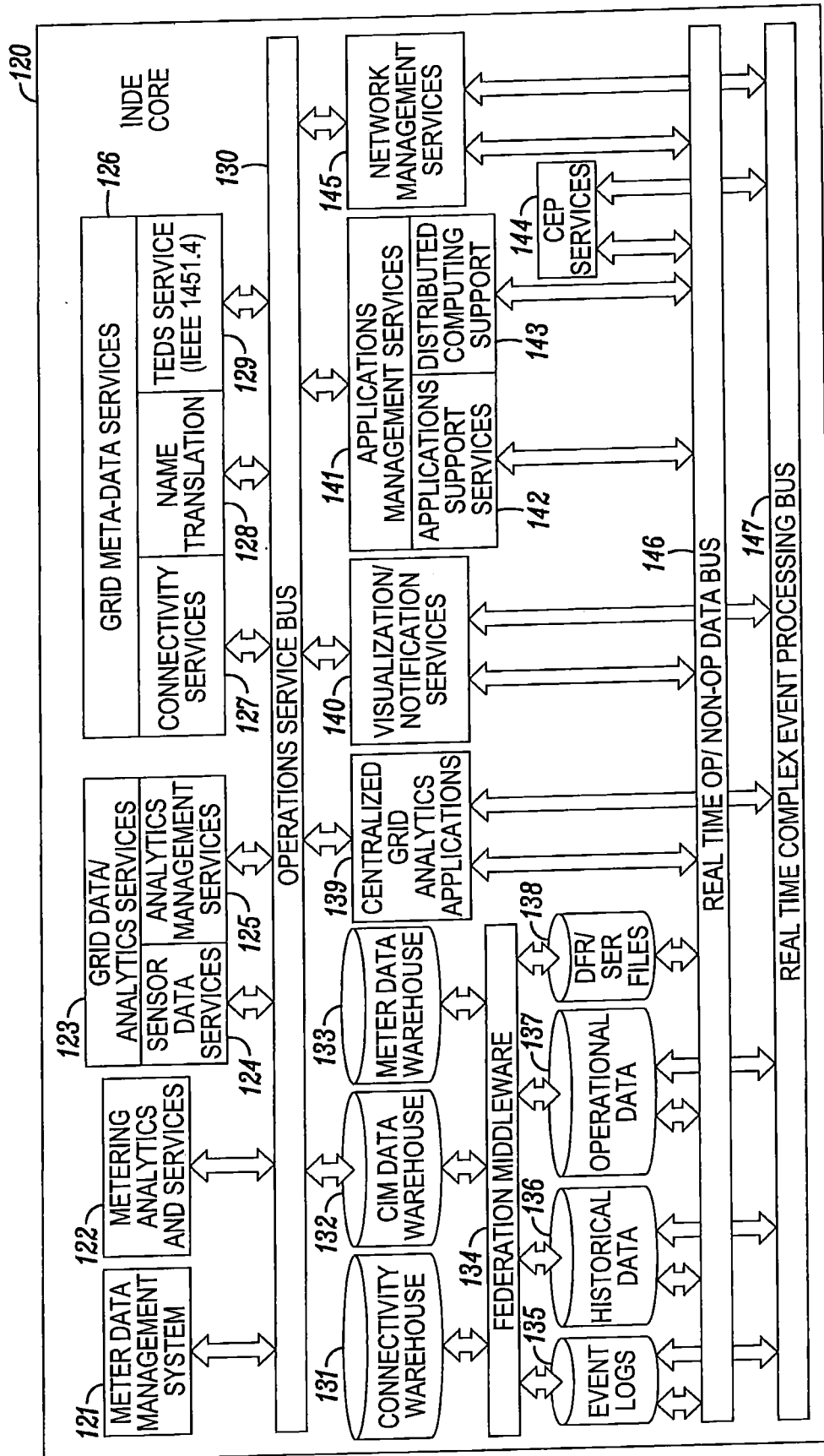
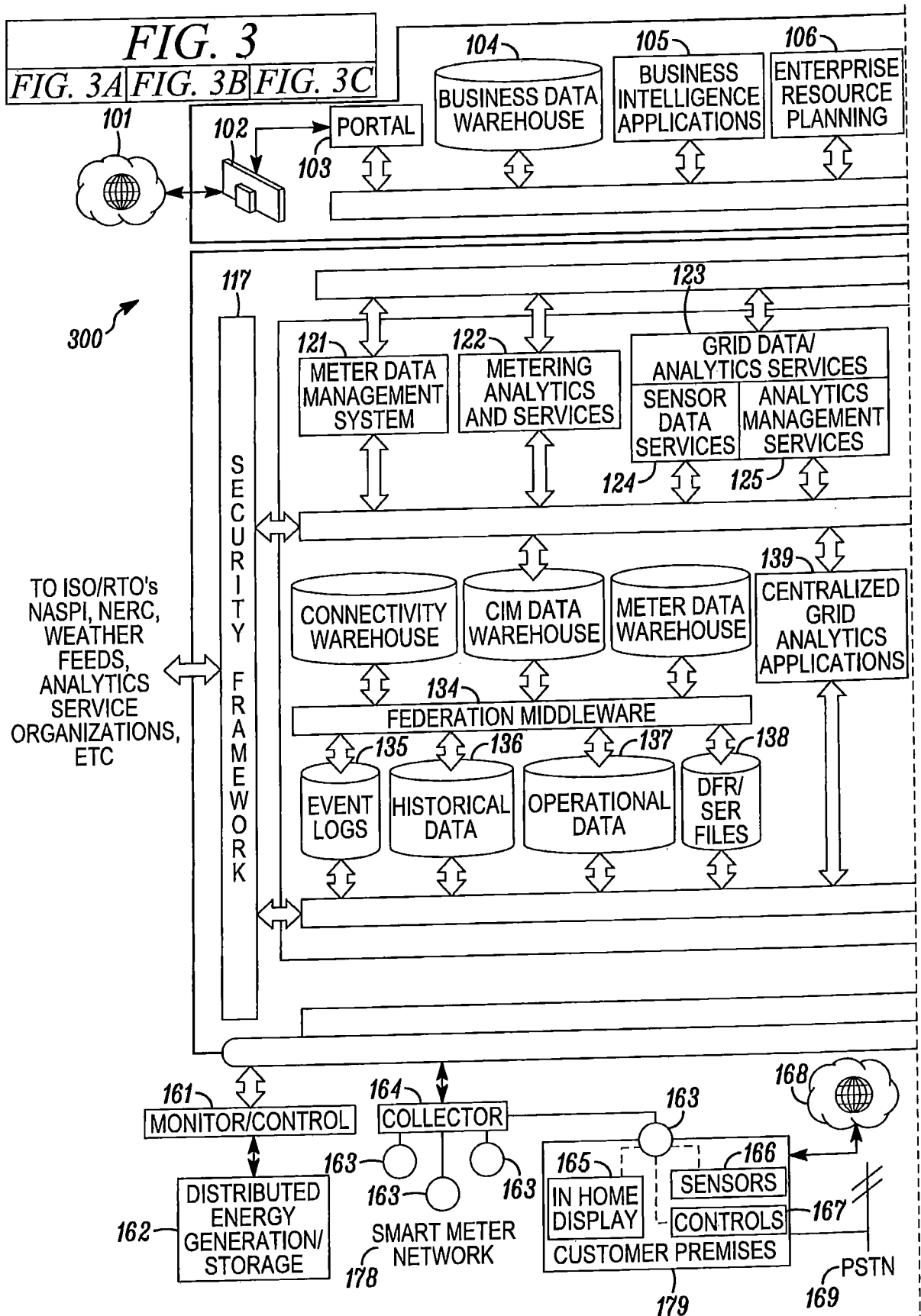


FIG. 2

**FIG. 3A**

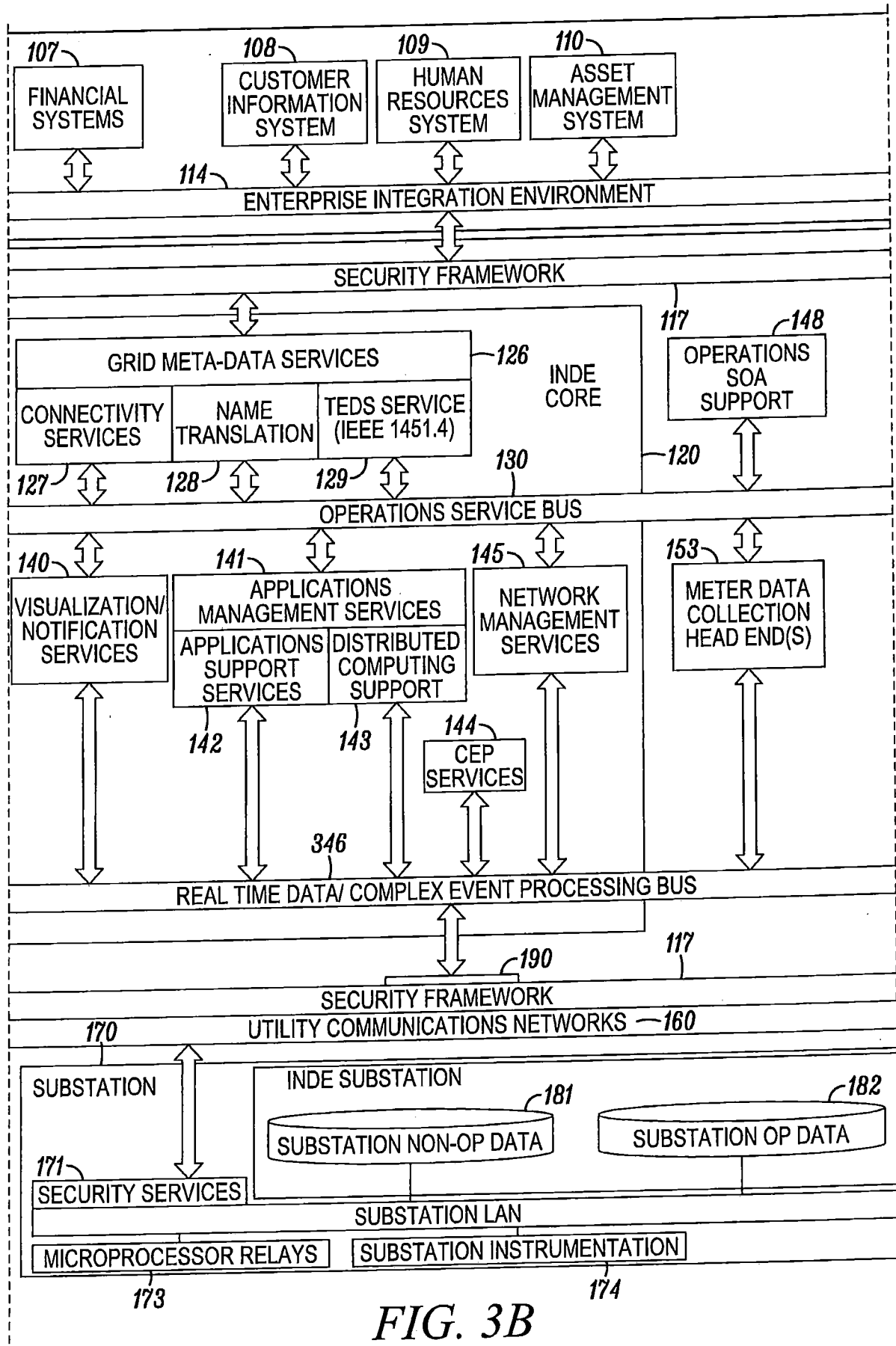
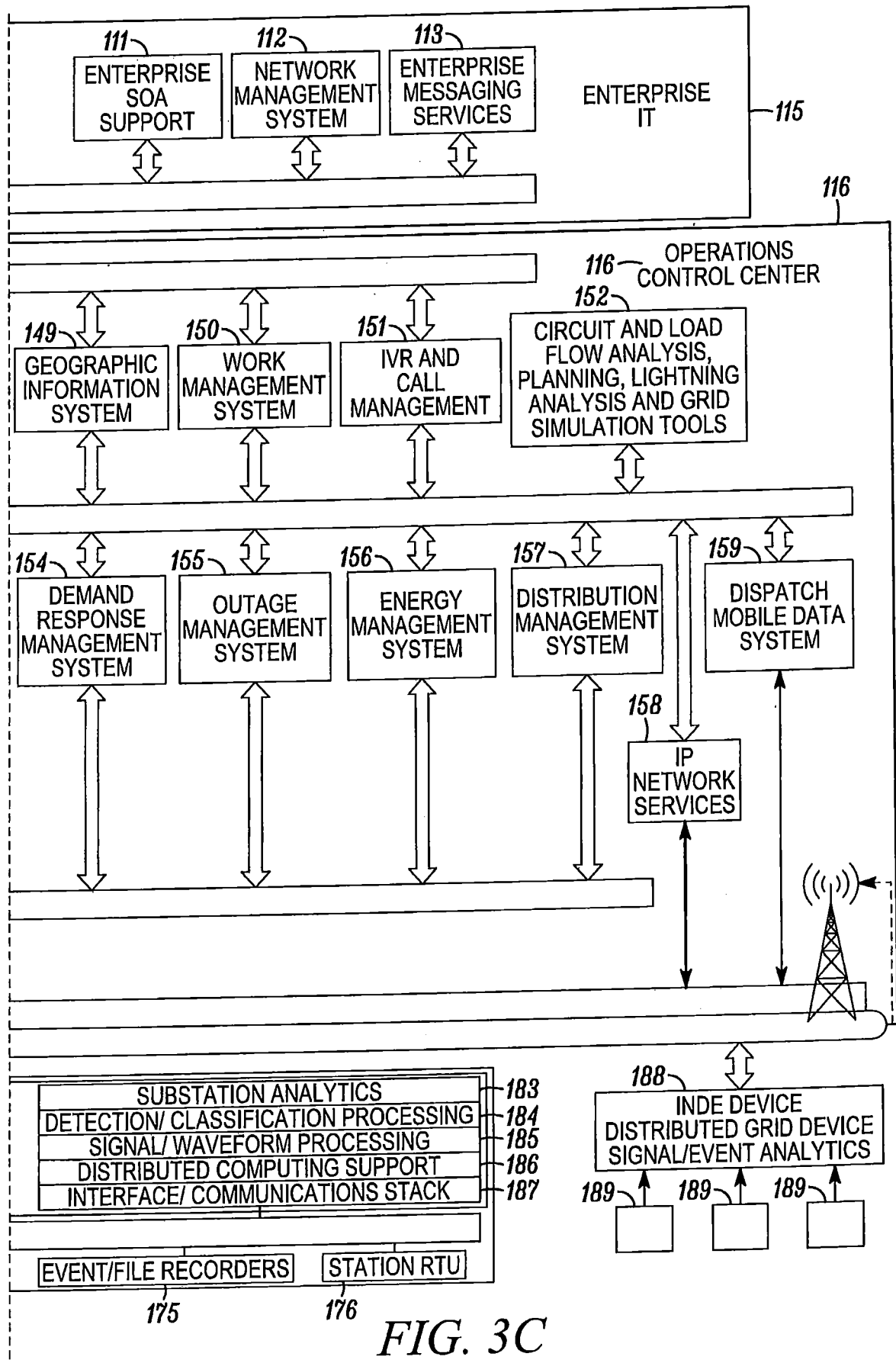


FIG. 3B



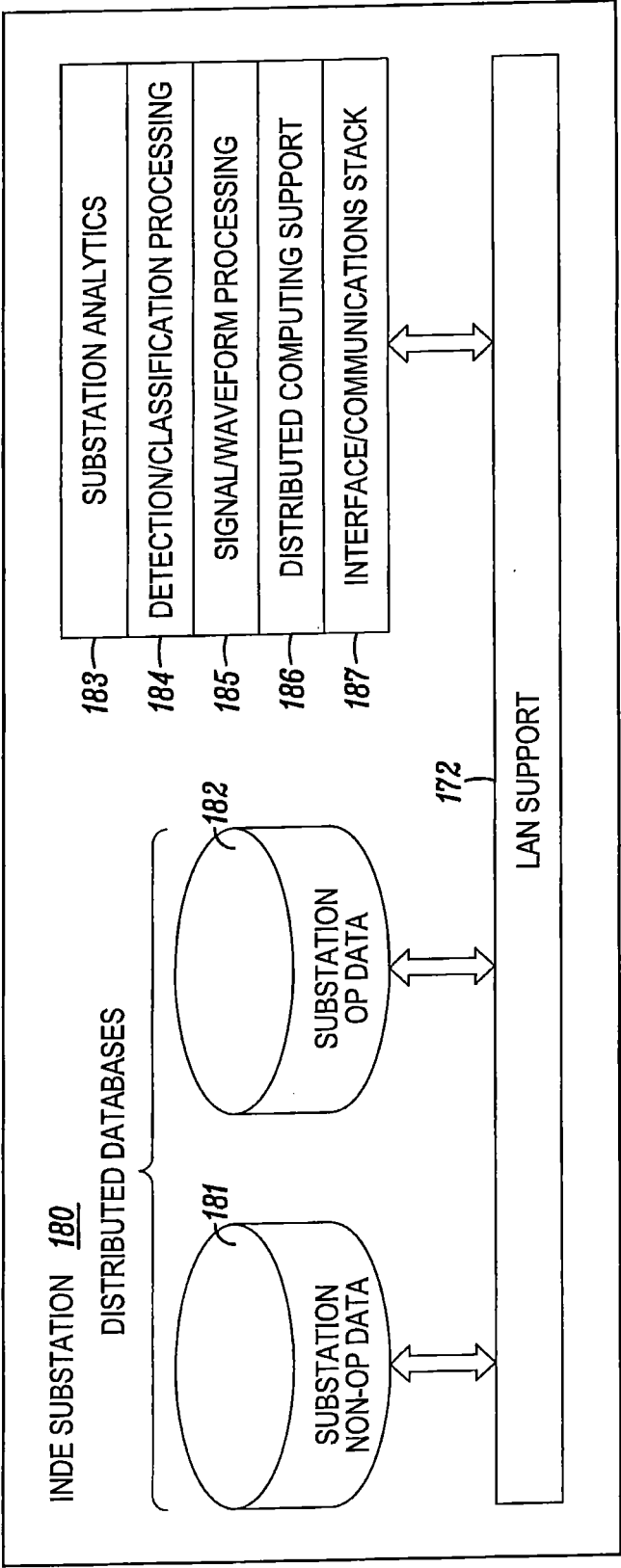


FIG. 4

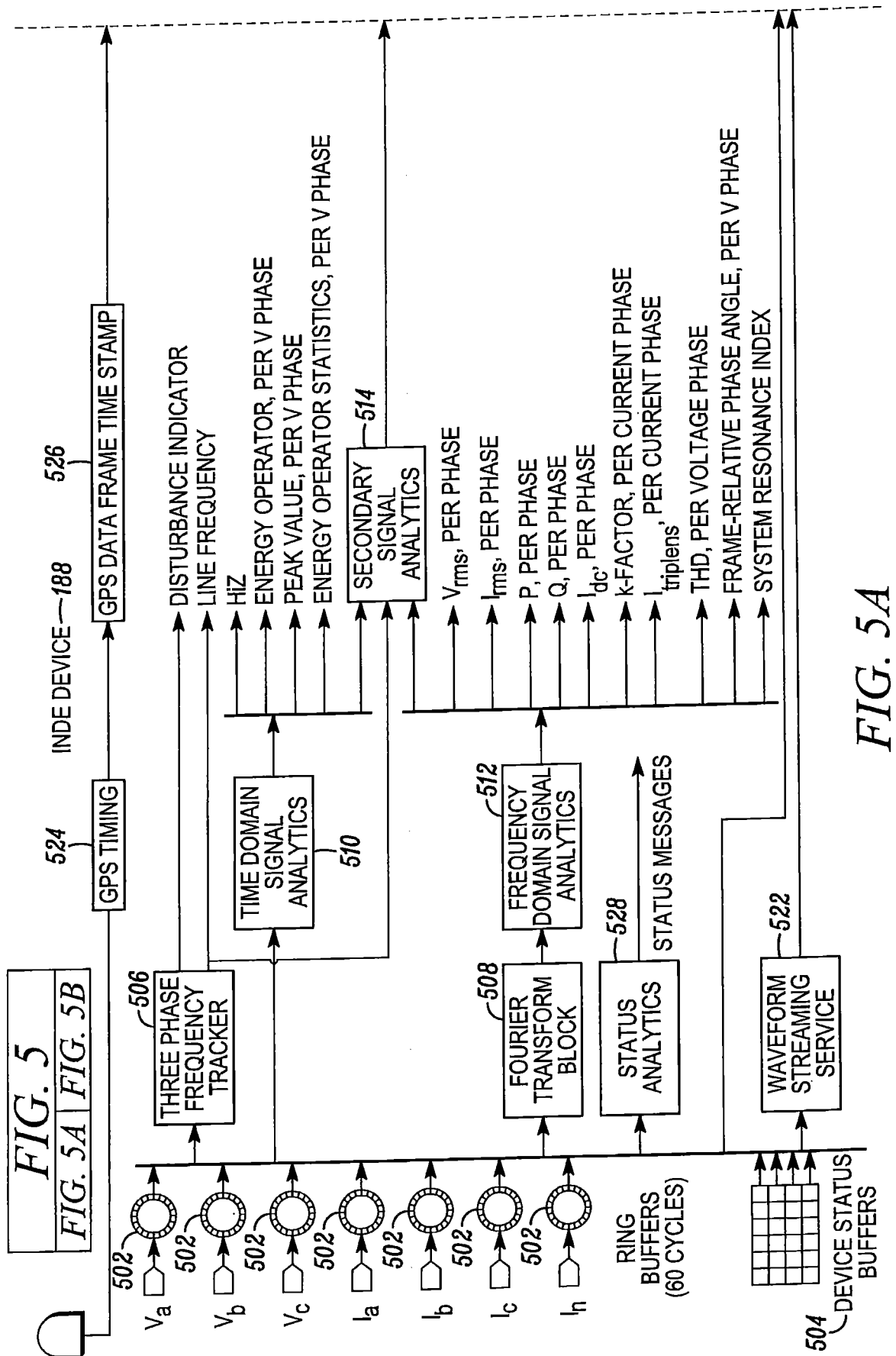


FIG. 5A

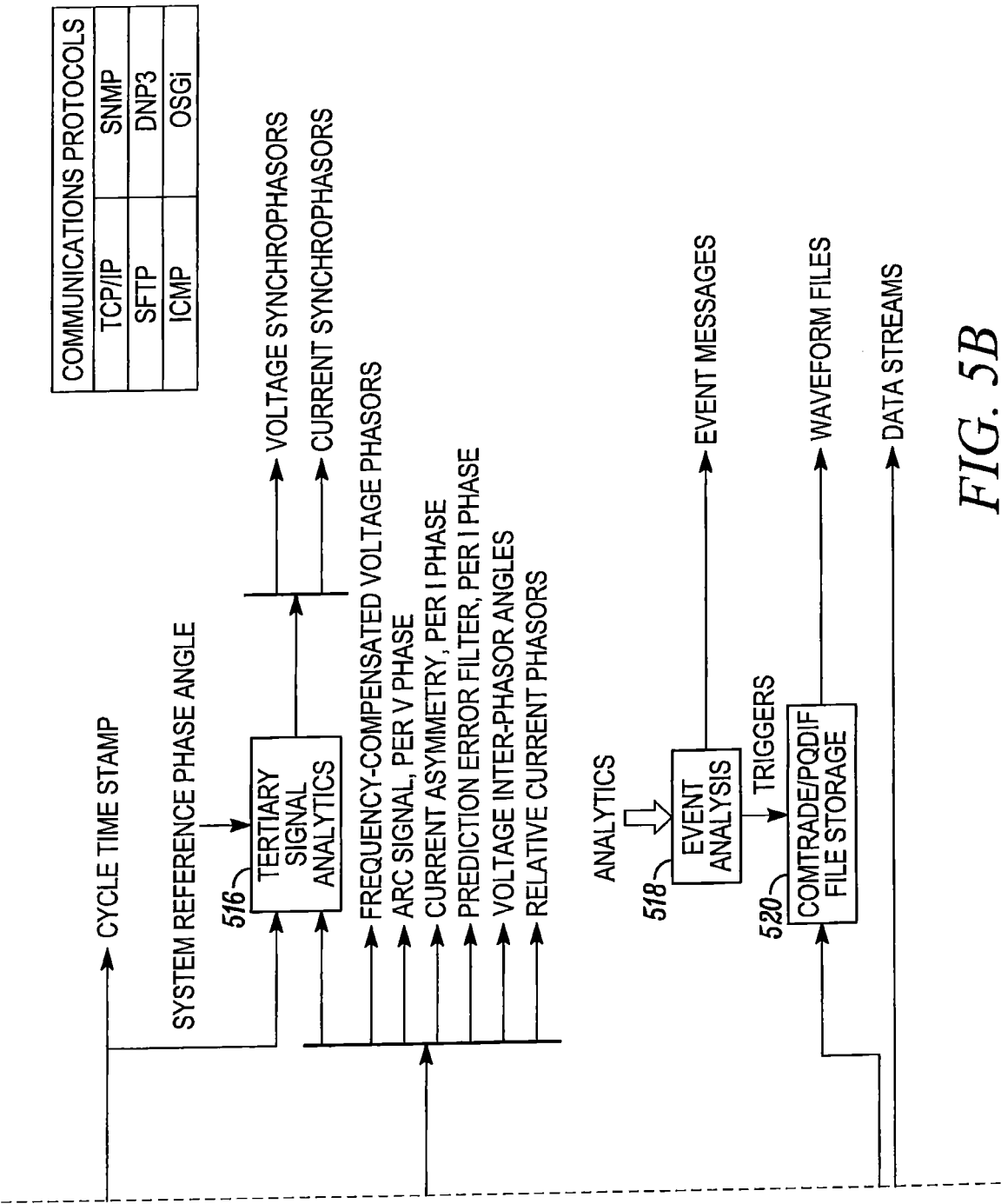
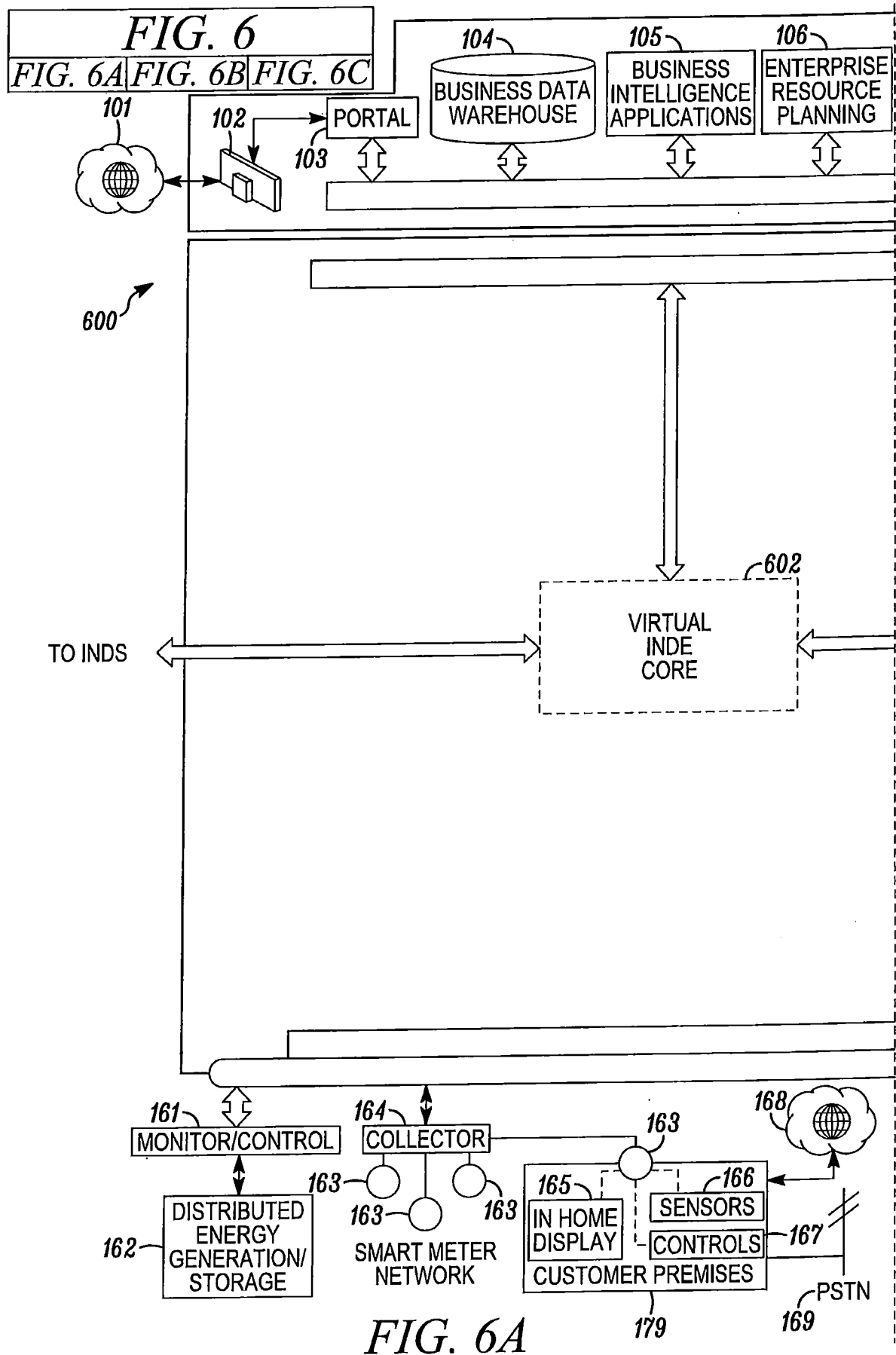
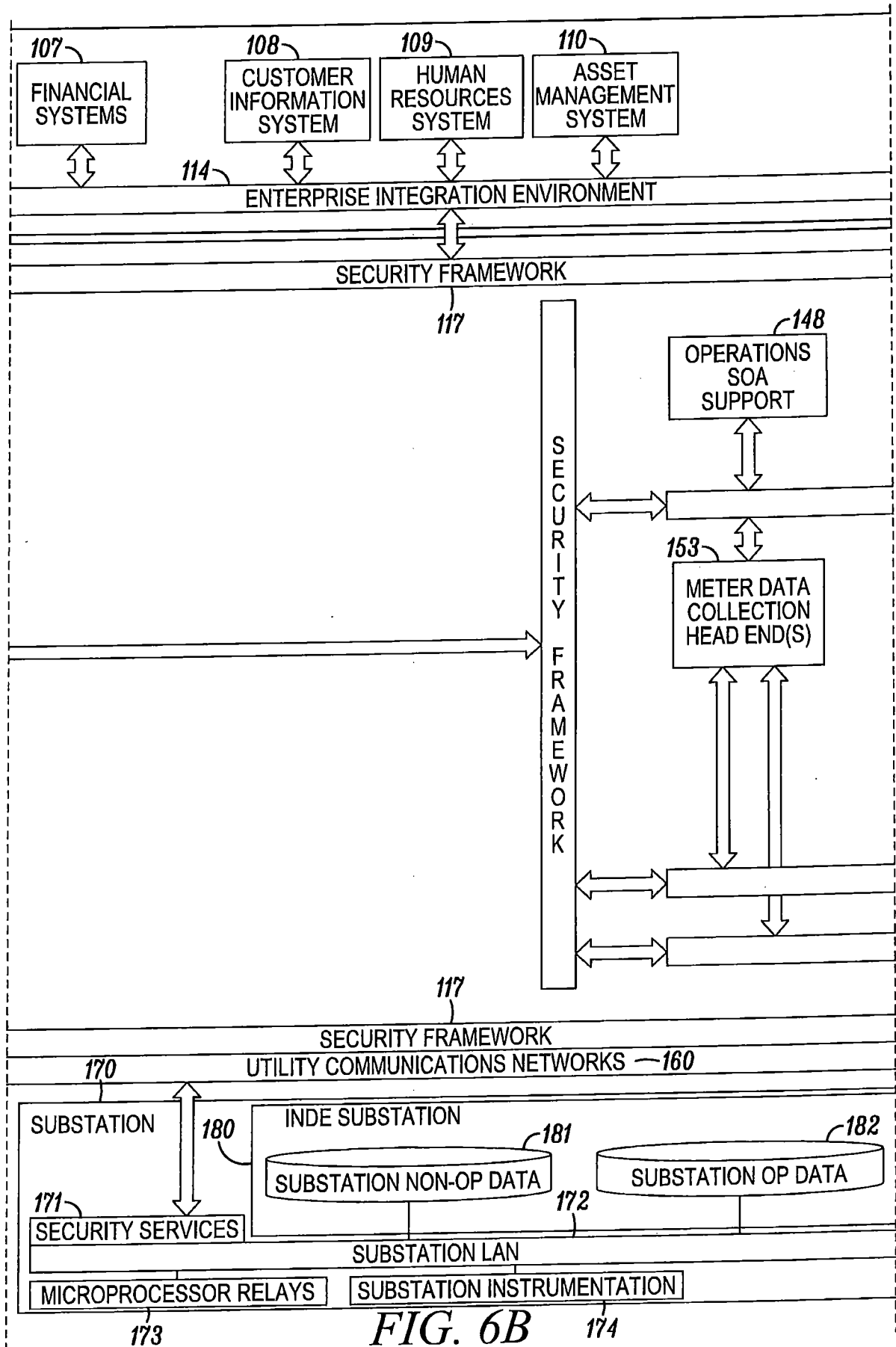


FIG. 5B





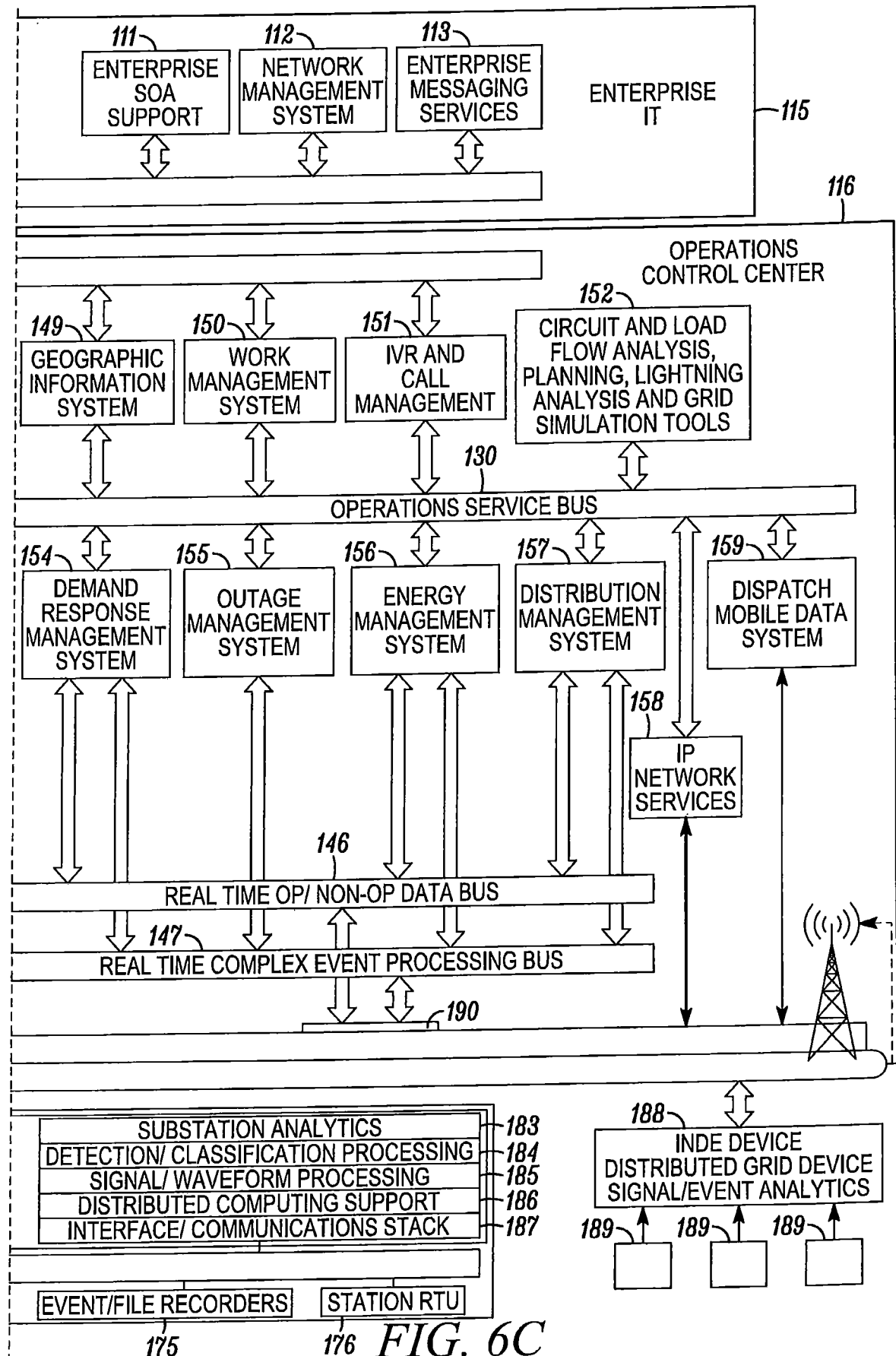


FIG. 6C

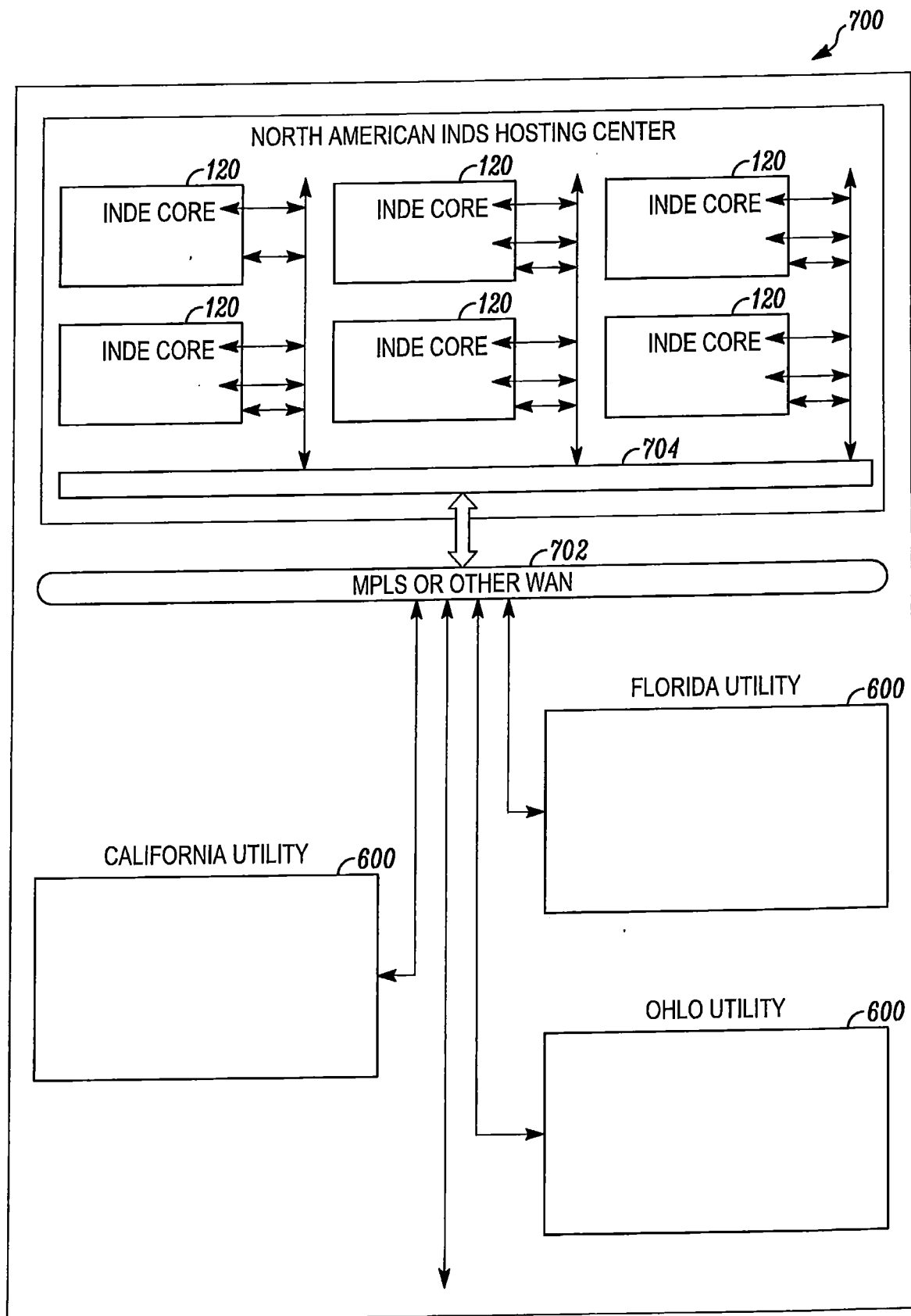


FIG. 7

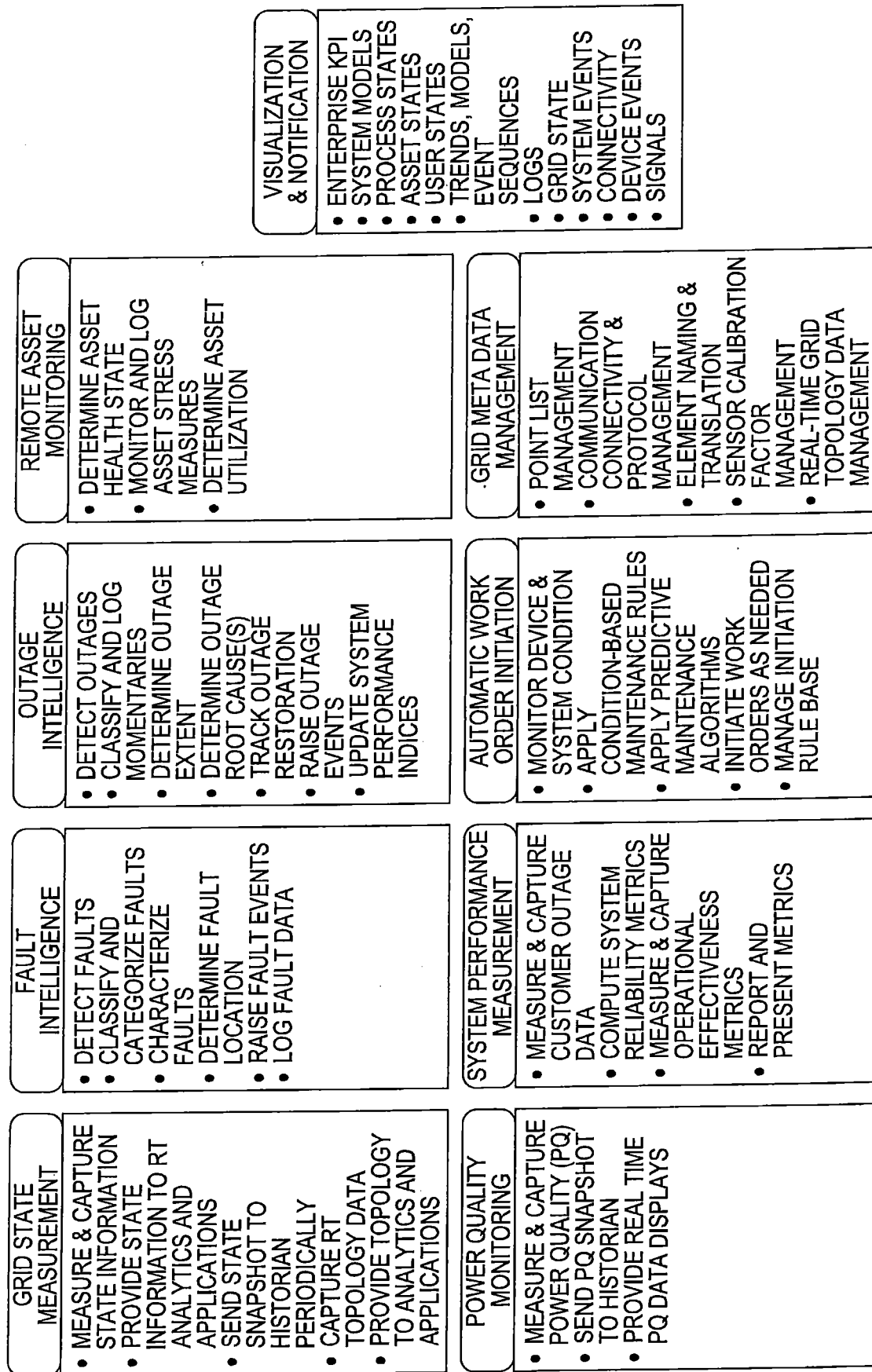
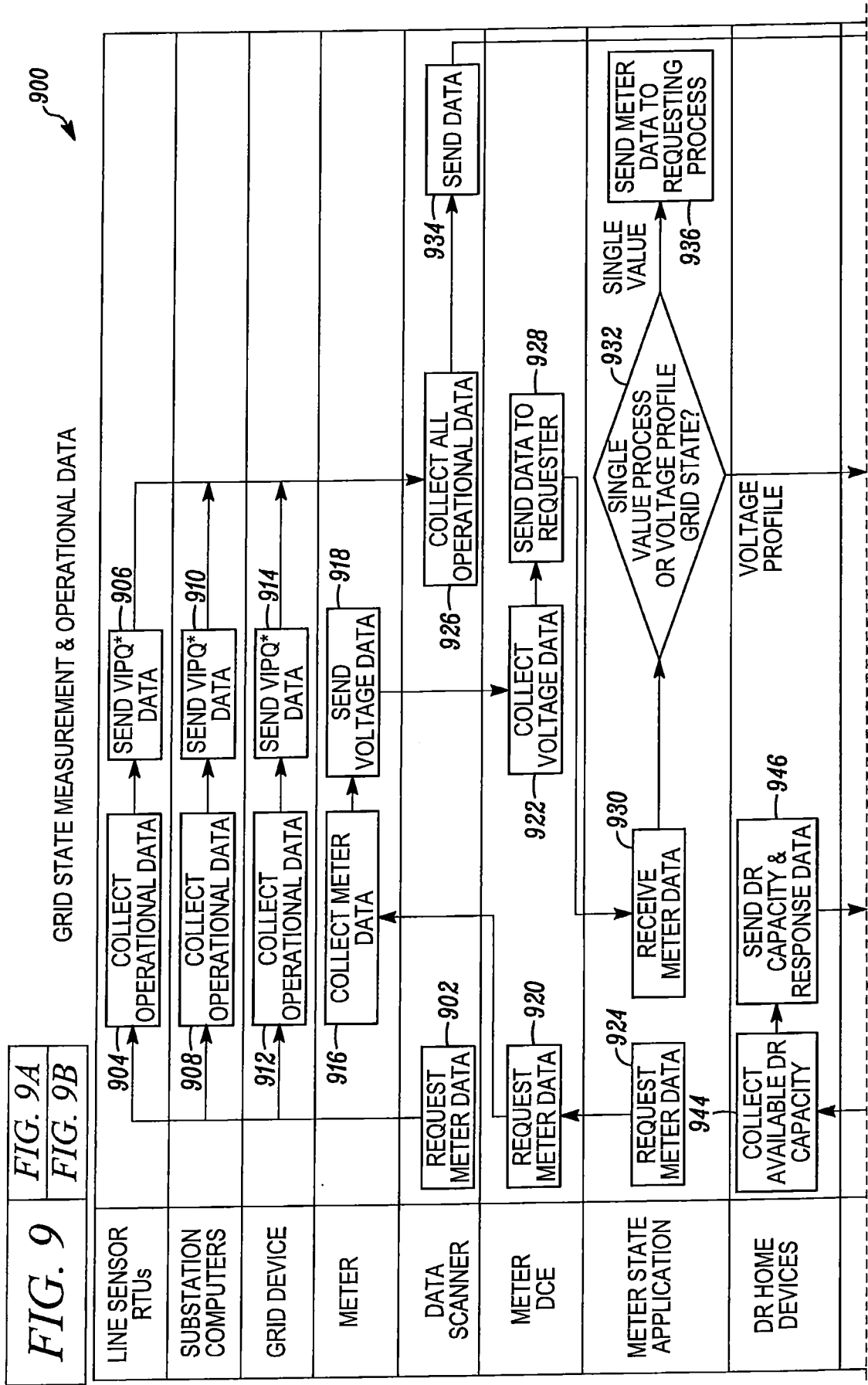
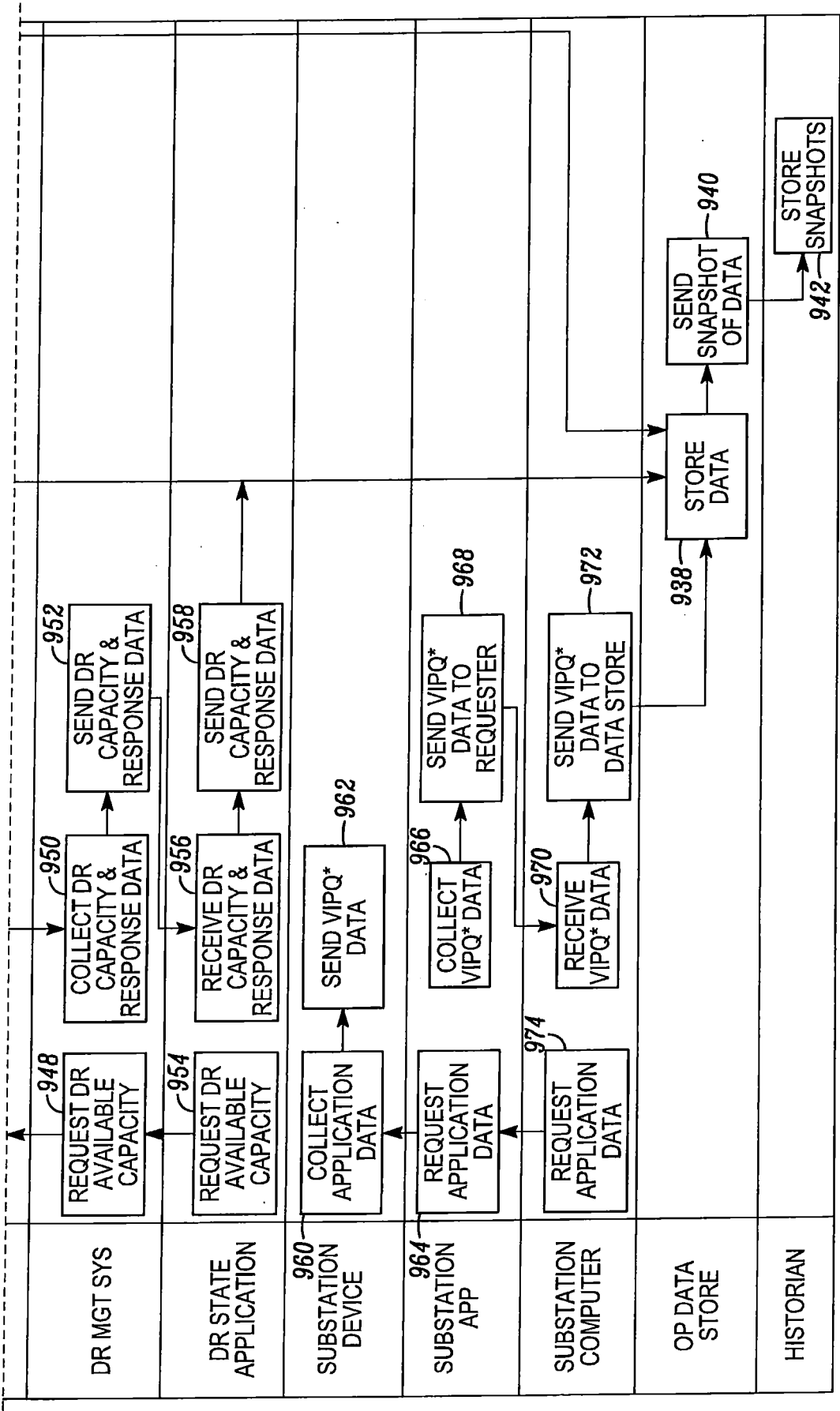


FIG. 8





* V = VOLTAGE, I = CURRENT, P = REAL POWER, Q = REACTIVE POWER

FIG. 9B

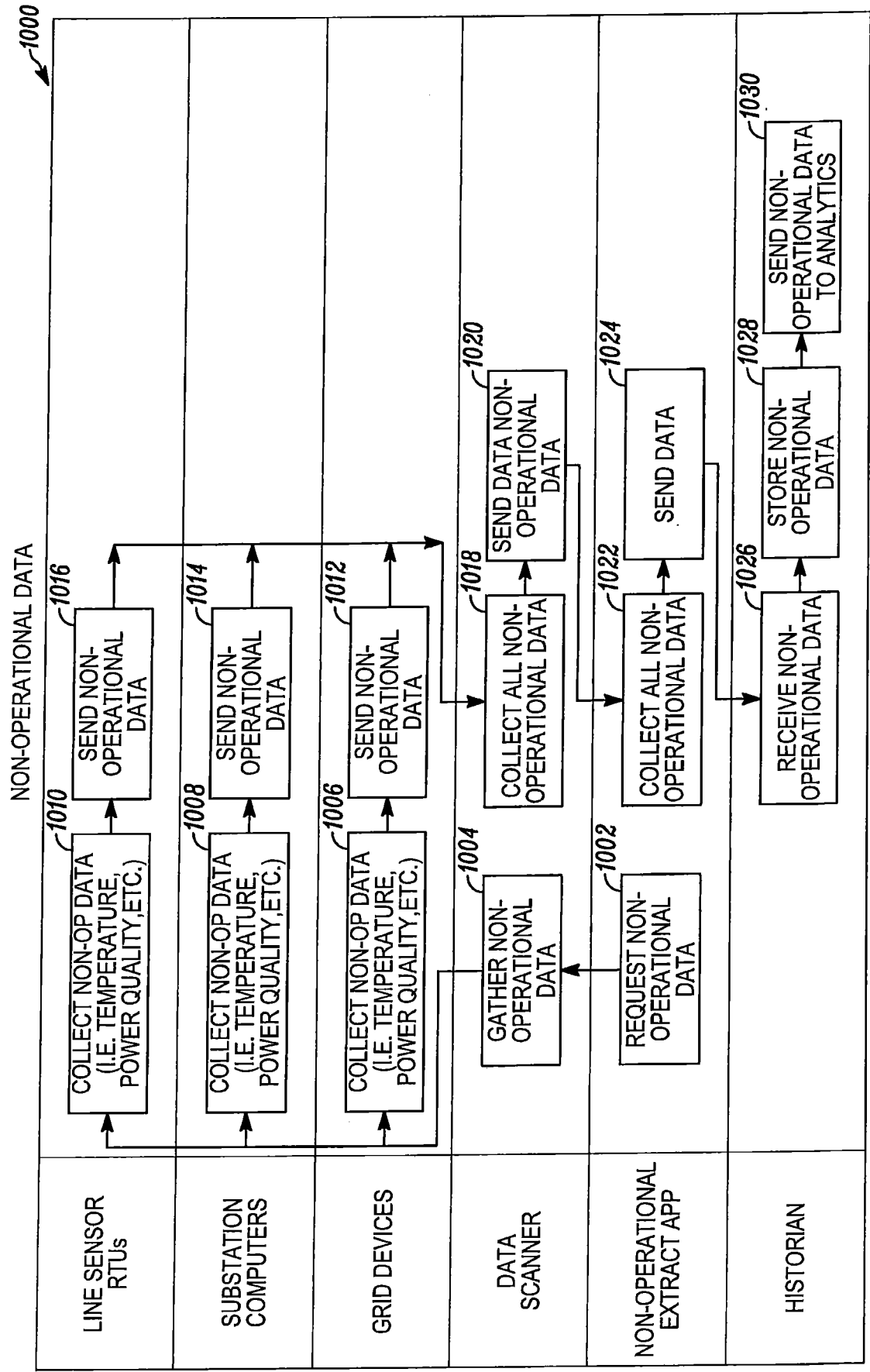


FIG. 10

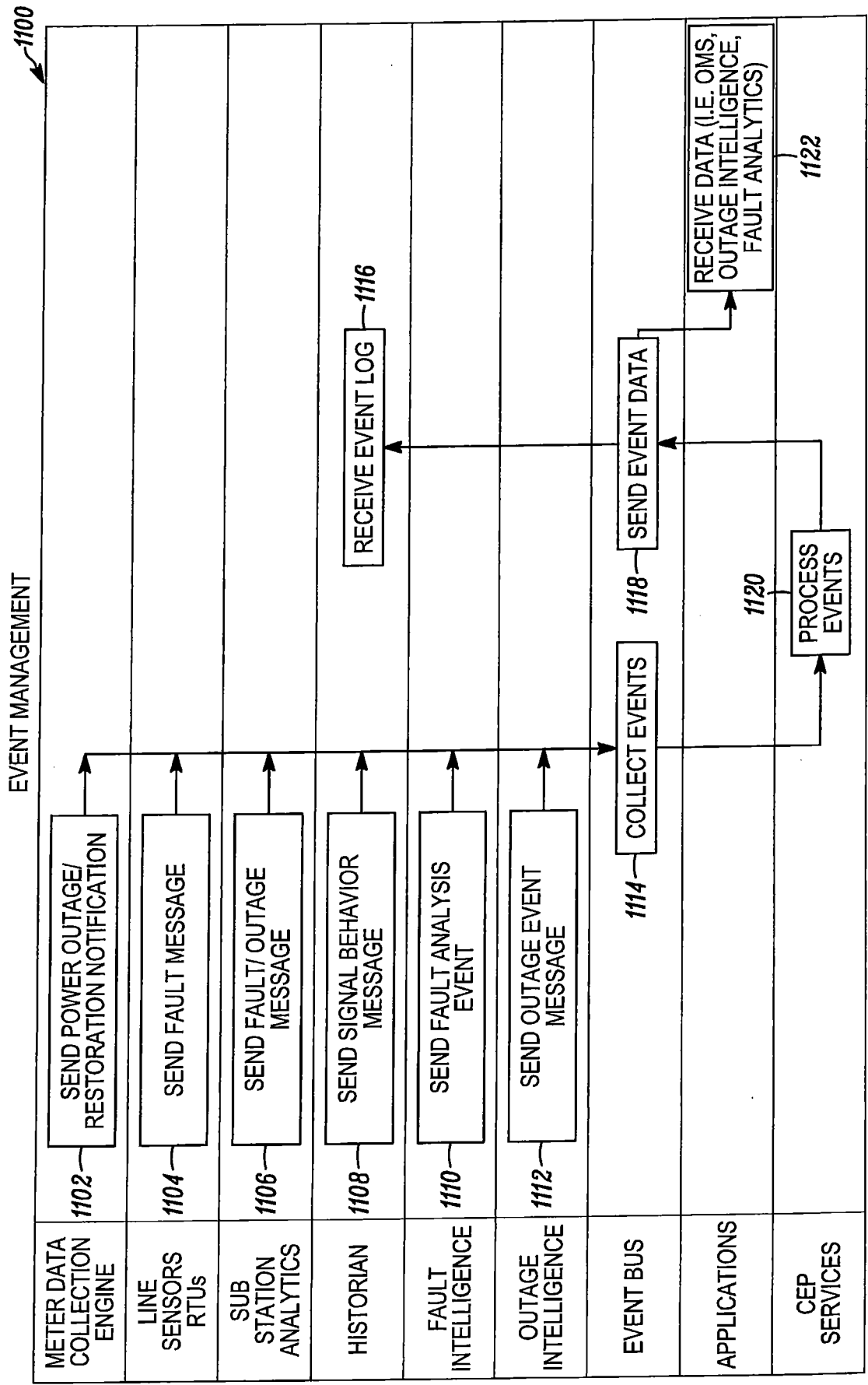
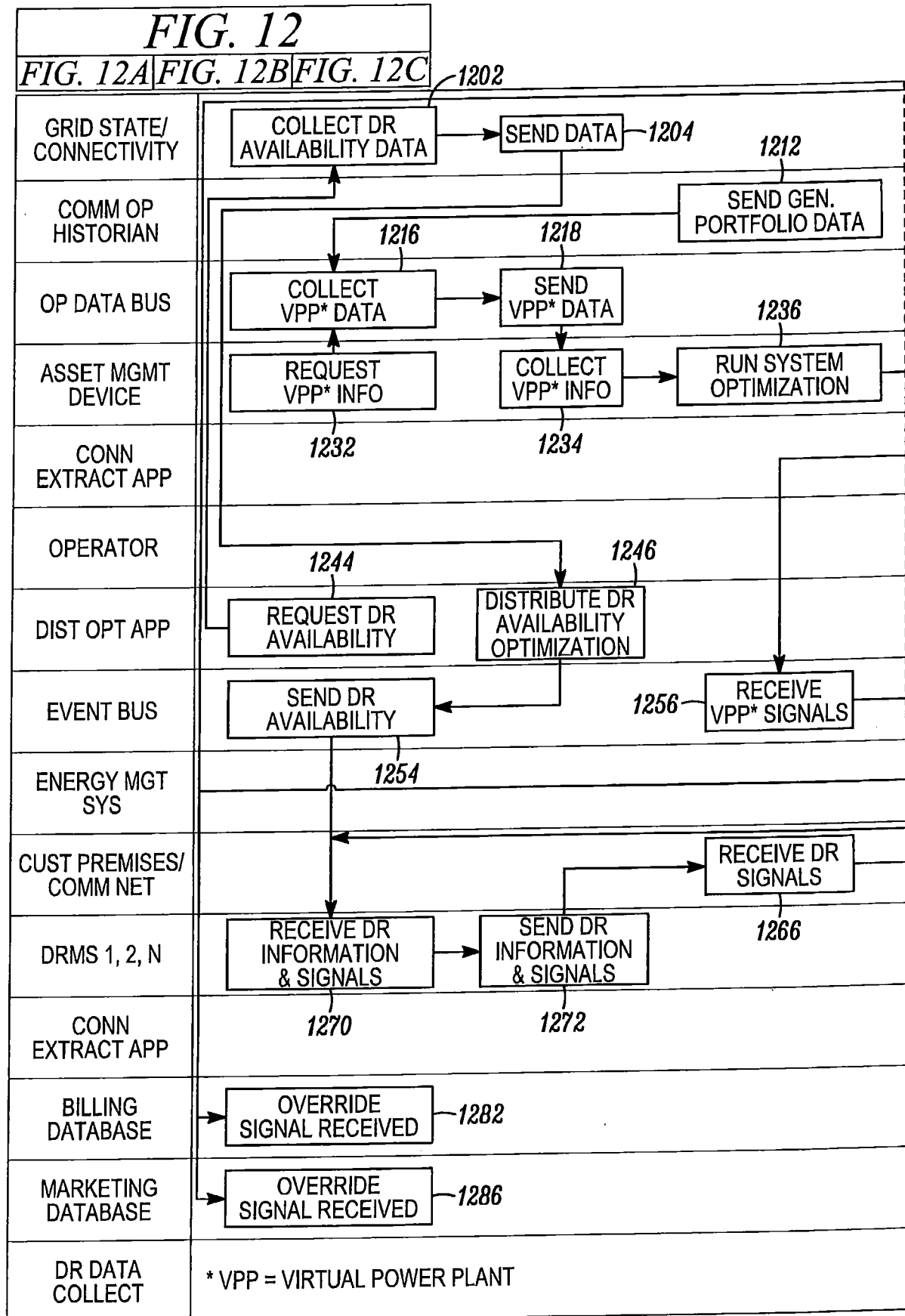


FIG. 11

**FIG. 12A**

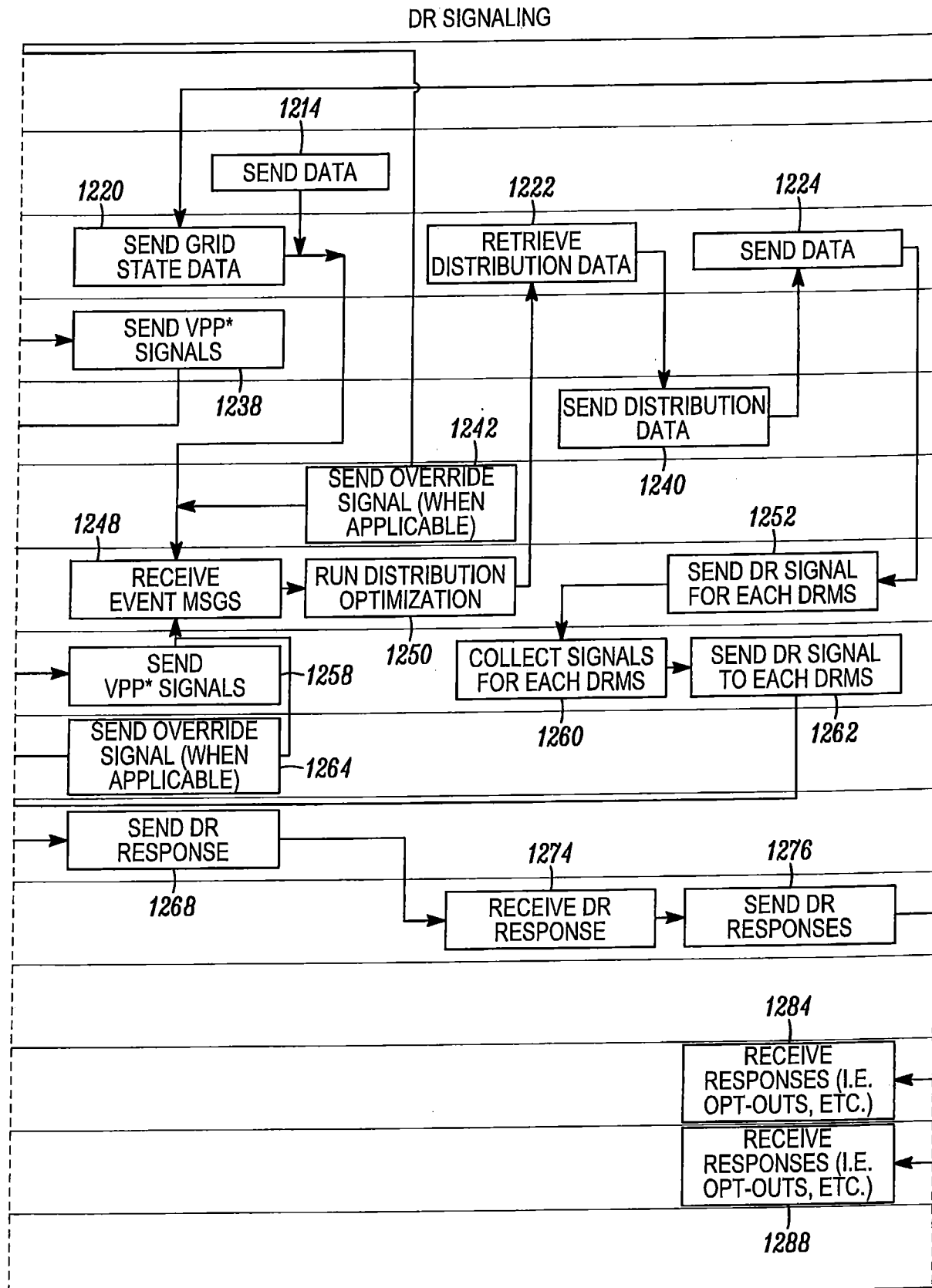


FIG. 12B

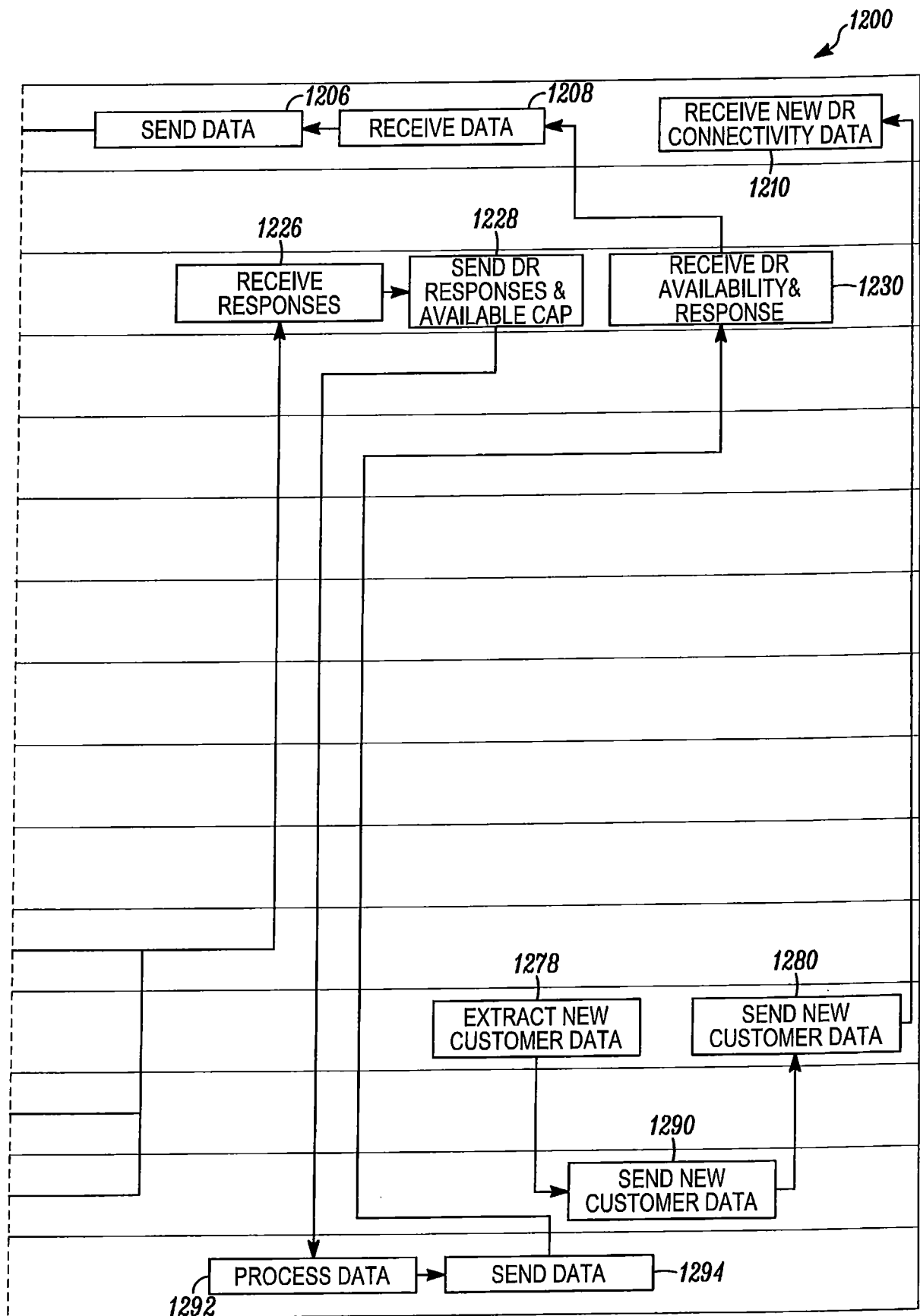
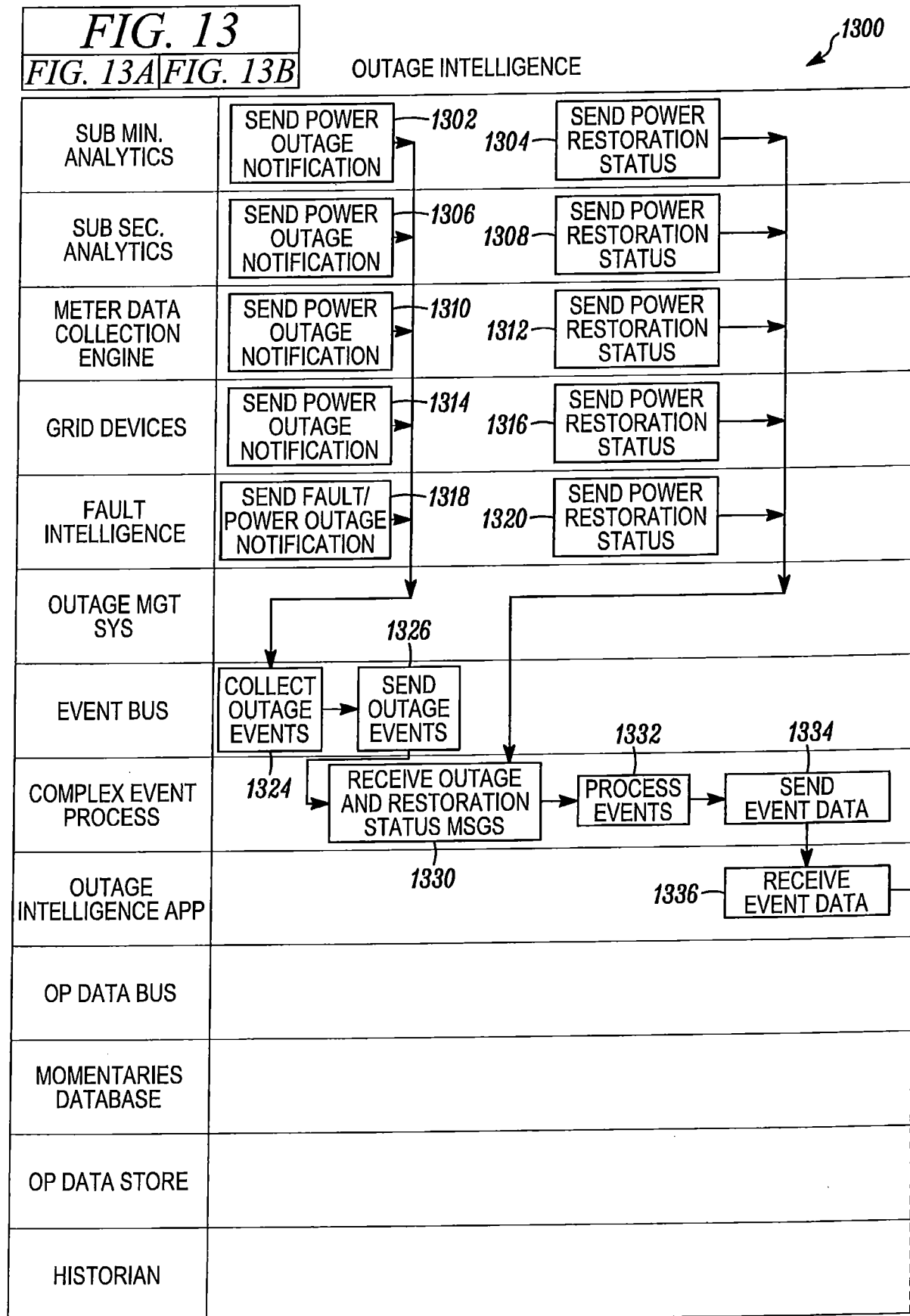


FIG. 12C

**FIG. 13A**

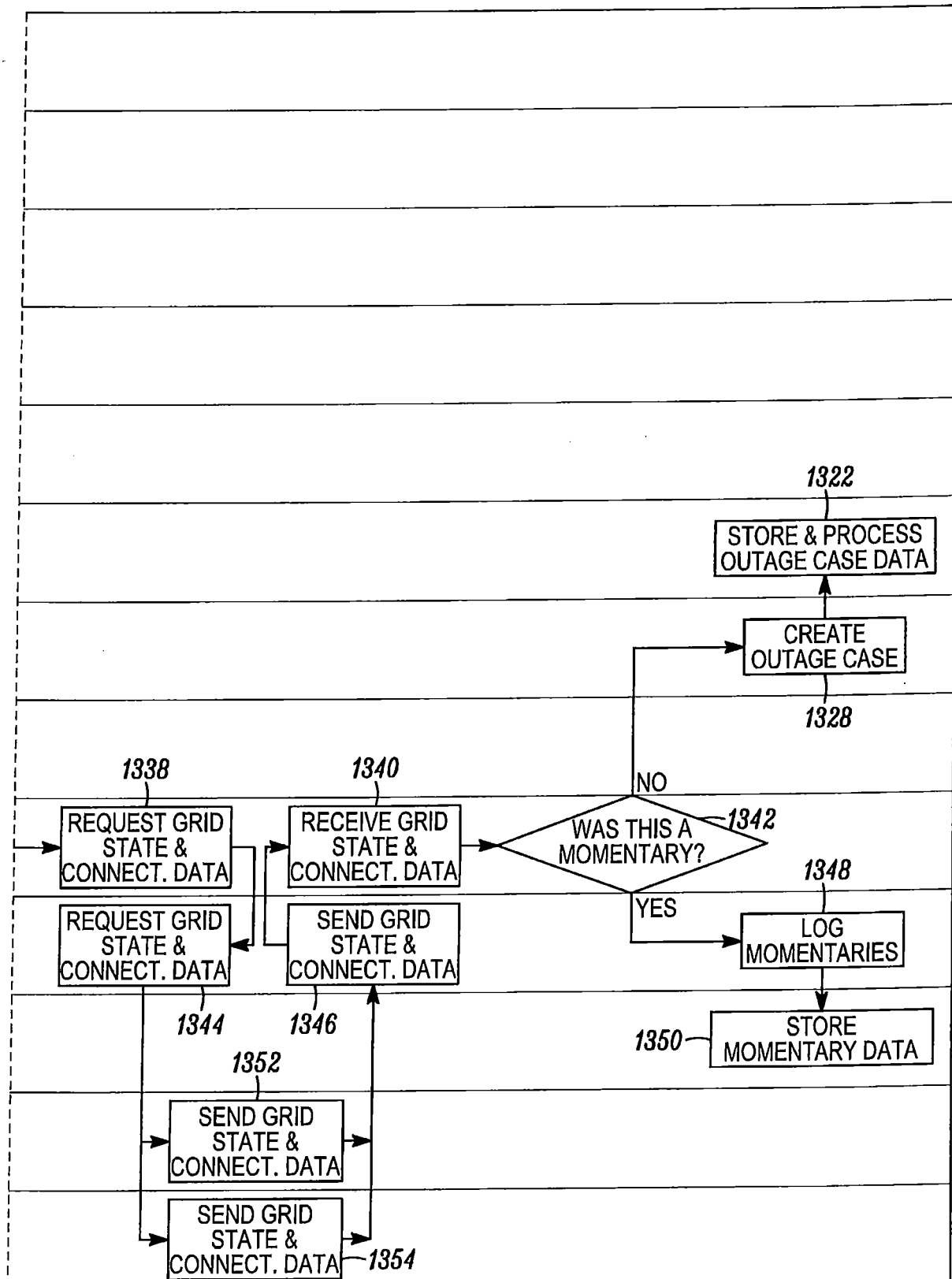


FIG. 13B

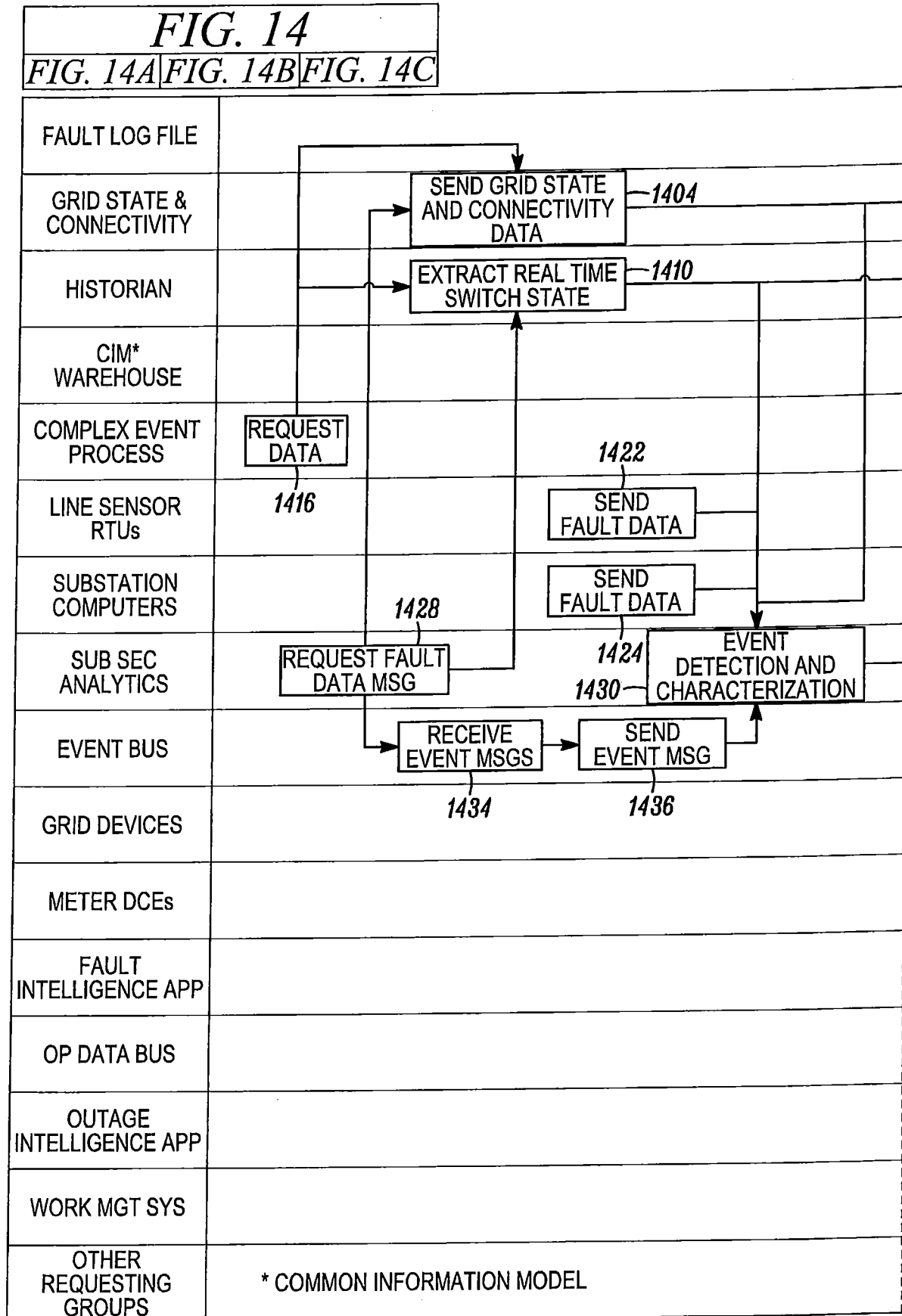


FIG. 14A

FAULT INTELLIGENCE

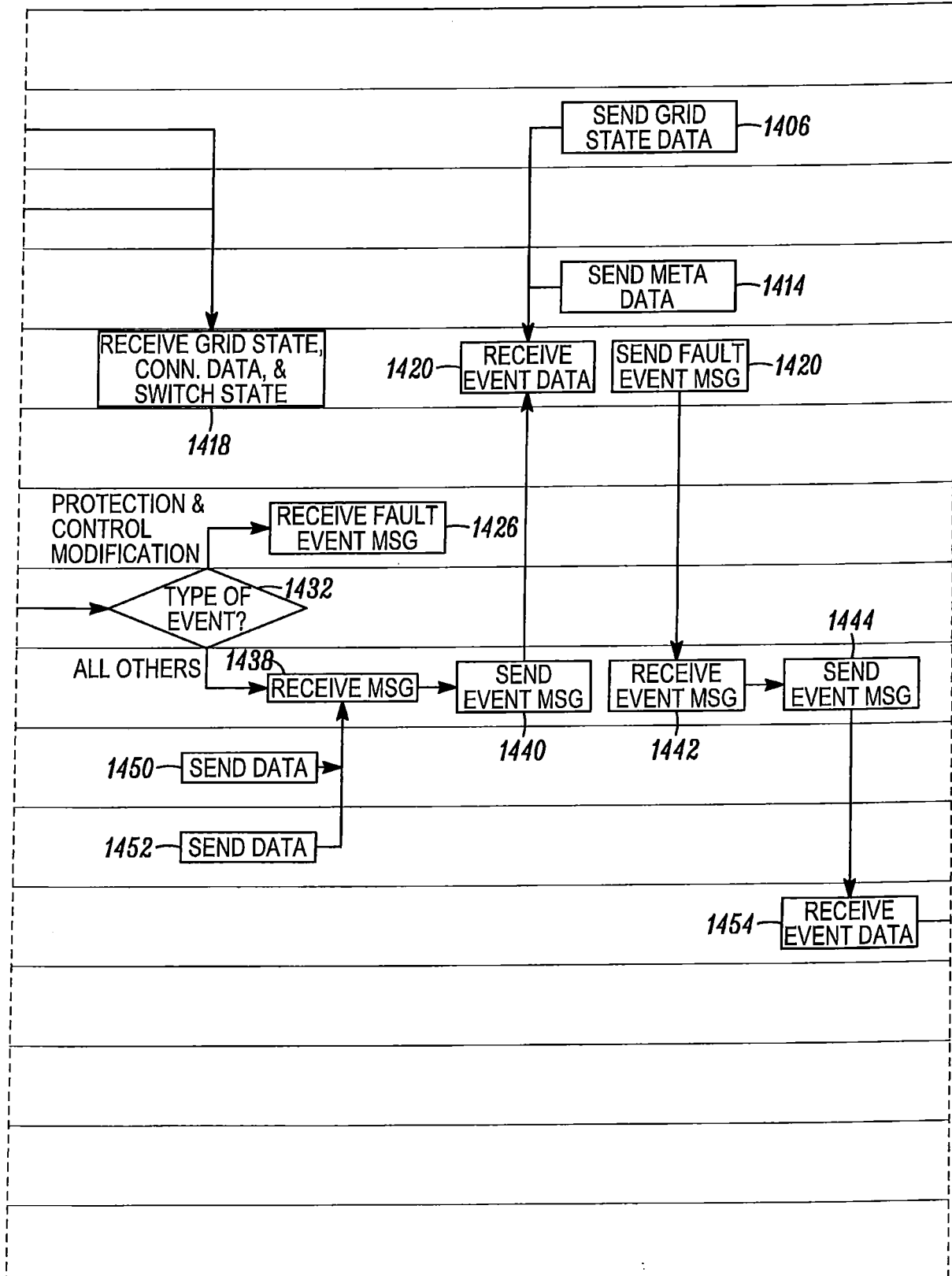


FIG. 14B

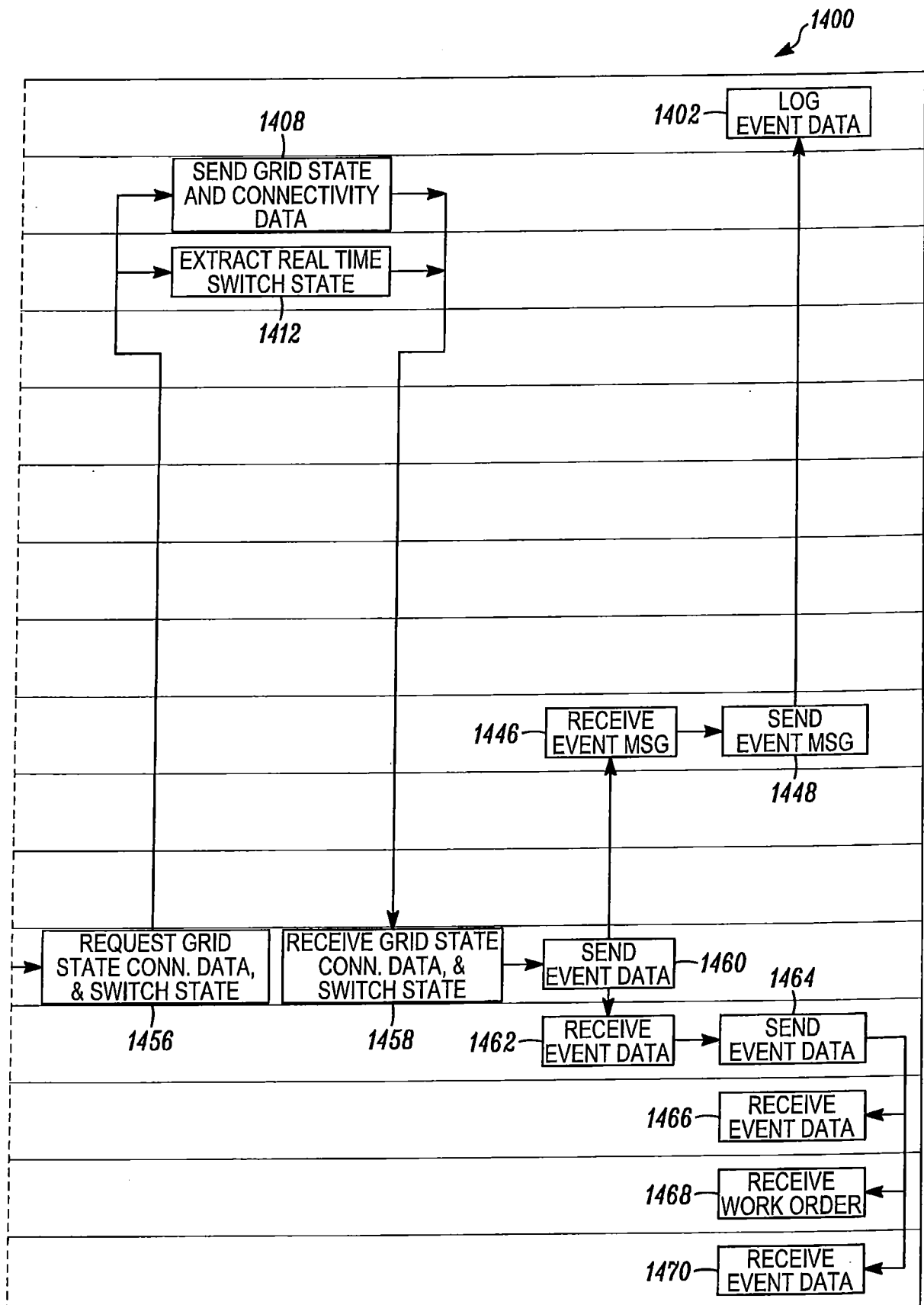
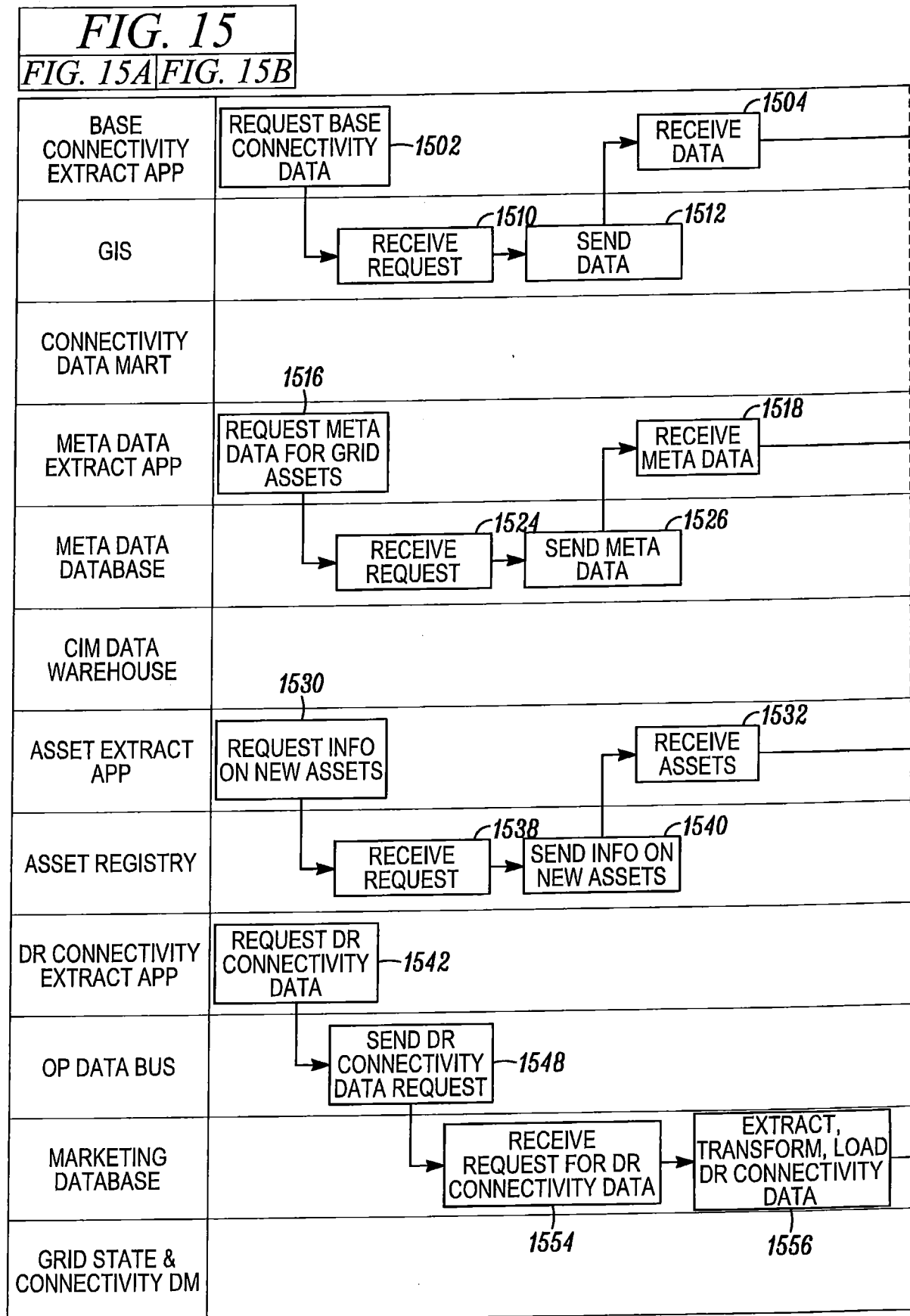


FIG. 14C

**FIG. 15A**

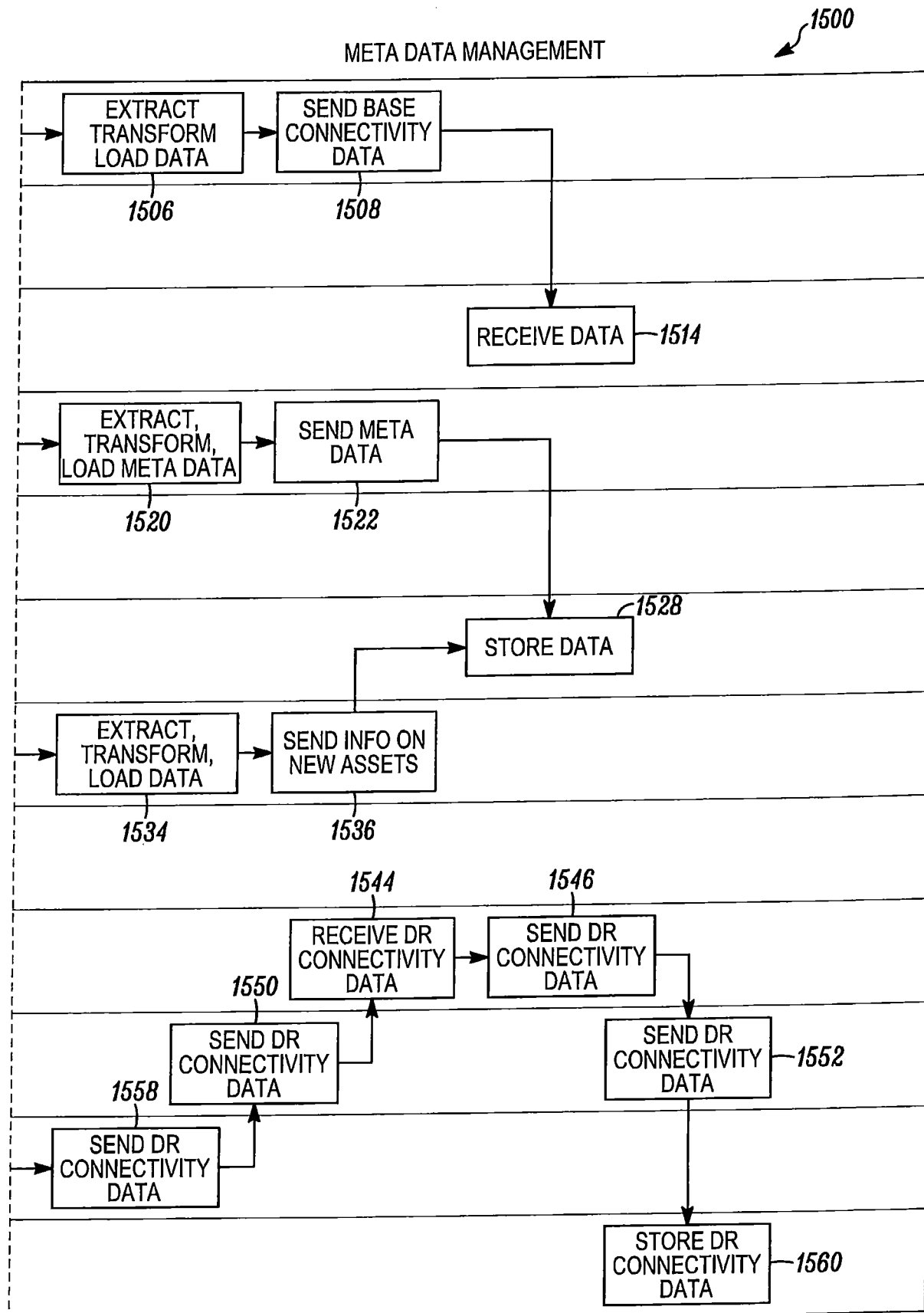


FIG. 15B

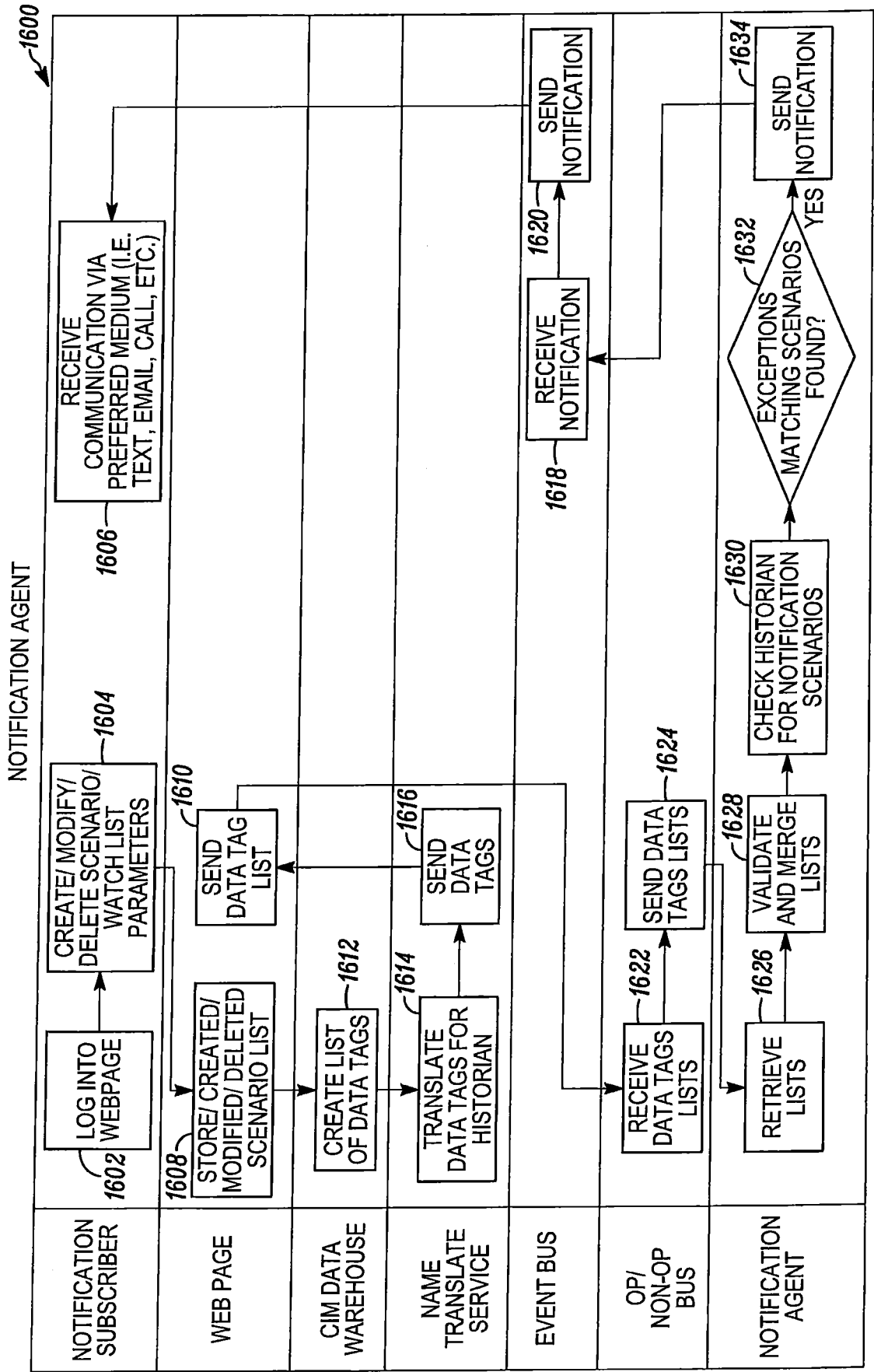


FIG. 16

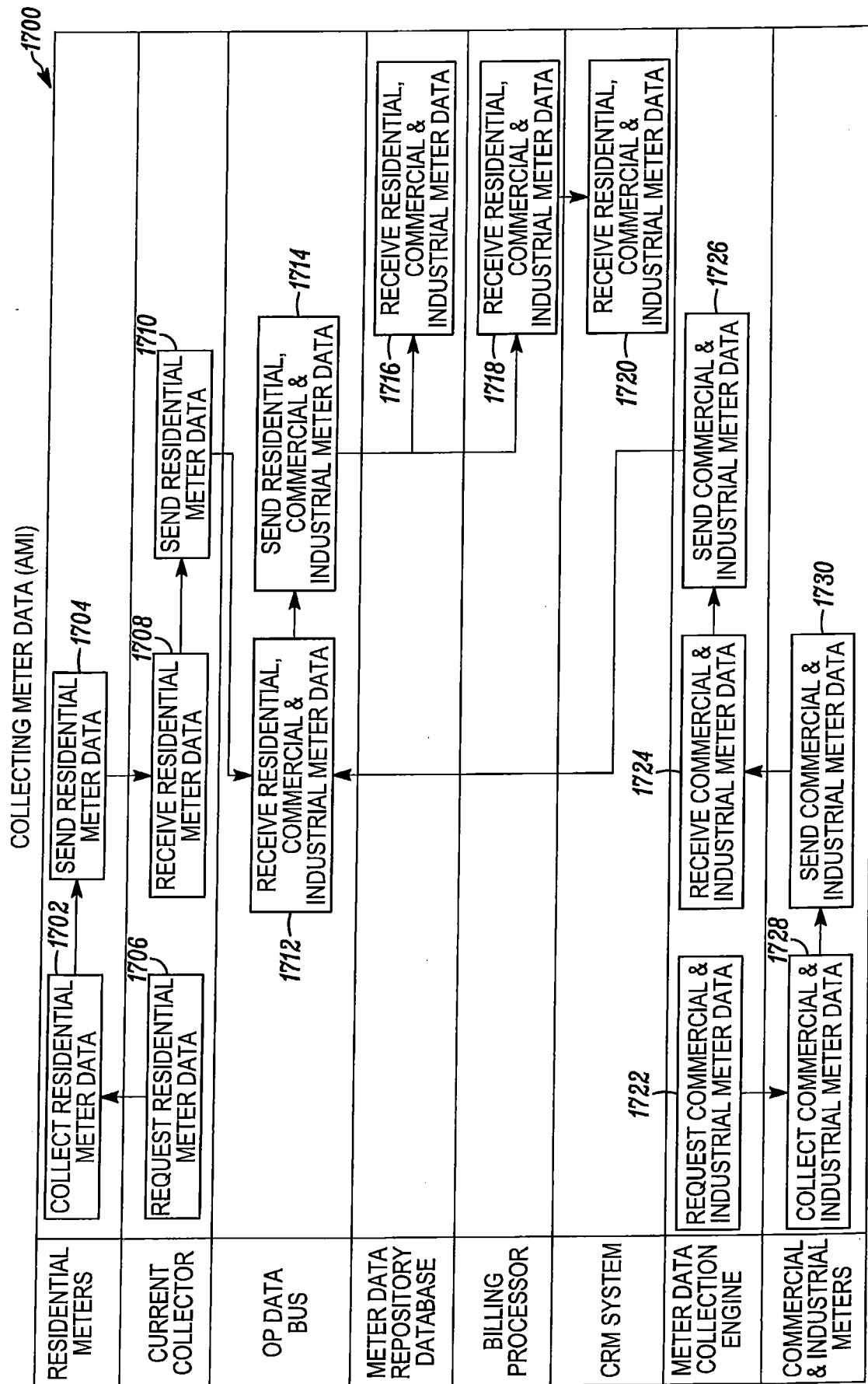
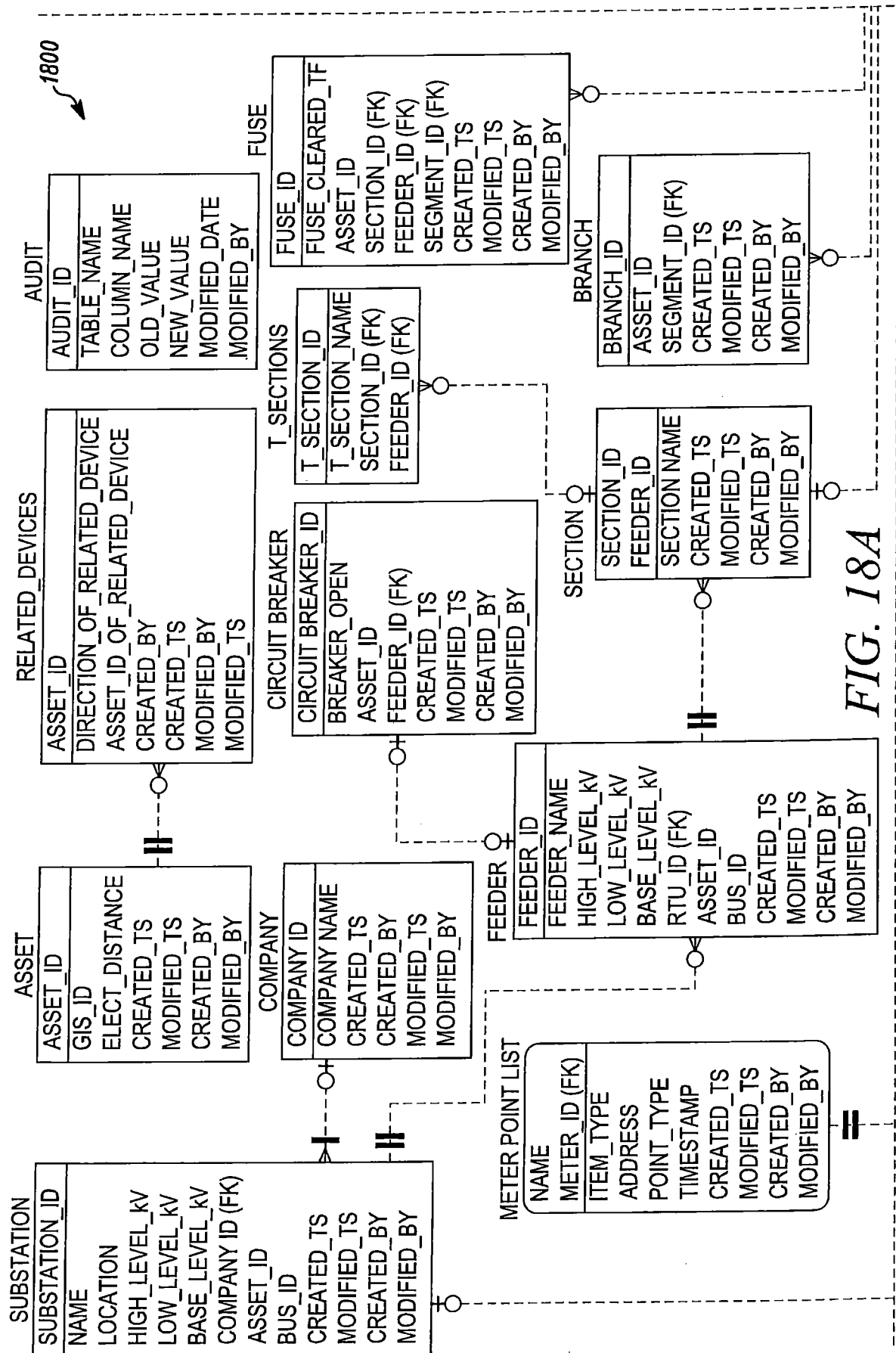
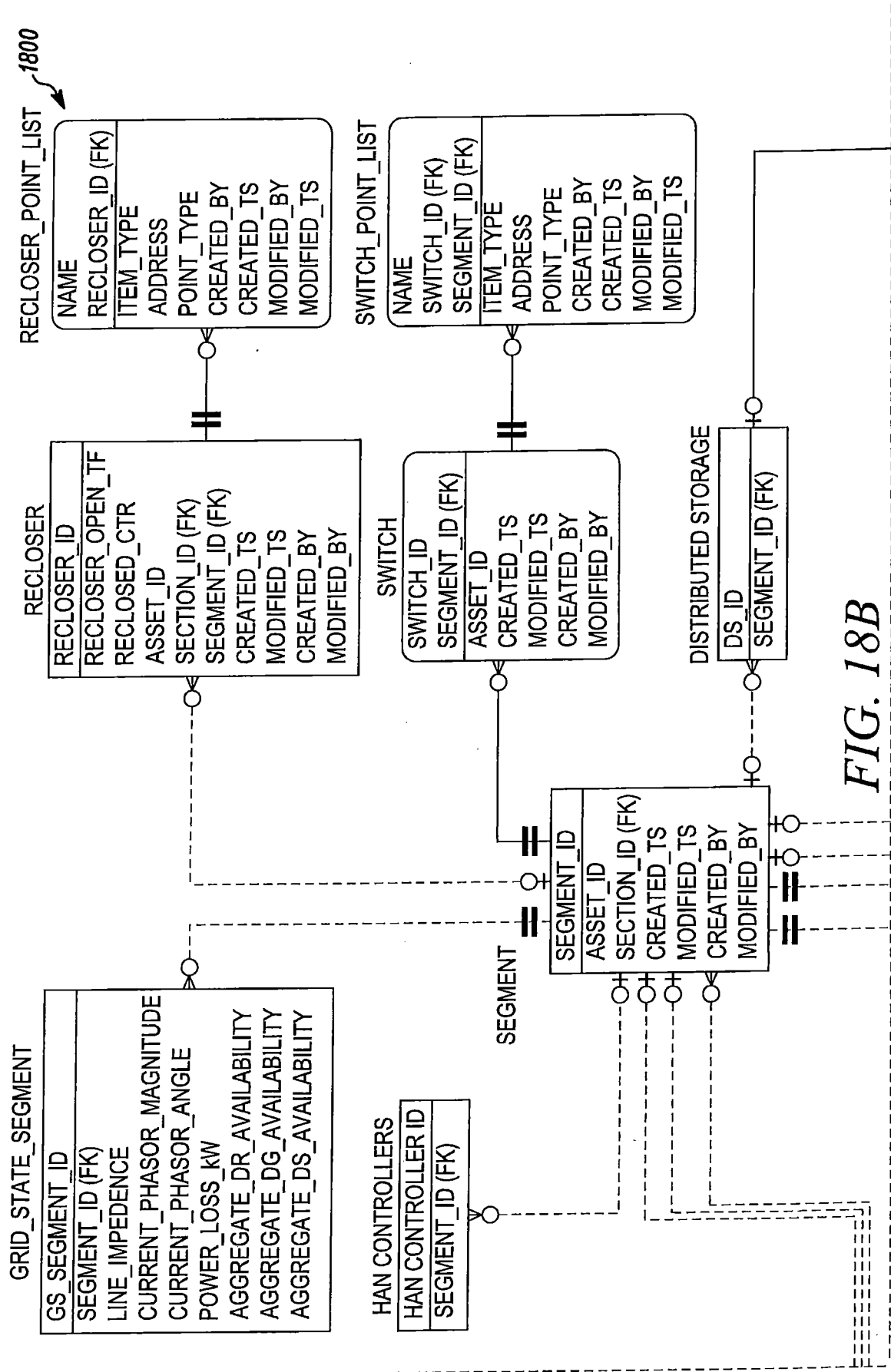
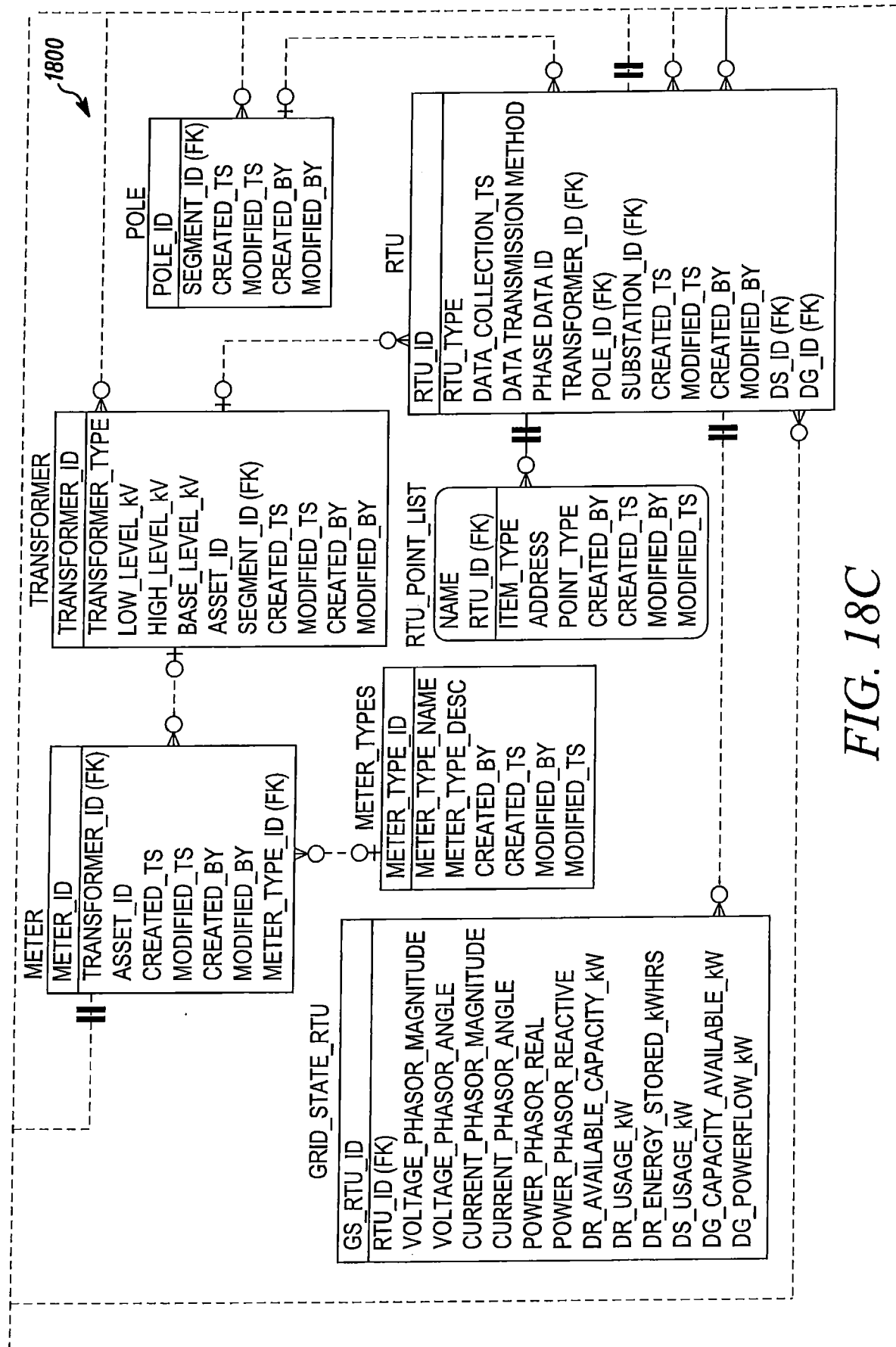


FIG. 17







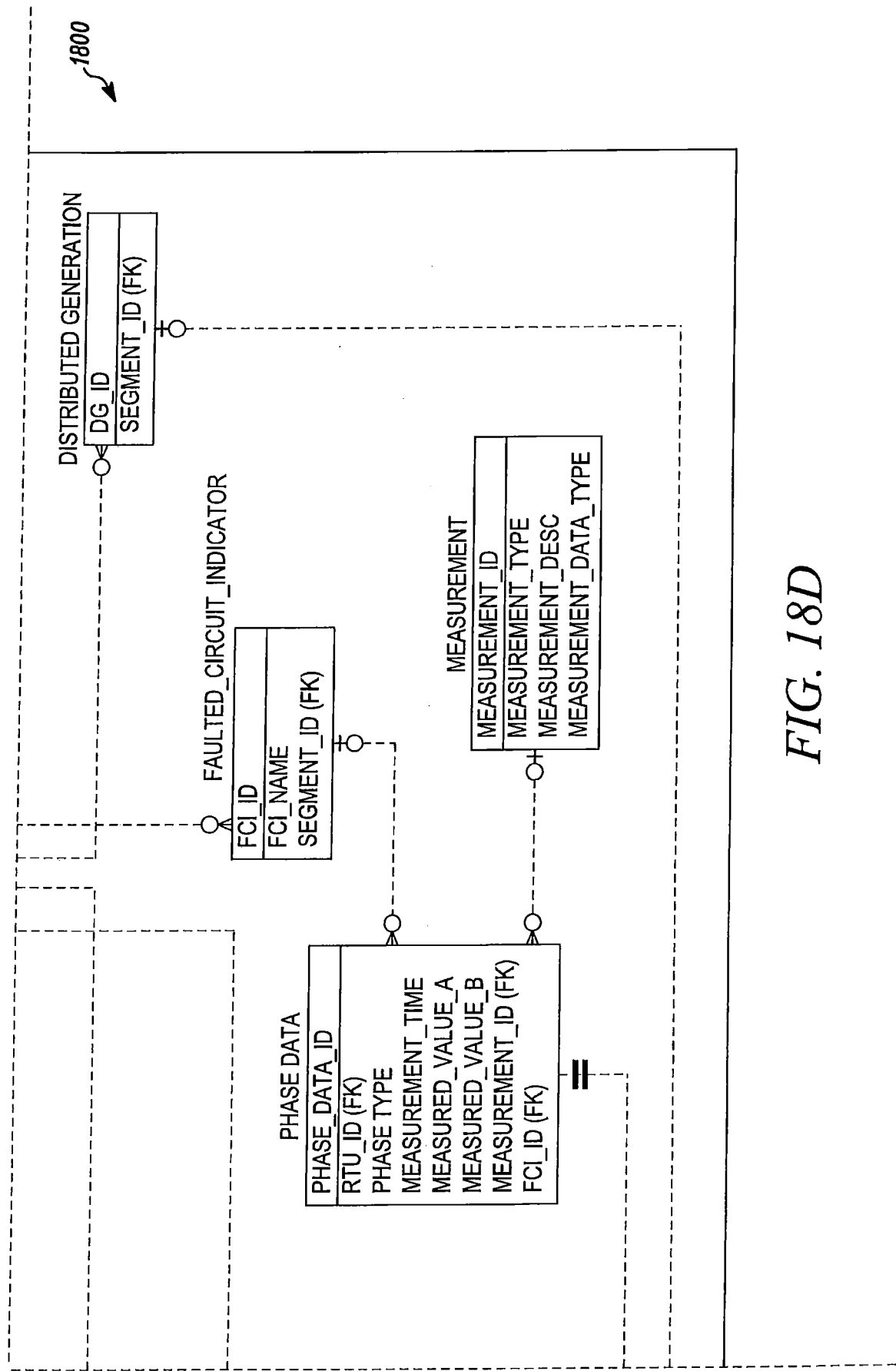
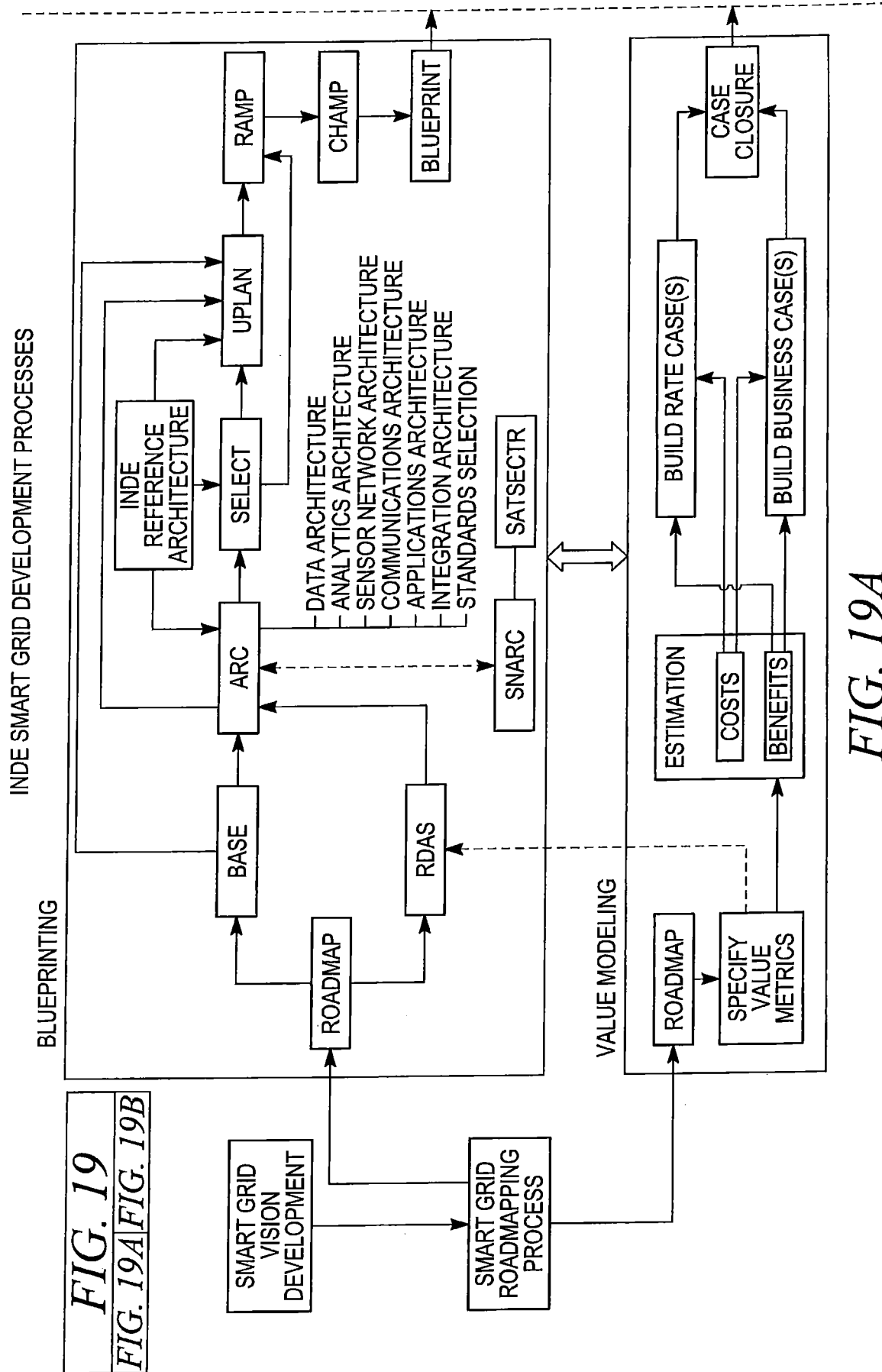


FIG. 18D



BLUEPRINT PROCESS LIST

BASE	BASELINE AND SYSTEMS EVALUATION
RDAS	REQUIREMENTS DEFINITION AND ANALYTICS SELECTION
ARC	ARCHITECTURE CONFIGURATION
SELECT	SOLUTION ELEMENT EVALUATION AND COMPONENTS TEMPLATE
UPLAN	UPGRADE PLANNING FOR APPLICATIONS AND NETWORKS
RAMP	RISK ASSESSMENT AND MANAGEMENT PLANNING
CHAMP	CHANGE ANALYSIS AND MANAGEMENT PLANNING
SNARC	SENSOR NETWORK ARCHITECTURE CONFIGURATION
SATSECTR	SENSOR ALLOCATION VIA T-SECTION RECURSION

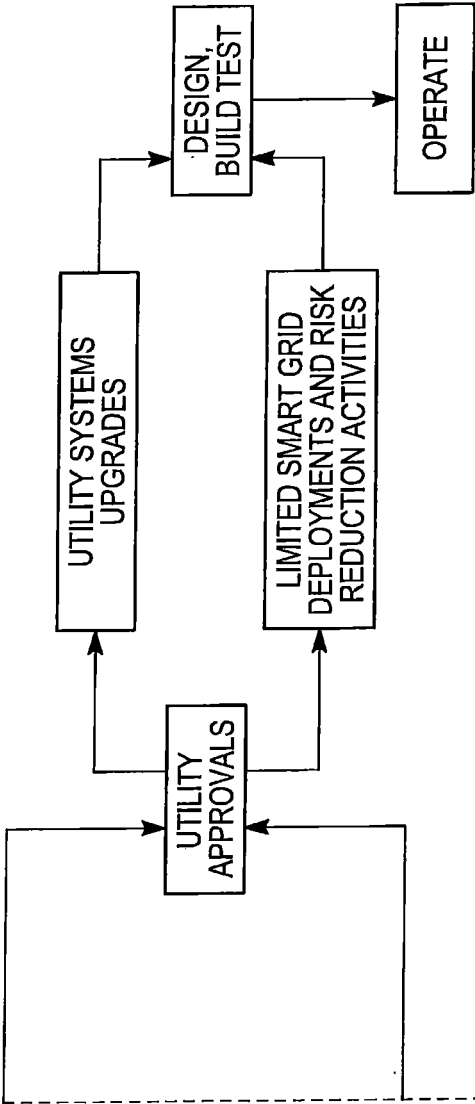


FIG. 19B

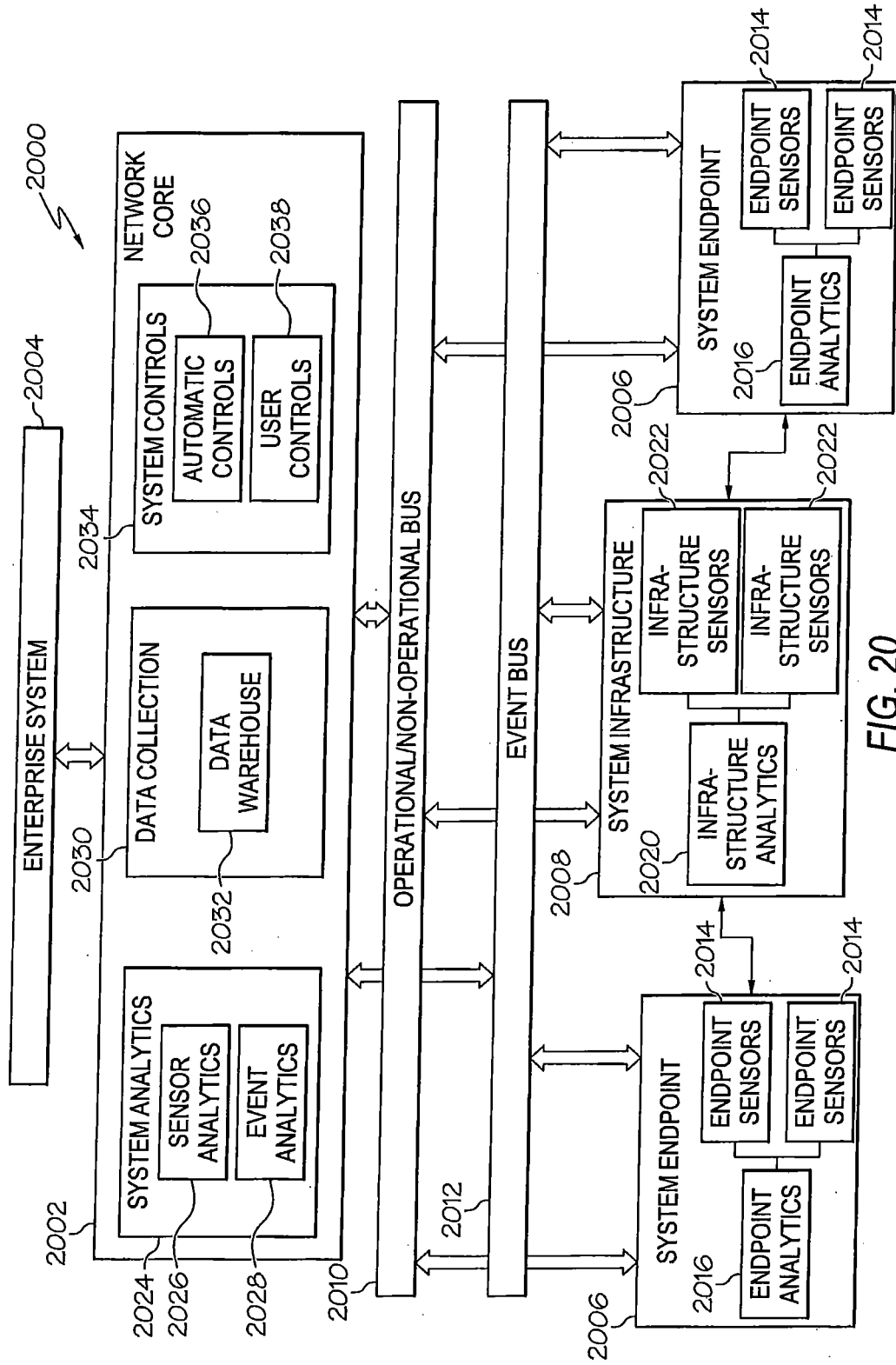
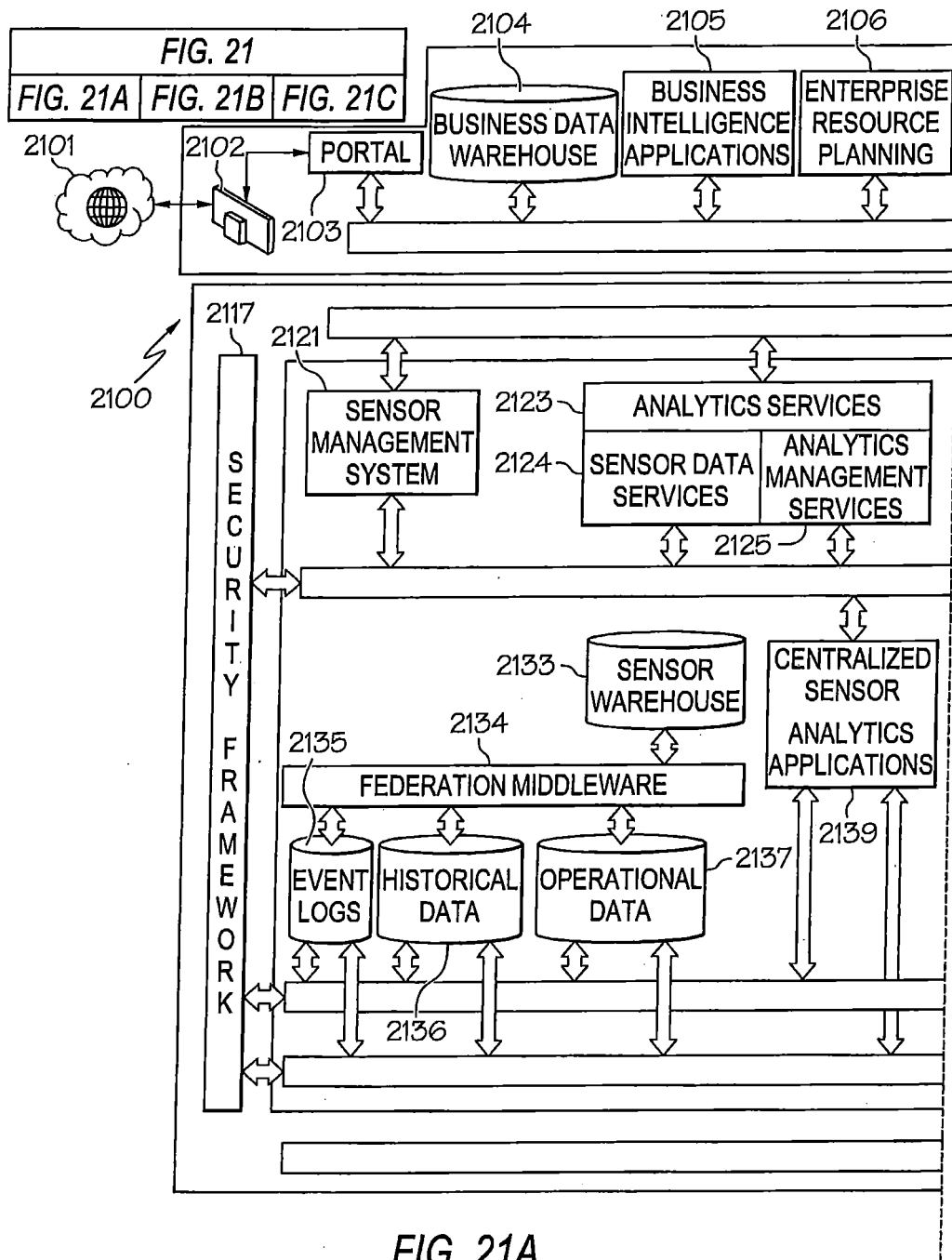


FIG. 20



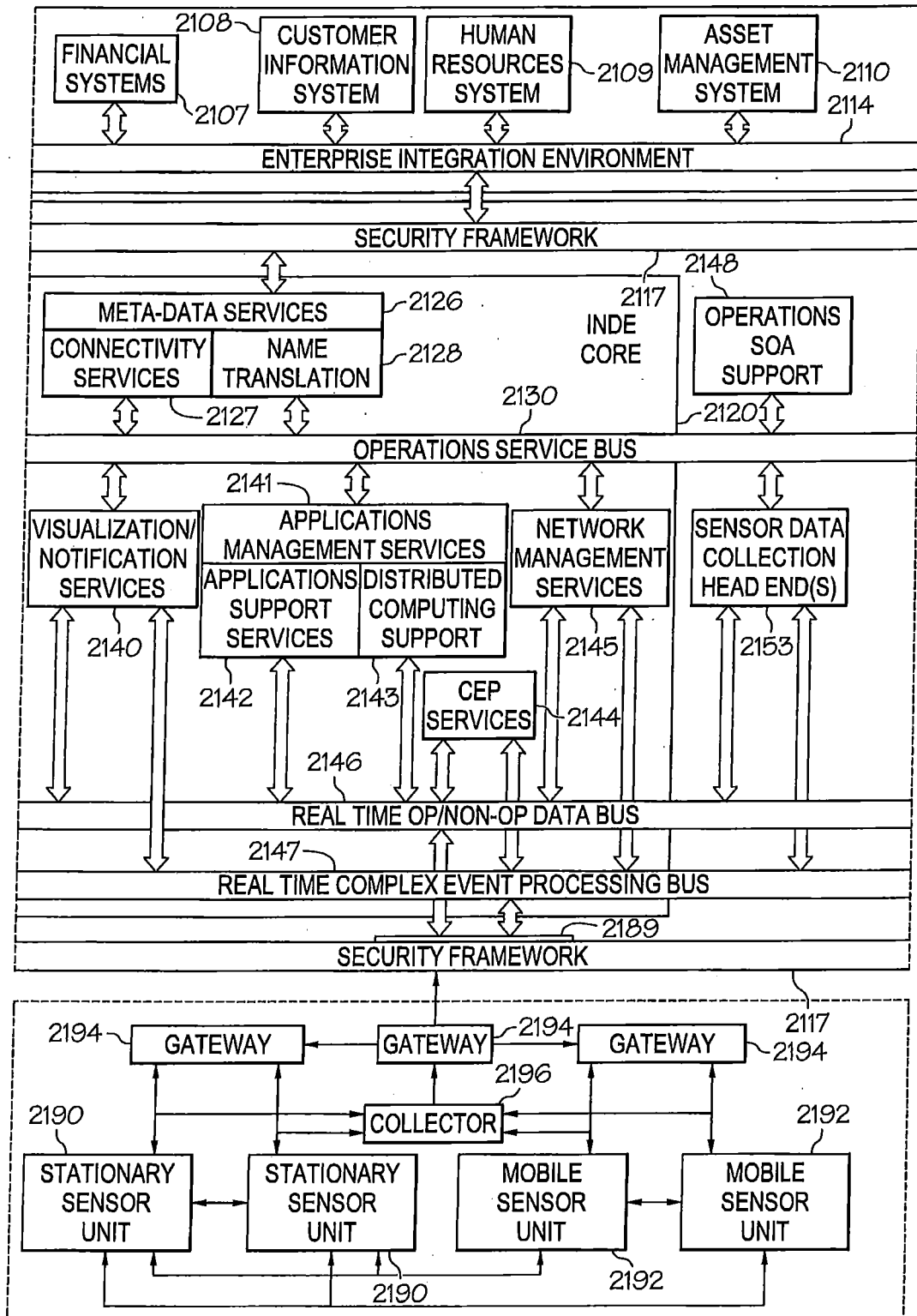


FIG. 21B

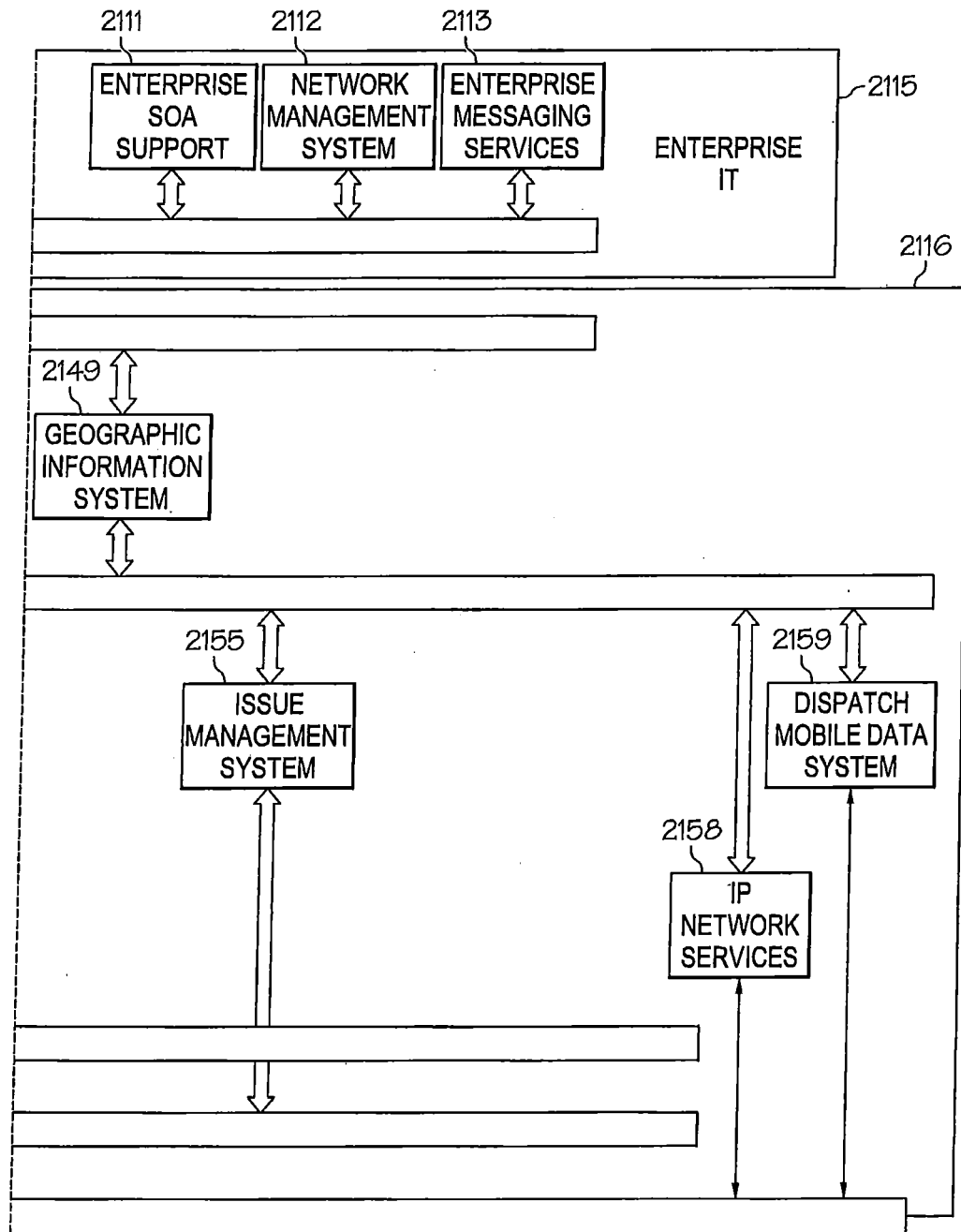
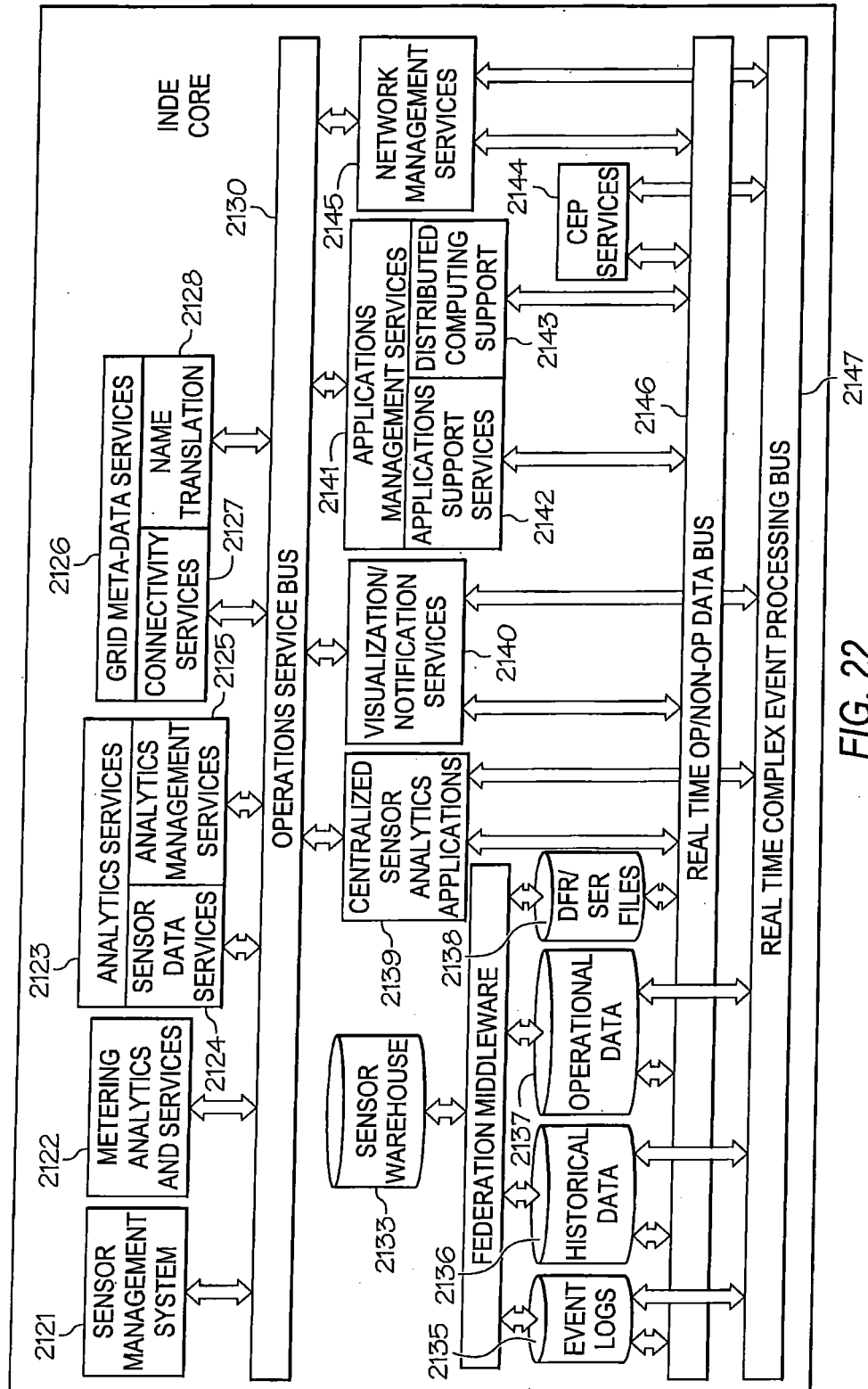


FIG. 21C



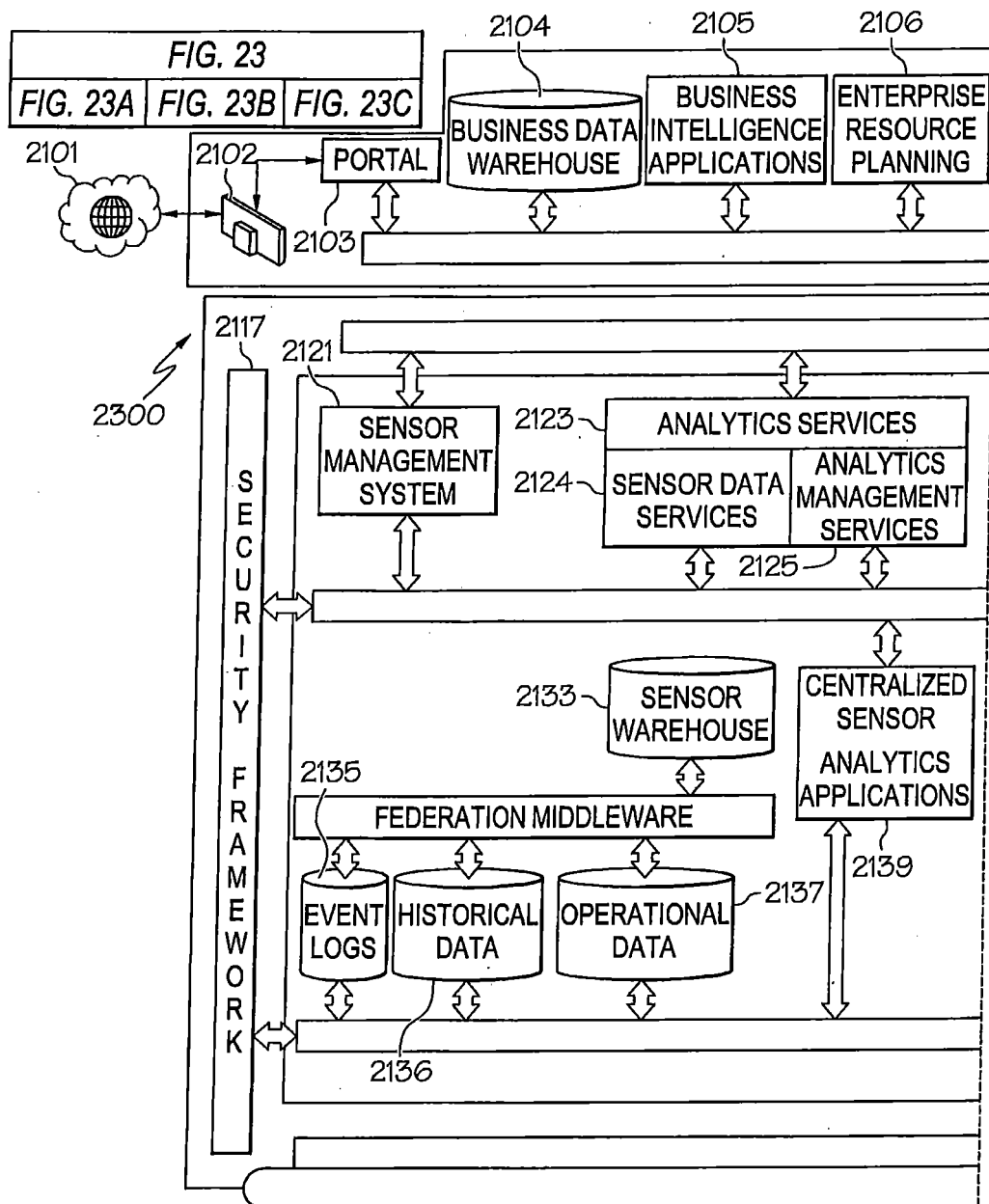


FIG. 23A

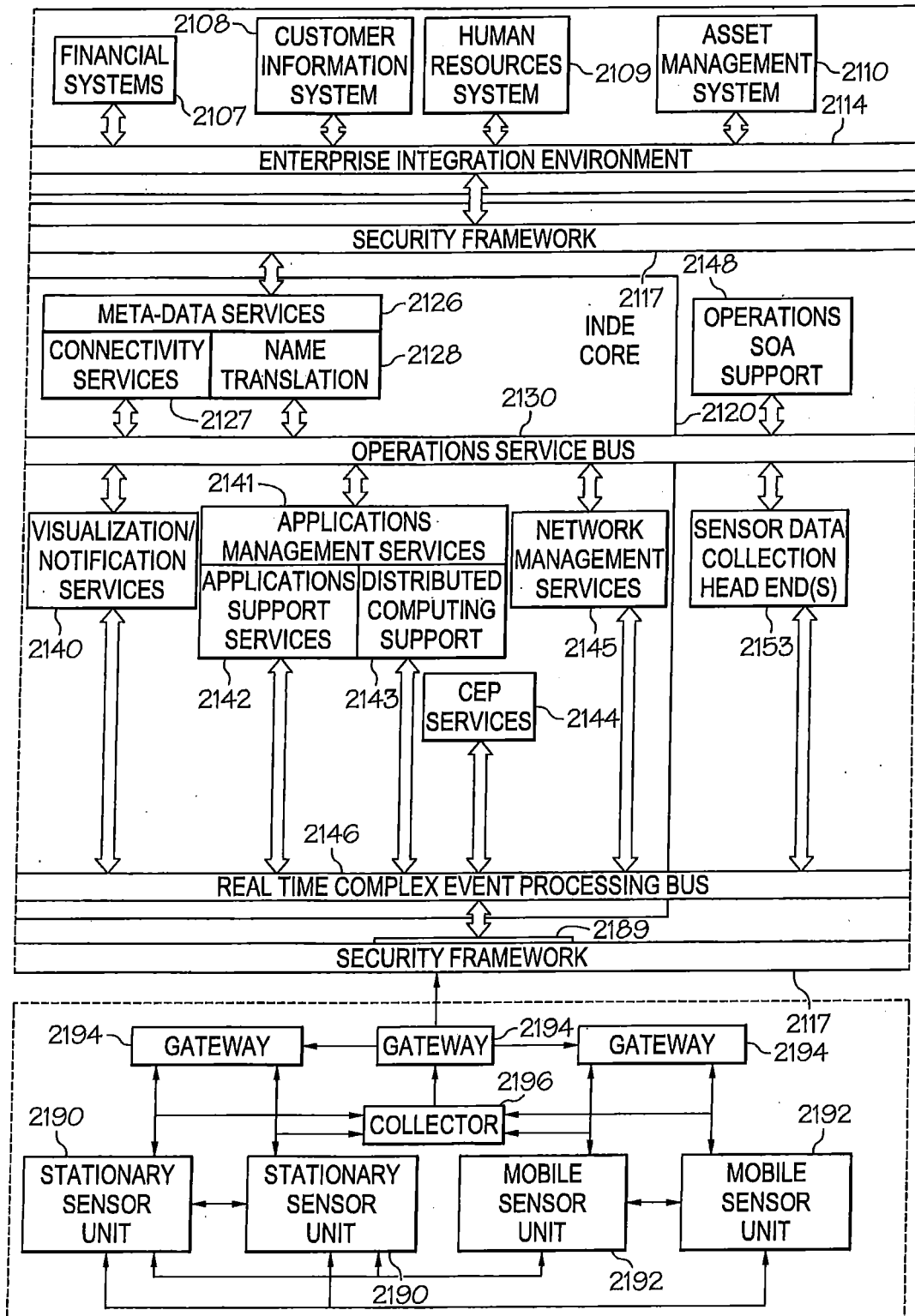


FIG. 23B

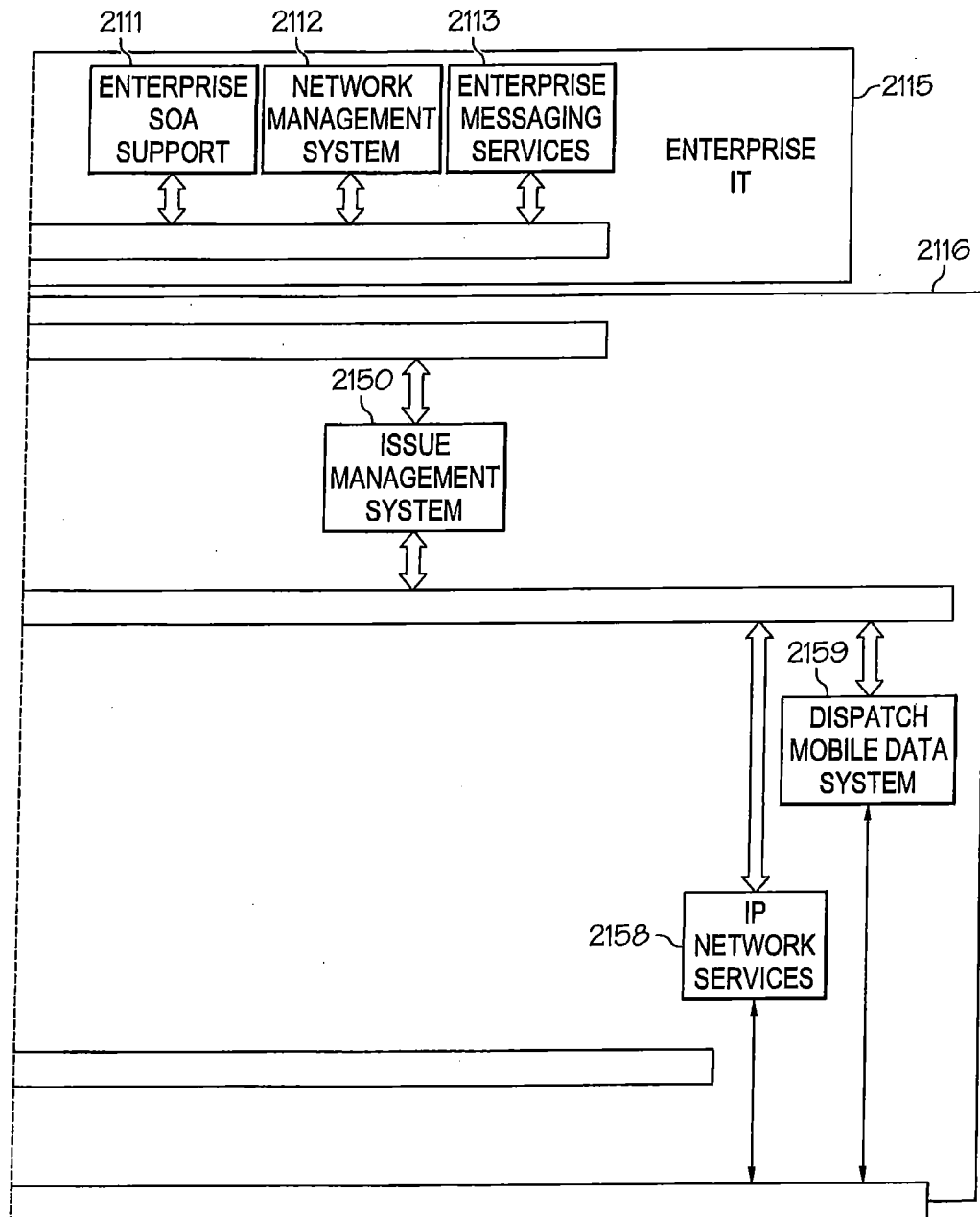


FIG. 23C

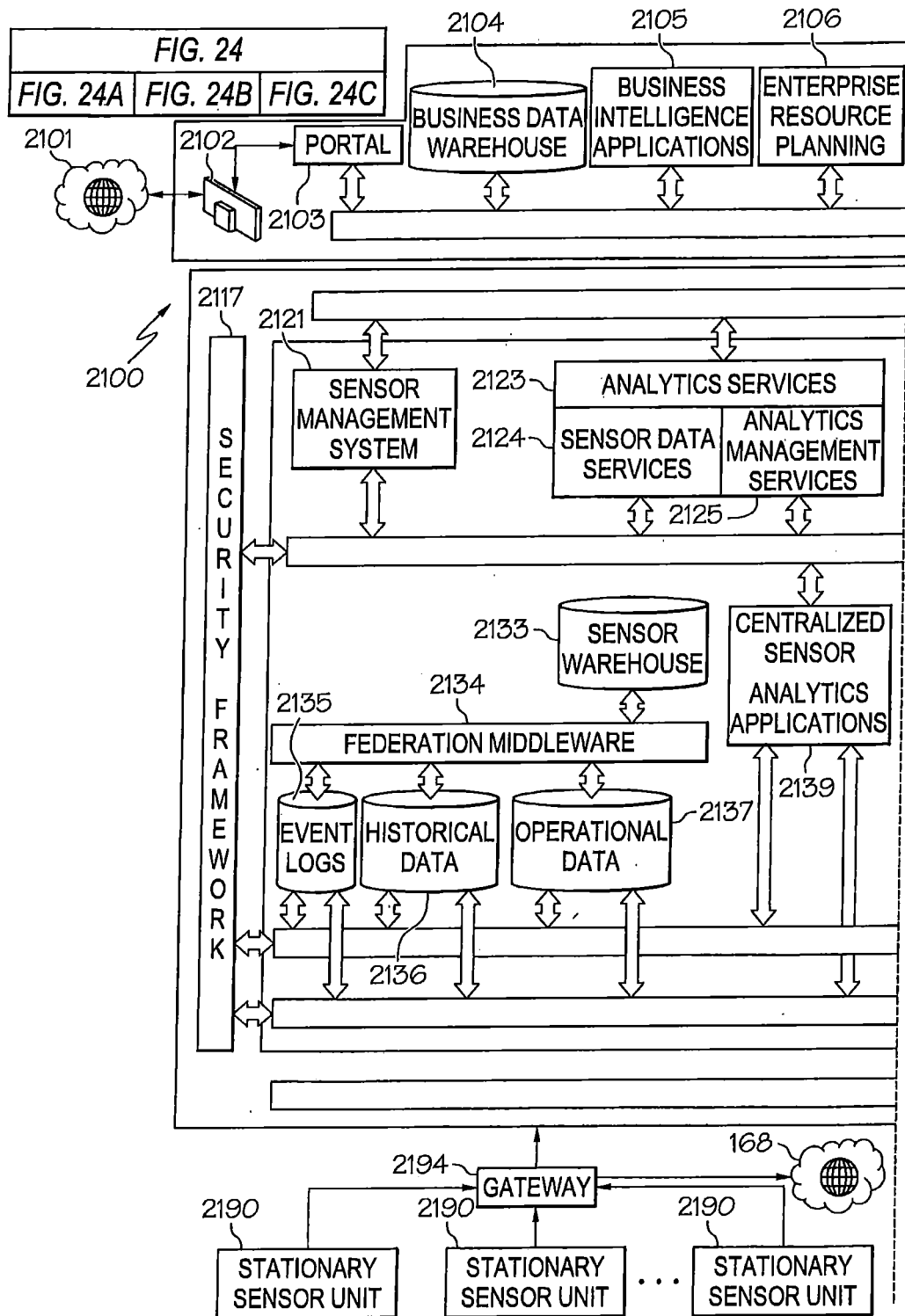


FIG. 24A

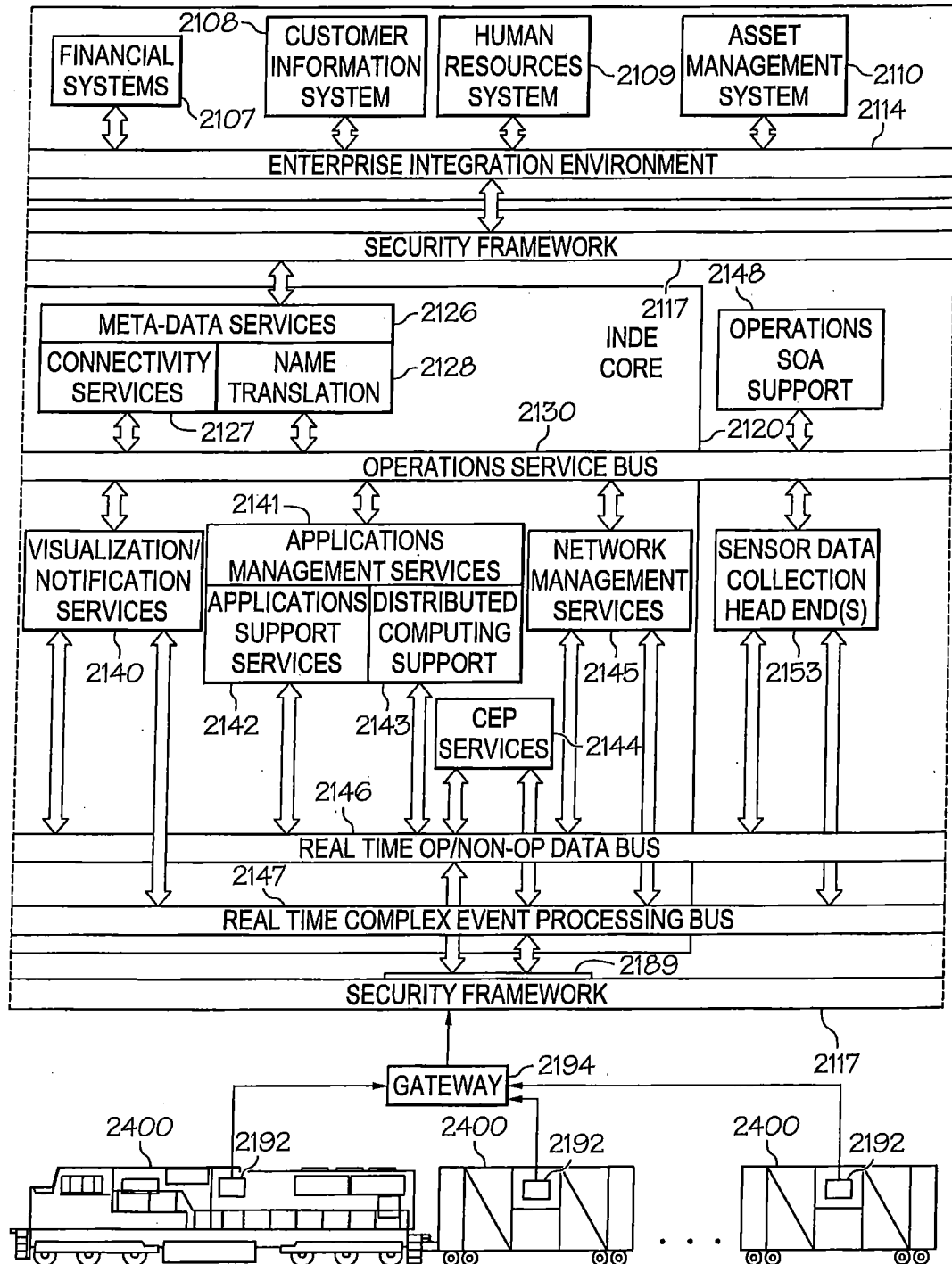


FIG. 24B

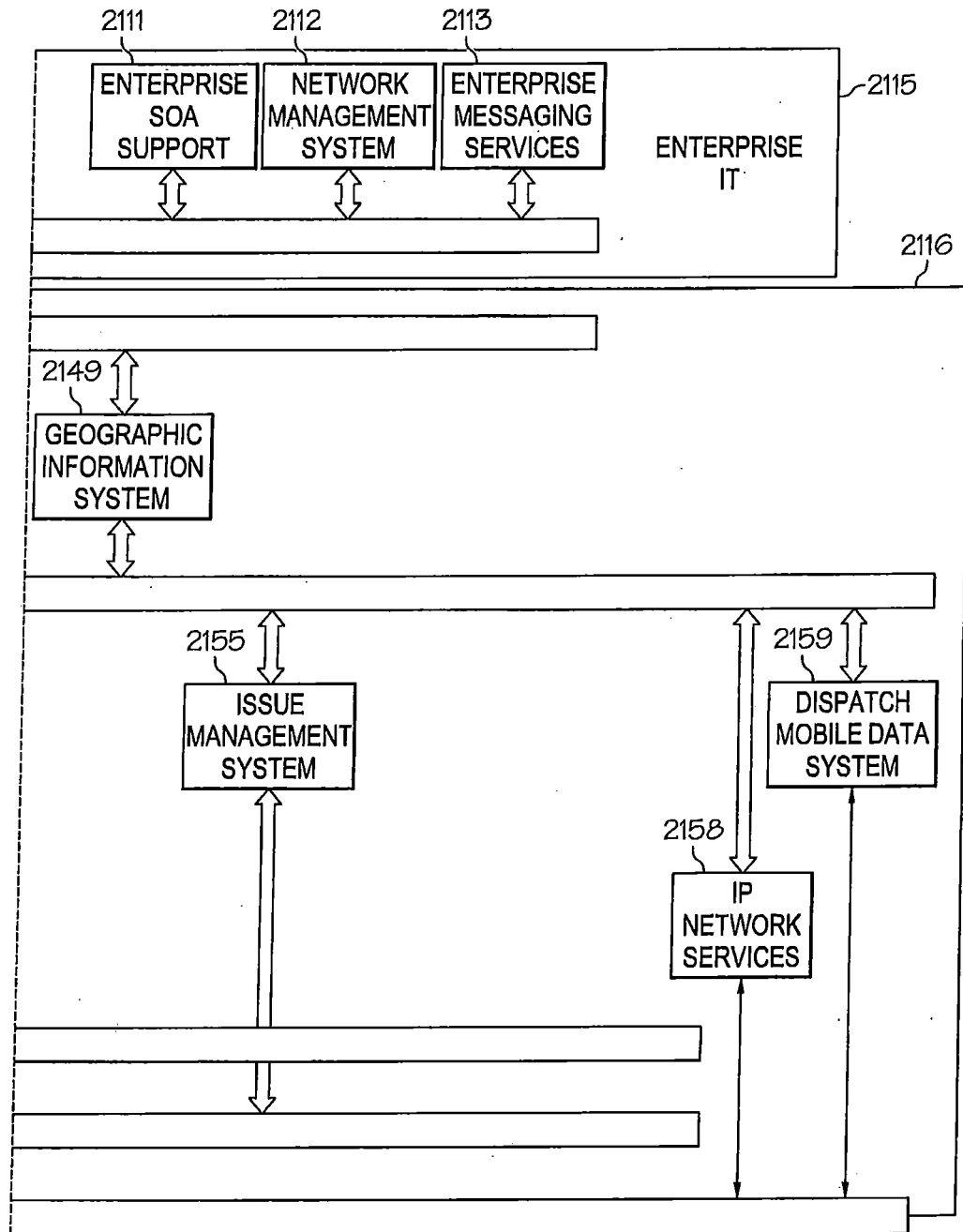


FIG. 24C

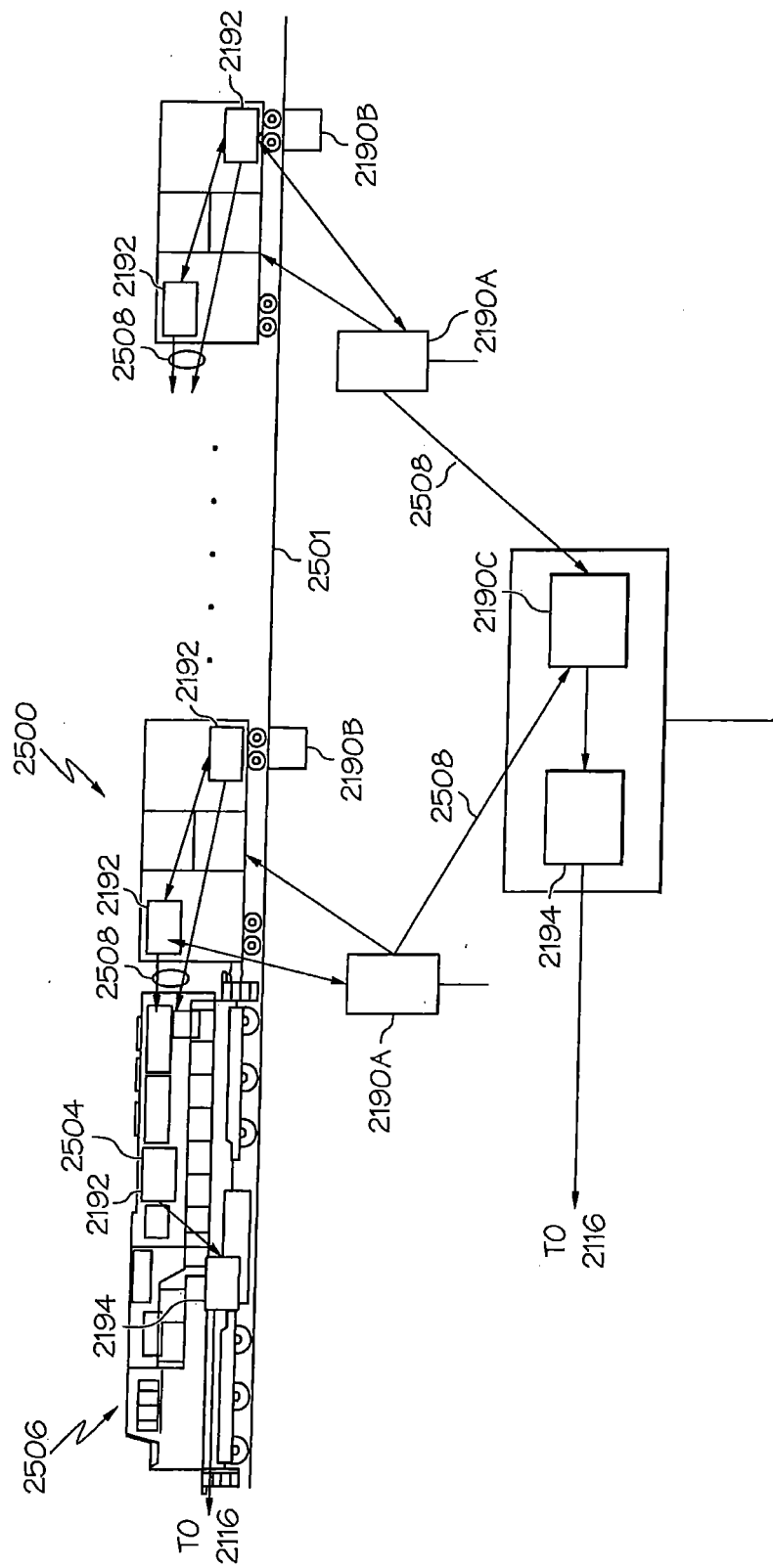
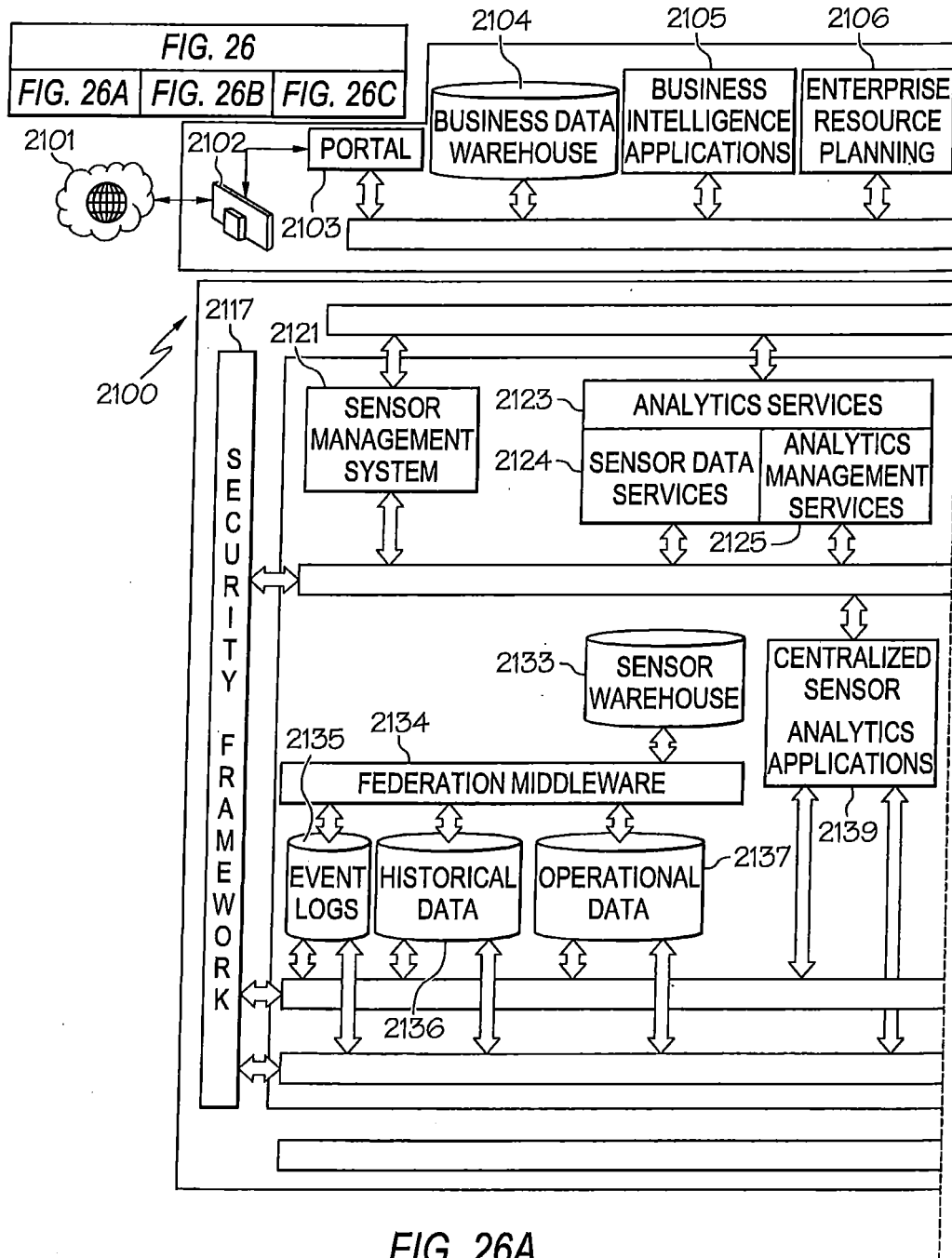


FIG. 25



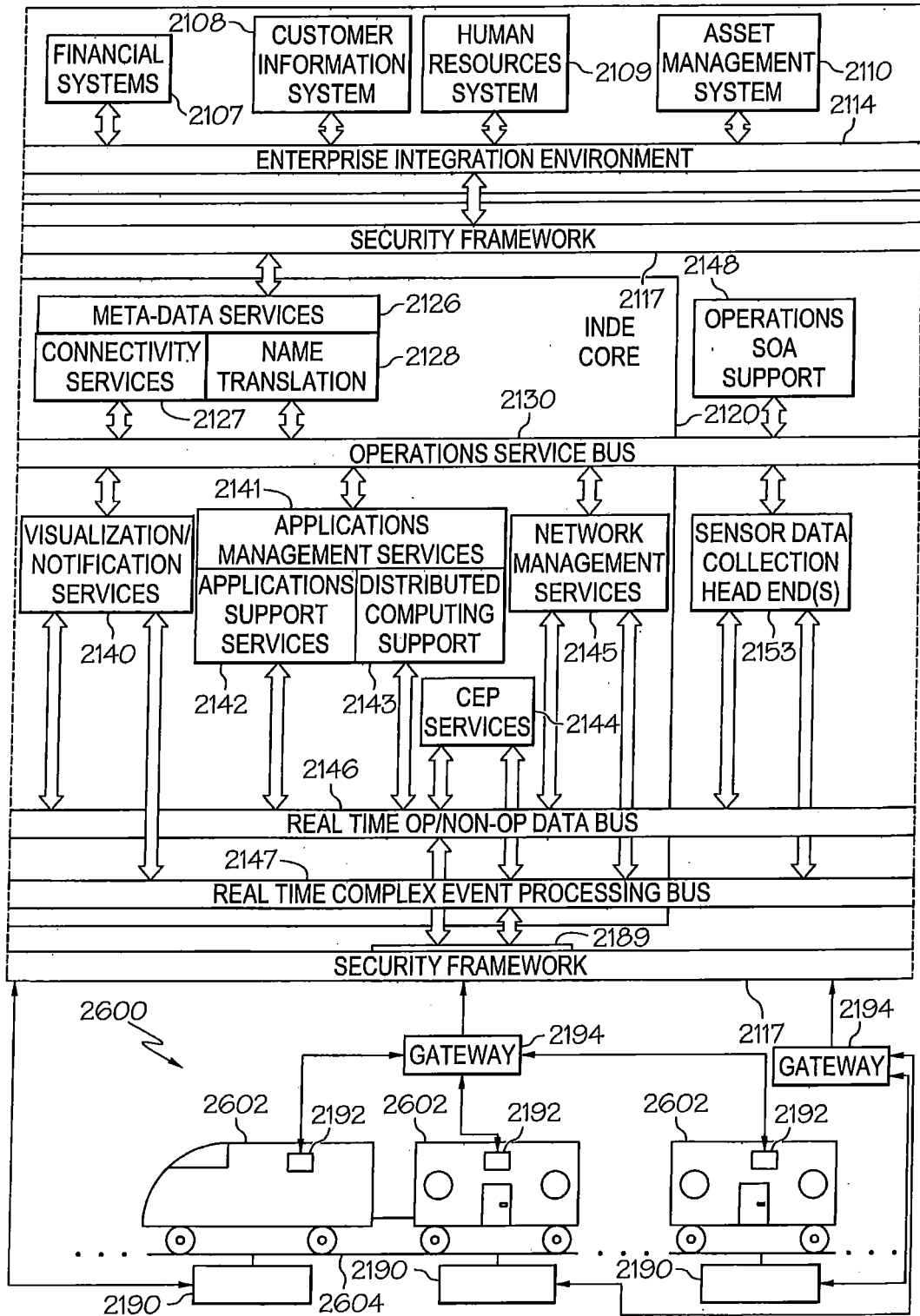


FIG. 26B

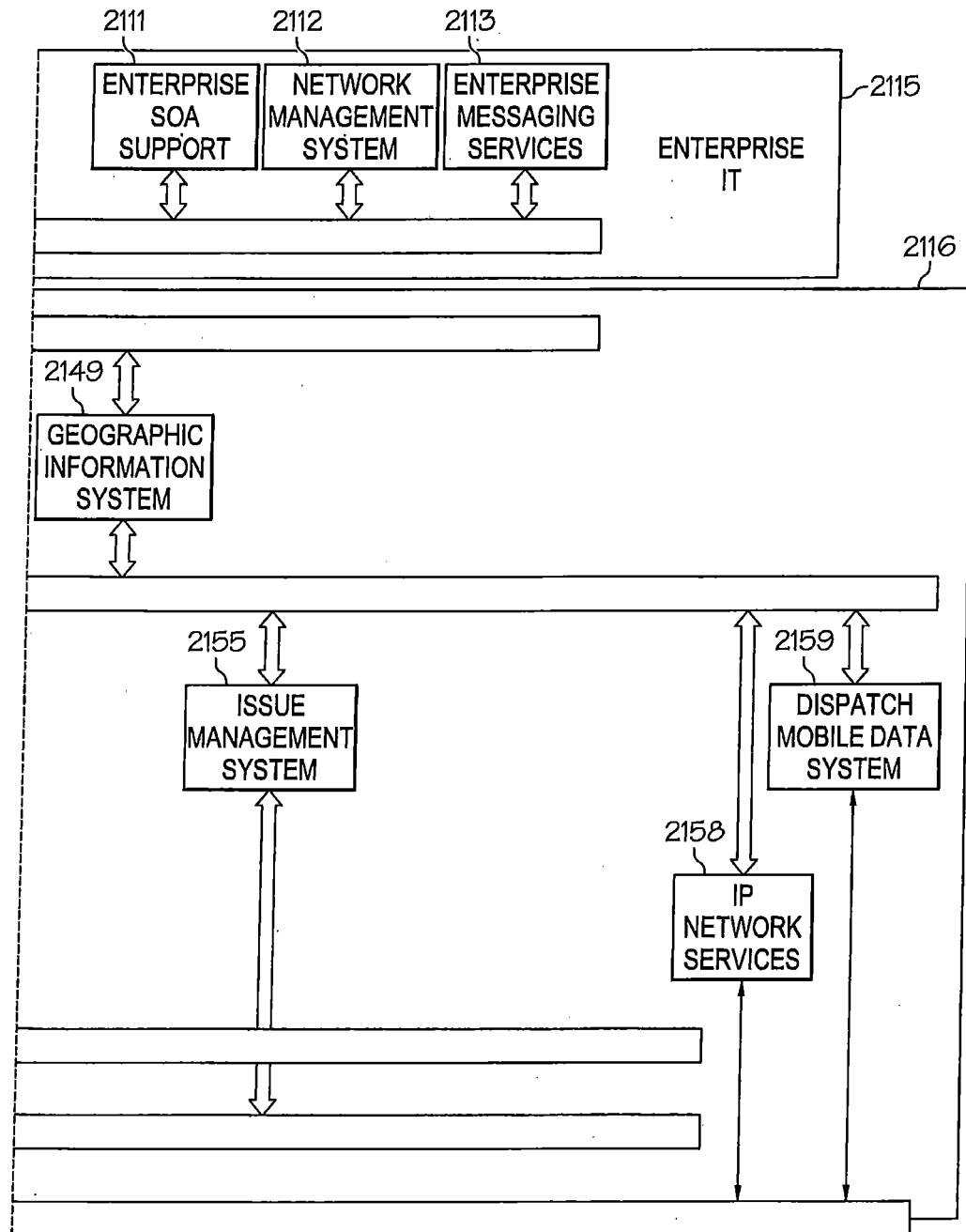
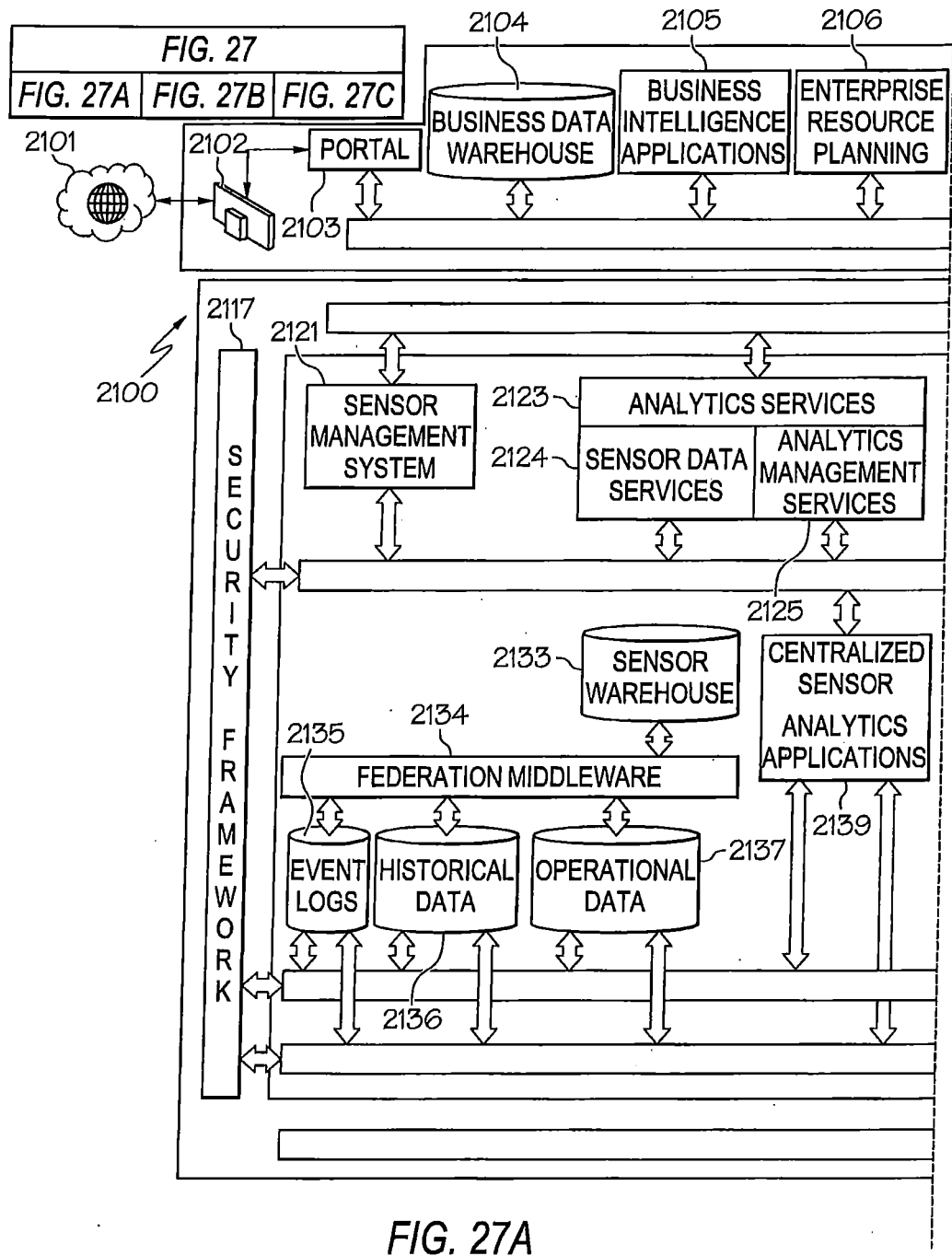


FIG. 26C



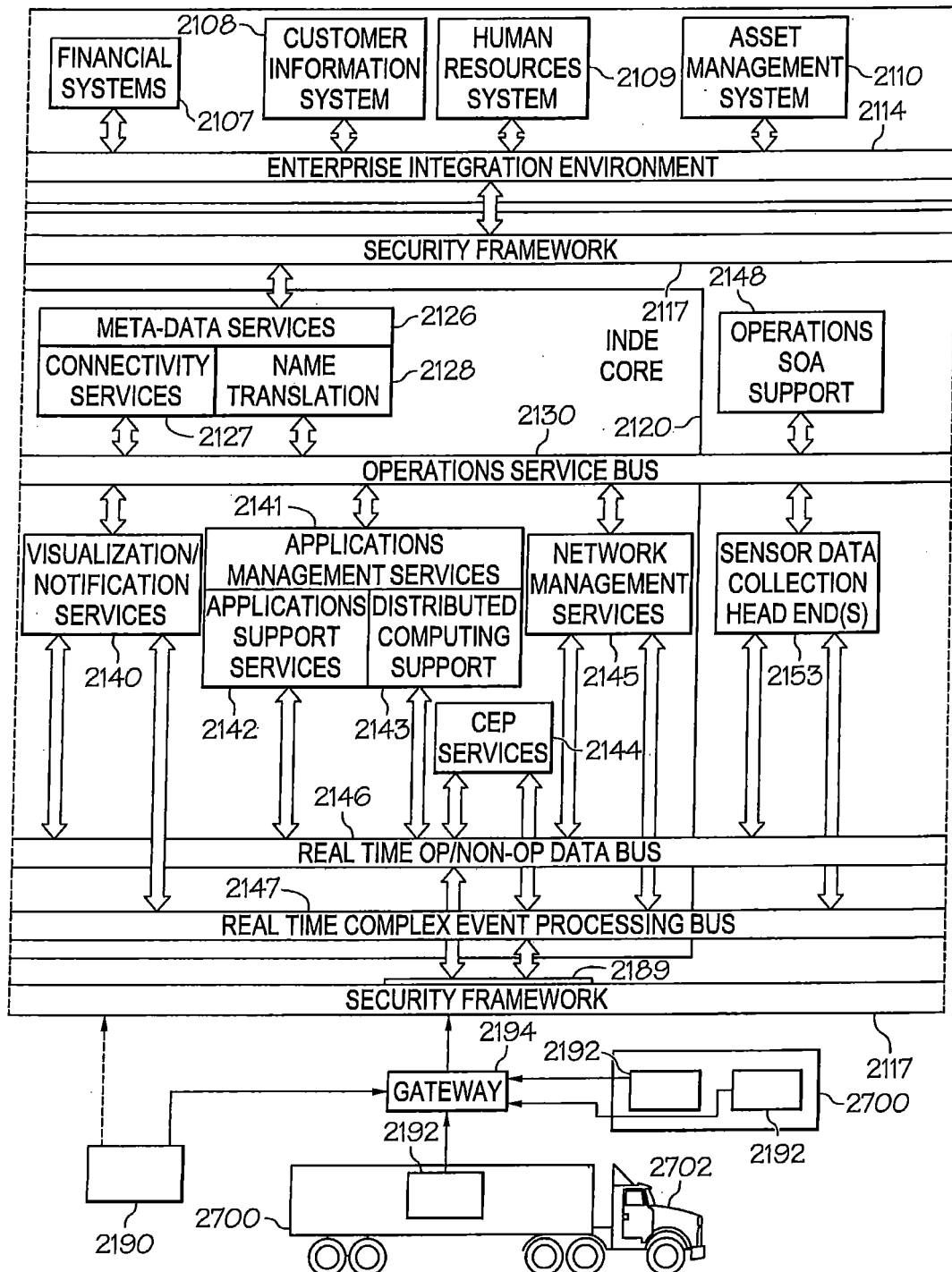


FIG. 27B

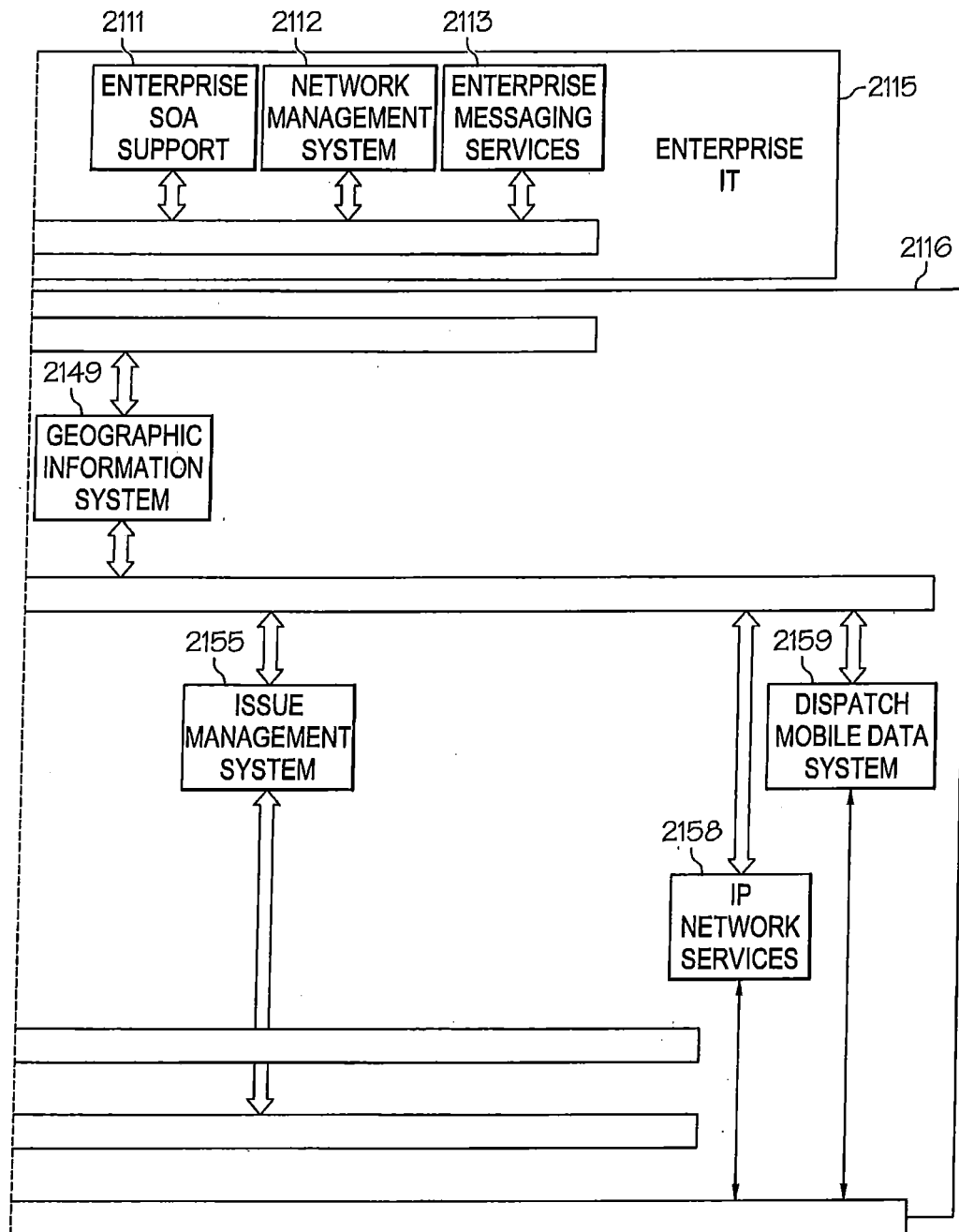
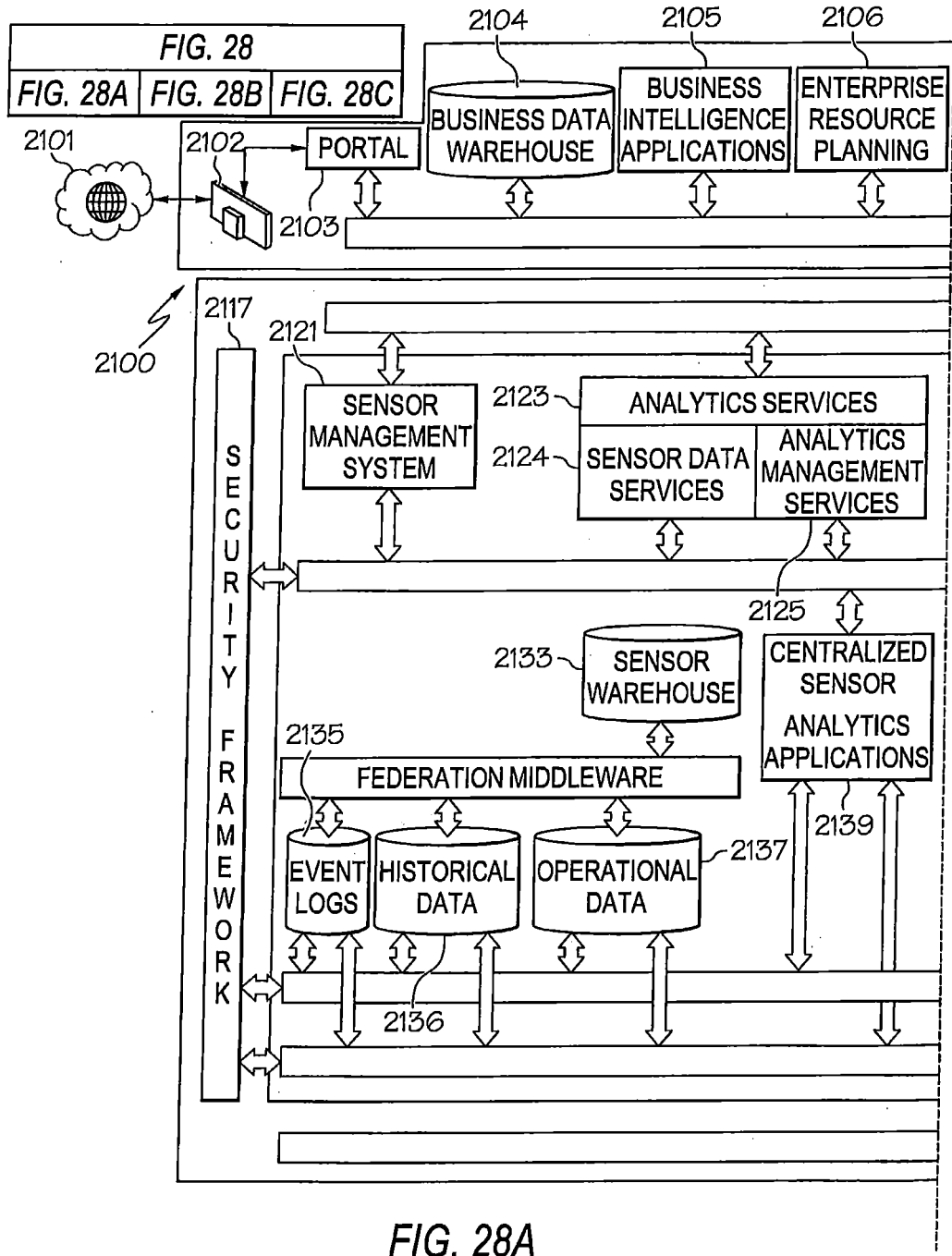


FIG. 27C



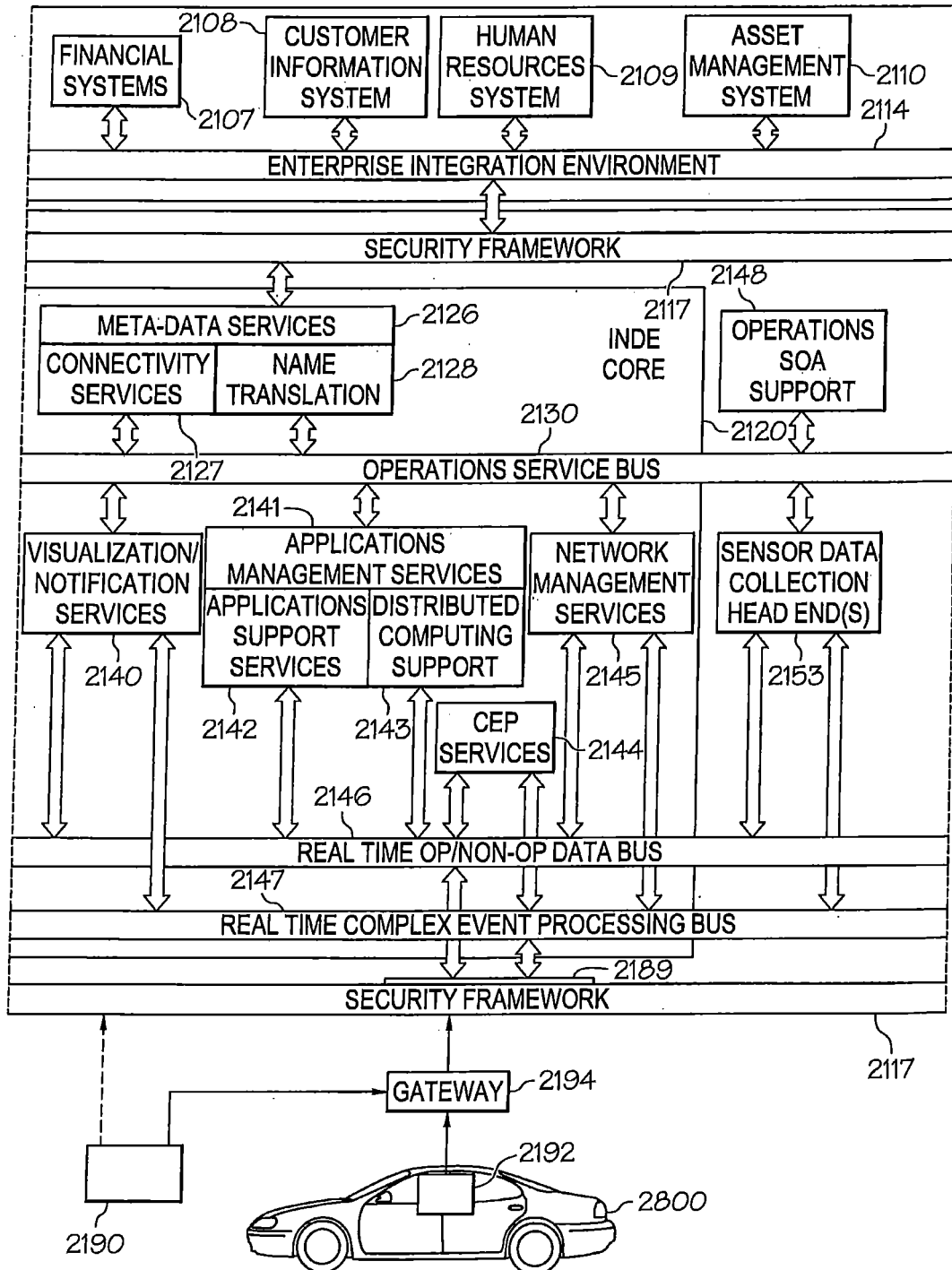


FIG. 28B

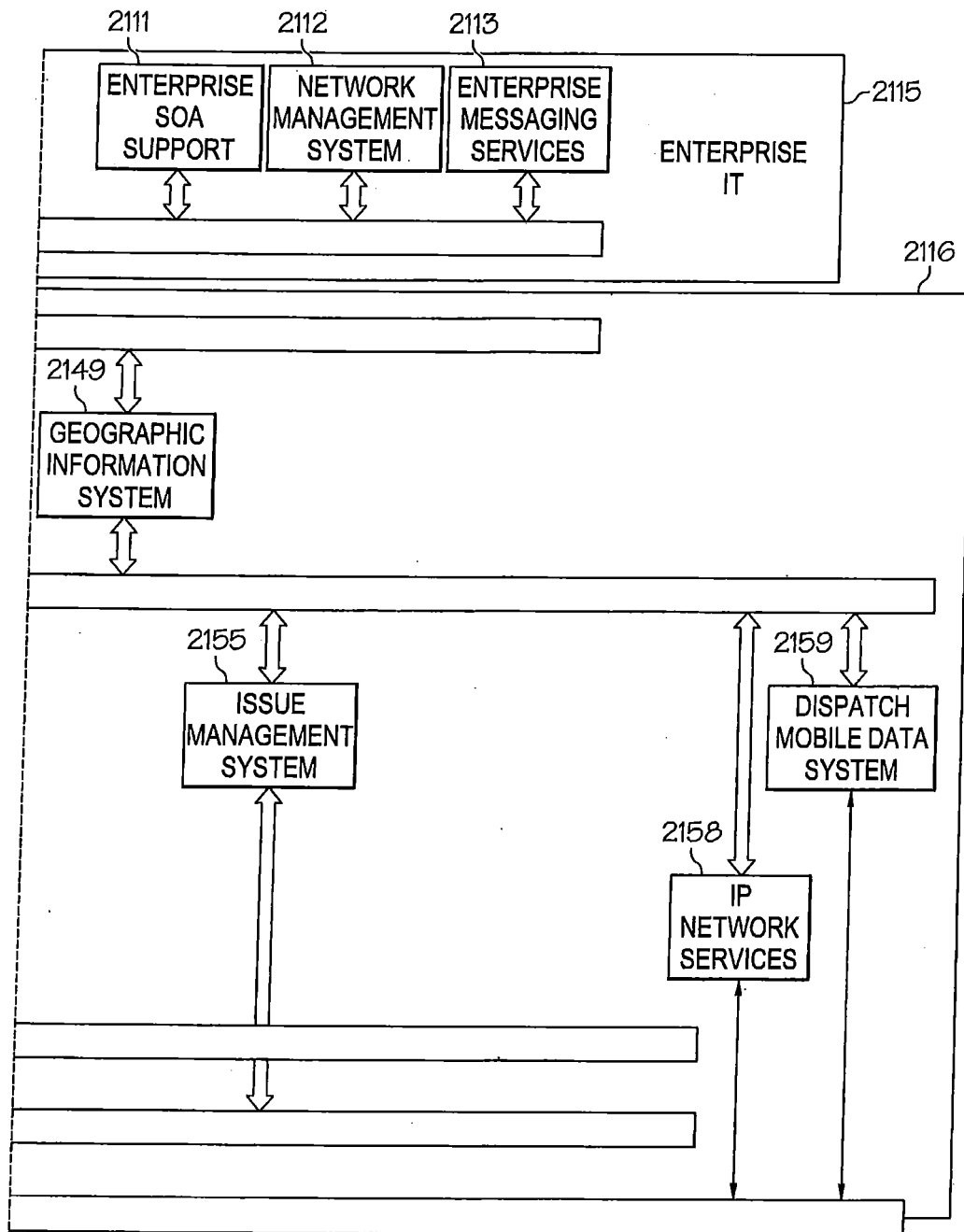


FIG. 28C

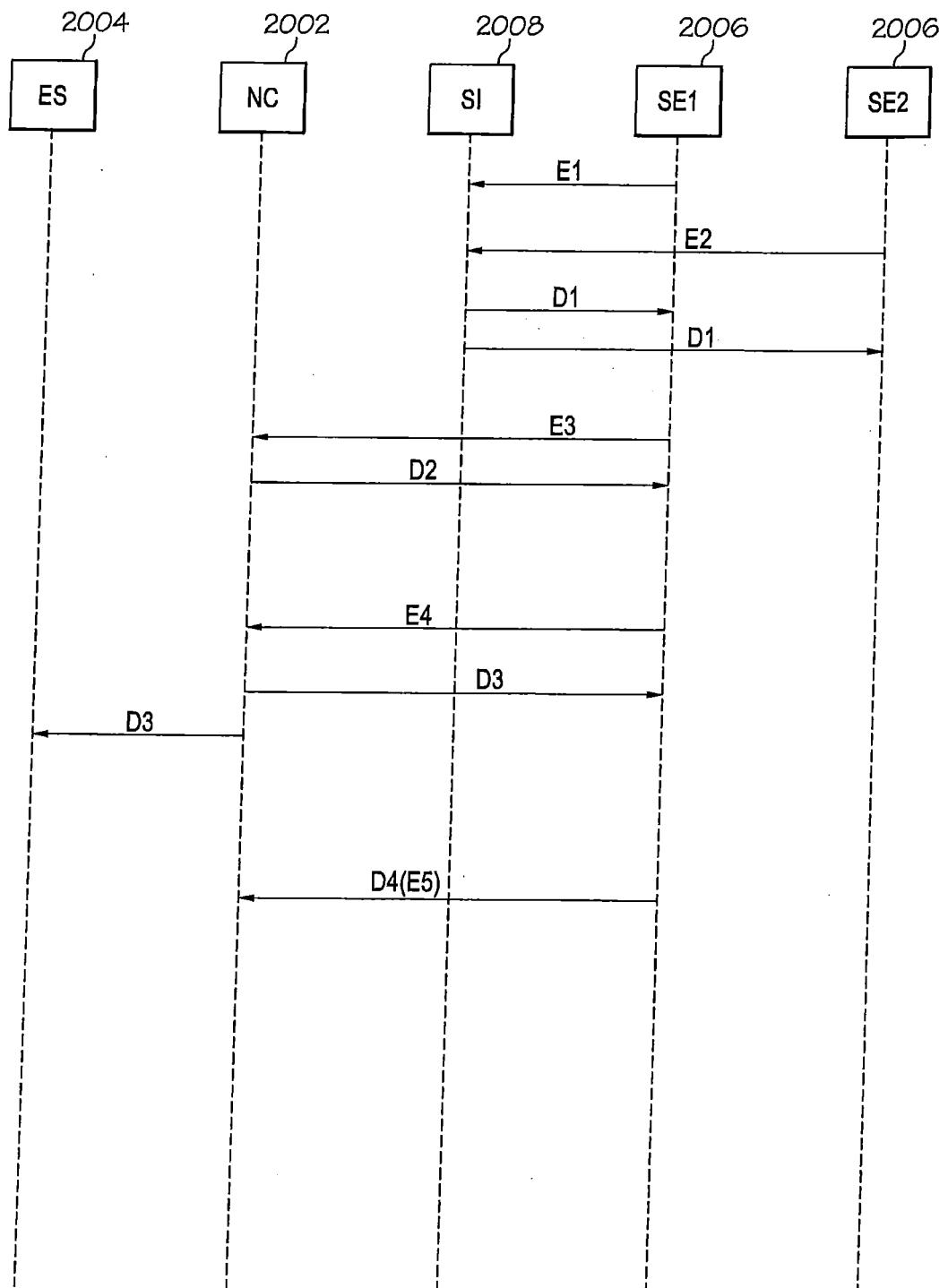


FIG. 29

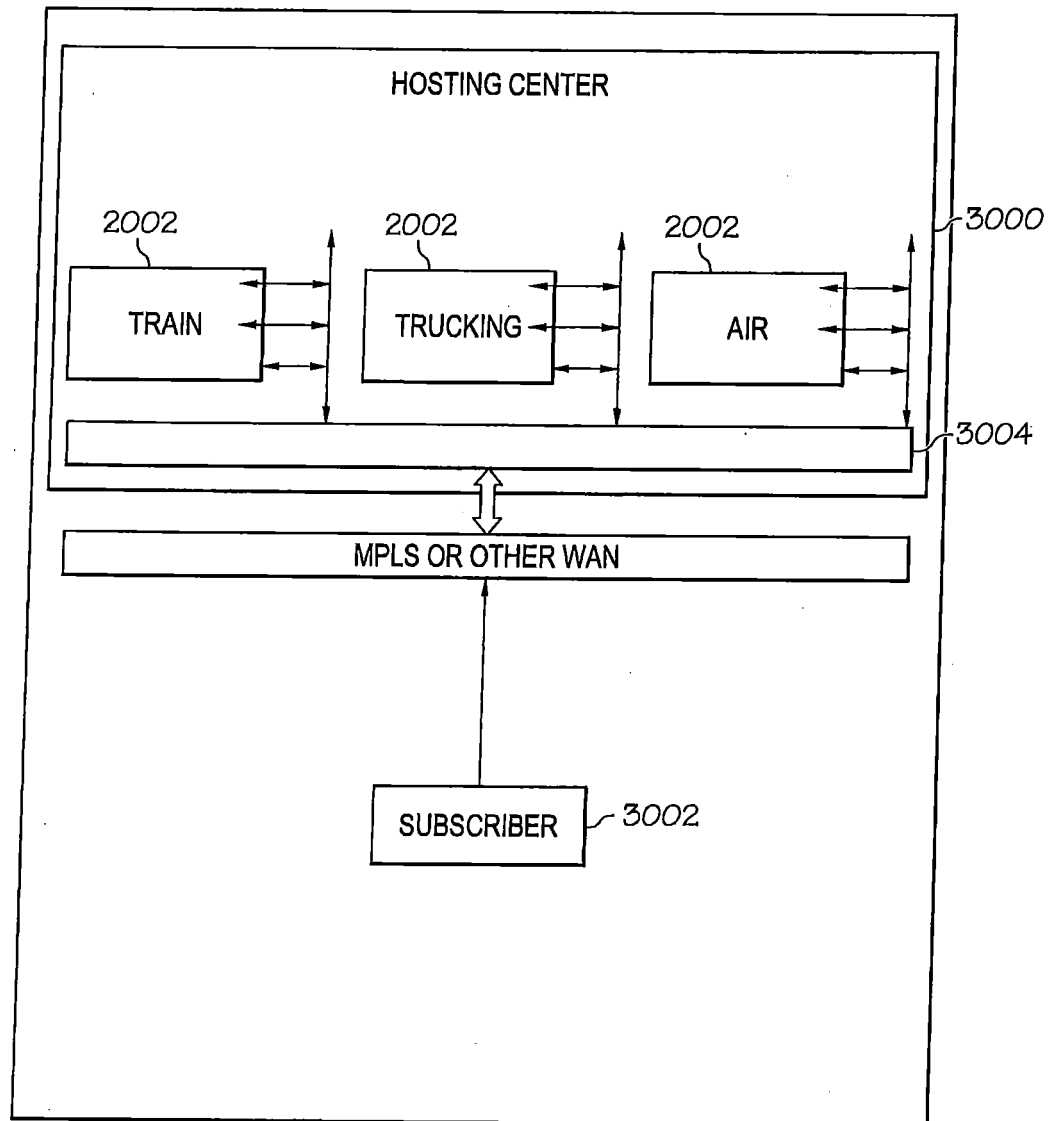


FIG. 30