

ABSTRACT

INTEGRATED CIRCUIT SYSTEM EMPLOYING RESISTANCE ALTERING TECHNIQUES

An integrated circuit system that includes: providing a substrate including a first
5 region and a second region; forming a first device over the first region and a resistance device
over the second region; forming a first dielectric layer and a second dielectric layer over the
substrate; removing a portion of the second dielectric layer; and annealing the integrated
circuit system to remove dopant from the resistance device.

10 **FIG. 1**