

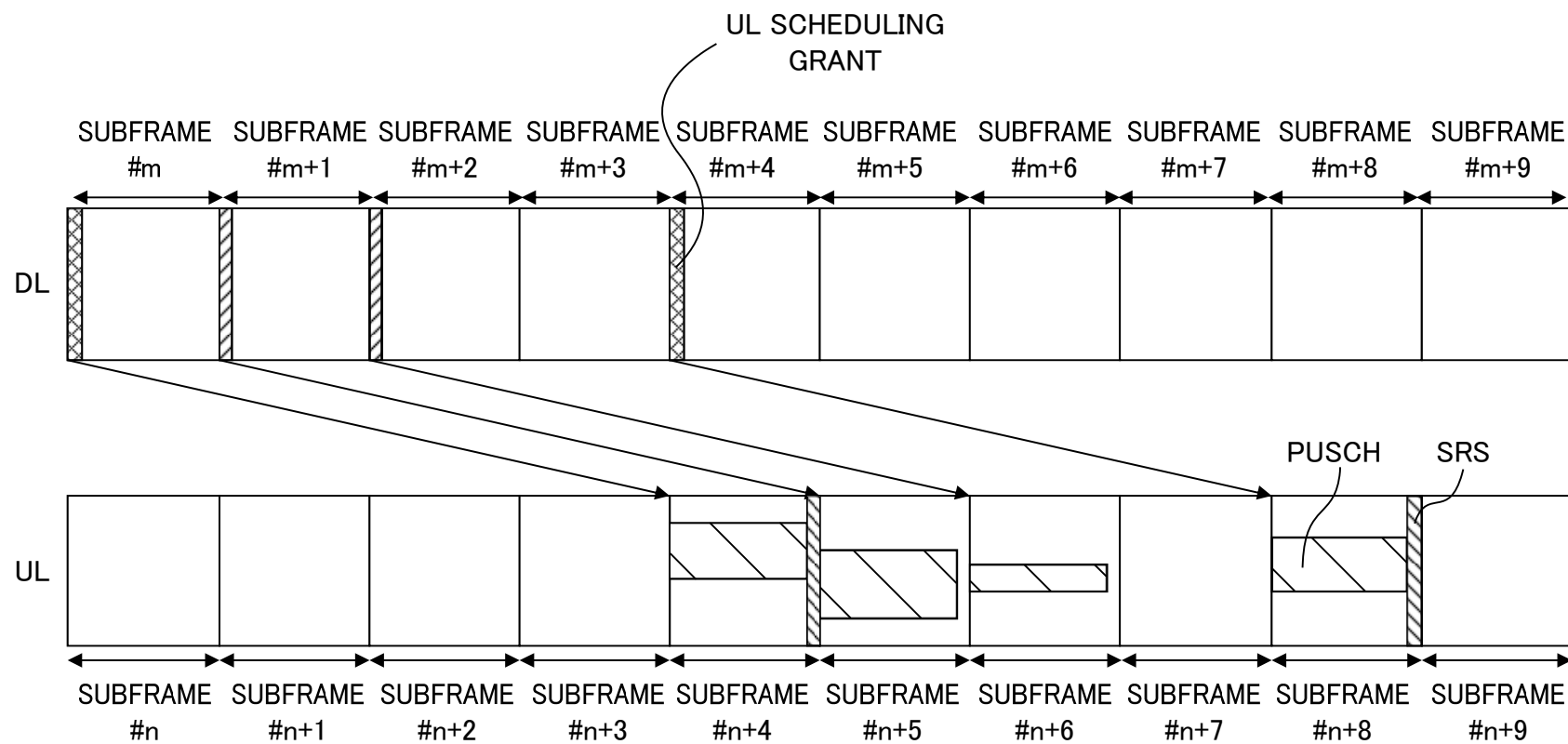
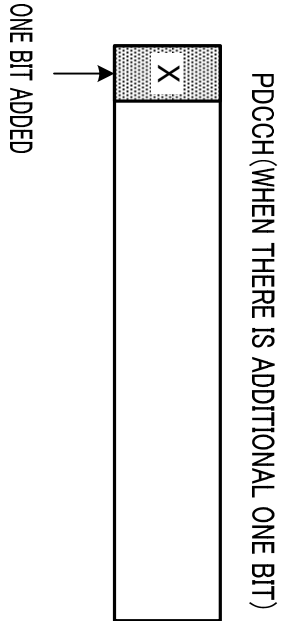


FIG.1



BIT	SRS TRANSMISSION CONTENT
0	A-SRS NOT TRANSMITTED
1	A-SRS TRANSMITTED

FIG. 2

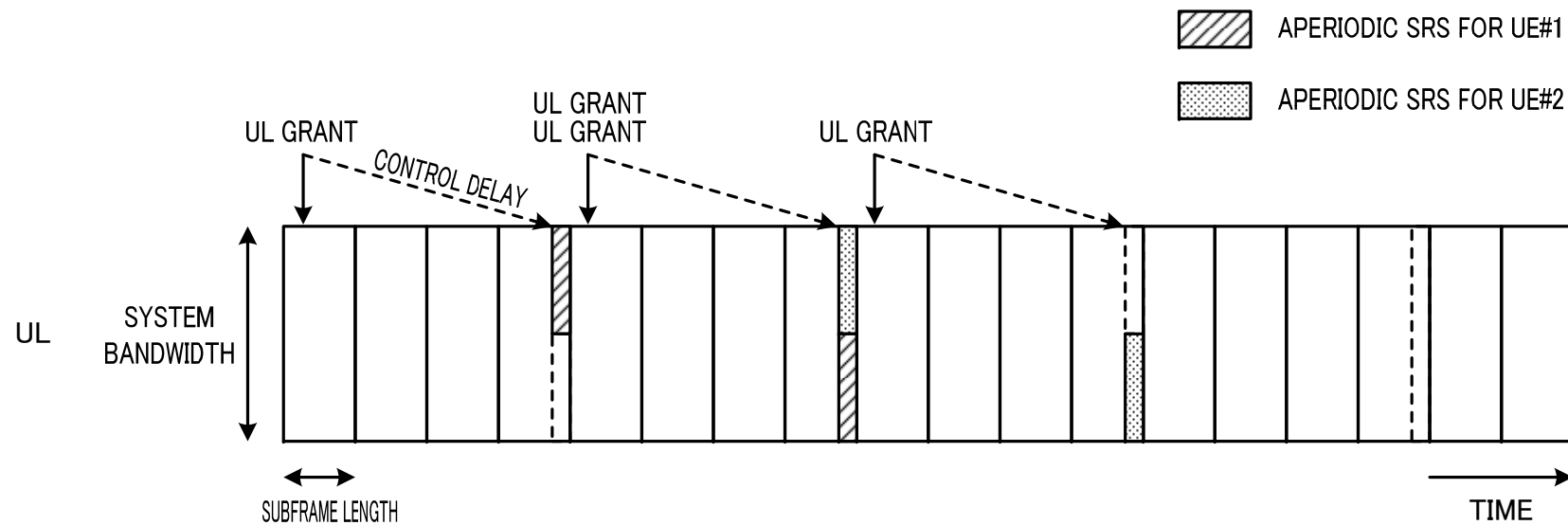


FIG. 3A CASE1 (IN CASE OF NARROWBAND SRS)

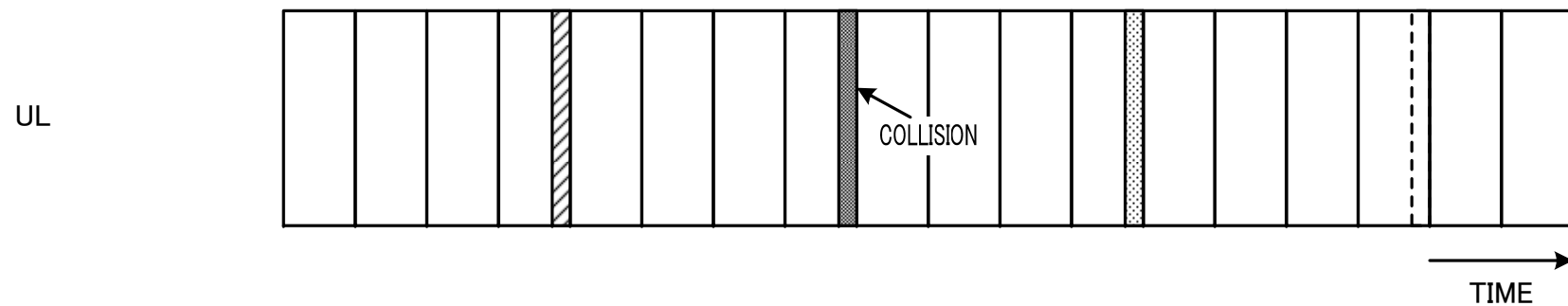
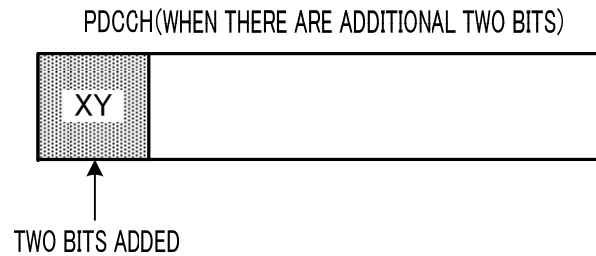


FIG. 3B CASE2 (IN CASE OF WIDEBAND SRS)



BIT	SRS TRANSMISSION CONTENT
00	A-SRS NOT TRANSMITTED
01	TRANSMIT BY COMB 0
10	TRANSMIT BY COMB 1
11	N/A

FIG. 4A

BIT	SRS TRANSMISSION CONTENT
00	A-SRS NOT TRANSMITTED
01	TRANSMIT IN FREQUENCY POSITION 0
10	TRANSMIT IN FREQUENCY POSITION 1
11	TRANSMIT IN FREQUENCY POSITION 2

FIG. 4B

BIT	SRS TRANSMISSION CONTENT
00	A-SRS NOT TRANSMITTED
01	TRANSMIT BY CS 0
10	TRANSMIT BY CS 1
11	TRANSMIT BY CS 2

FIG. 4C

BIT	SRS TRANSMISSION CONTENT
00	A-SRS NOT TRANSMITTED
01	TRANSMIT BY DEFAULT
10	TRANSMIT BY SHIFTING CS FROM DEFAULT BY VALUE OF X
11	TRANSMIT BY SHIFTING CS FROM DEFAULT BY VALUE OF Y

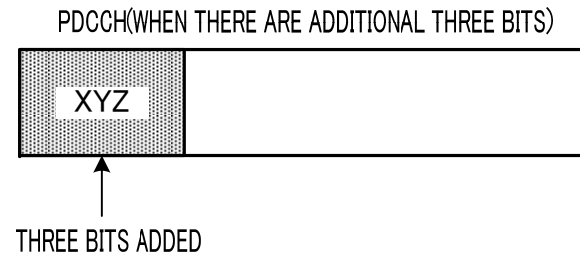
FIG. 4D

BIT	SRS TRANSMISSION CONTENT
00	A-SRS NOT TRANSMITTED
01	TRANSMIT BY DEFAULT
10	TRANSMIT BY DIFFERENT COMB FROM DEFAULT
11	TRANSMIT BY SHIFTING CS FROM DEFAULT BY VALUE OF X

FIG. 4E

BIT	SRS TRANSMISSION CONTENT
00	A-SRS NOT TRANSMITTED
01	TRANSMIT BY DEFAULT <i>a</i>
10	TRANSMIT BY DEFAULT <i>b</i>
11	TRANSMIT BY DEFAULT <i>c</i>

FIG. 4F



BIT	SRS TRANSMISSION CONTENT
000	A-SRS NOT TRANSMITTED
001	COMB 0 + FREQUENCY POSITION 0
010	COMB 0 + FREQUENCY POSITION 1
011	COMB 0 + FREQUENCY POSITION 2
100	COMB 1 + FREQUENCY POSITION 0
101	COMB 1 + FREQUENCY POSITION 1
110	COMB 1 + FREQUENCY POSITION 2
111	N/A

FIG. 5A

BIT	SRS TRANSMISSION CONTENT
000	A-SRS NOT TRANSMITTED
001	COMB 0 + CS 0
010	COMB 0 + CS 1
011	COMB 0 + CS 2
100	COMB 1 + CS 0
101	COMB 1 + CS 1
110	COMB 1 + CS 2
111	N/A

FIG. 5B

BIT	SRS TRANSMISSION CONTENT
000	A-SRS NOT TRANSMITTED
001	FREQUENCY POSITION 0 + CS 0
010	FREQUENCY POSITION 0 + CS 1
011	FREQUENCY POSITION 0 + CS 2
100	FREQUENCY POSITION 1 + CS 0
101	FREQUENCY POSITION 1 + CS 1
110	FREQUENCY POSITION 1 + CS 2
111	N/A

FIG. 5C

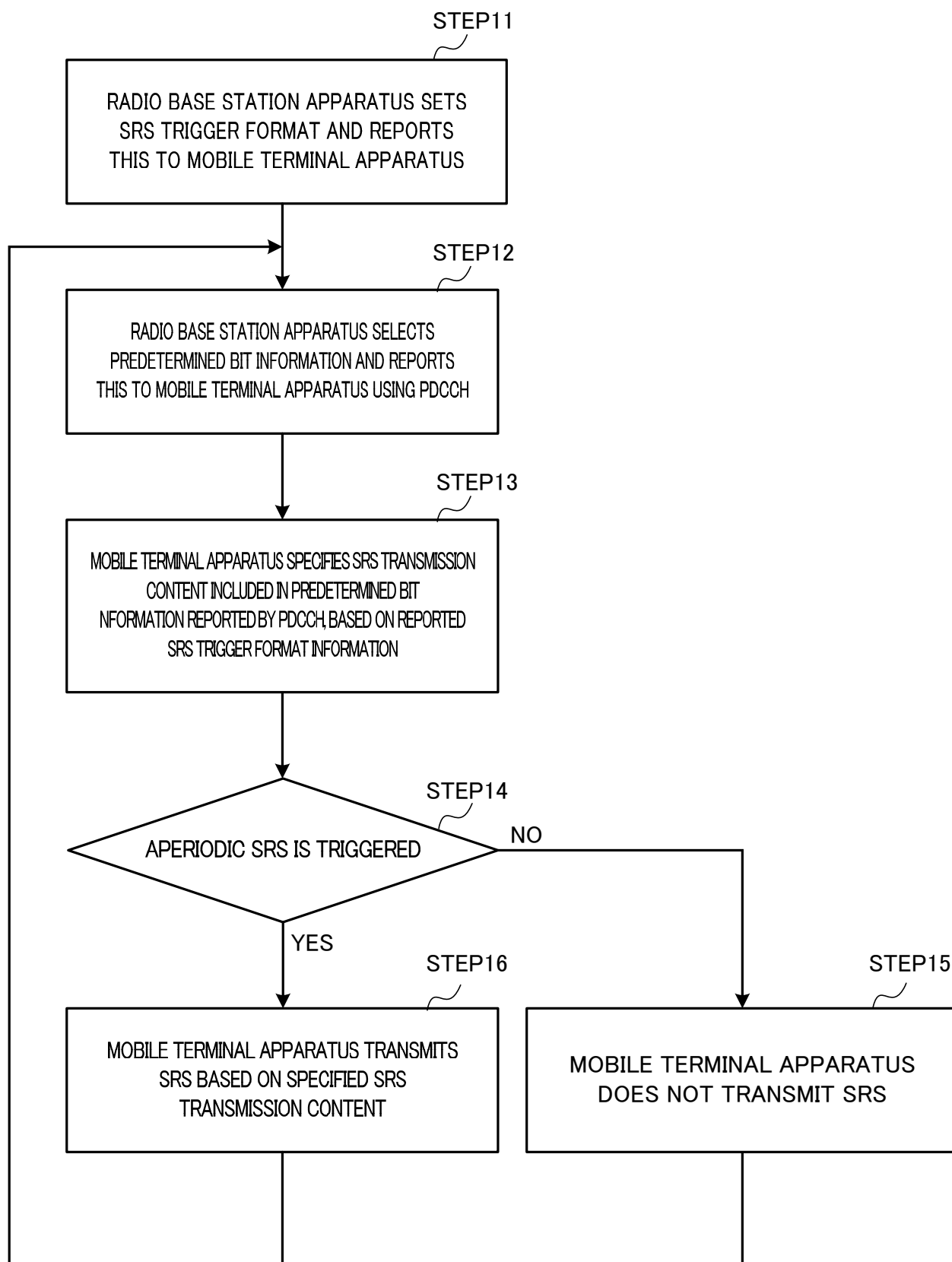


FIG. 6

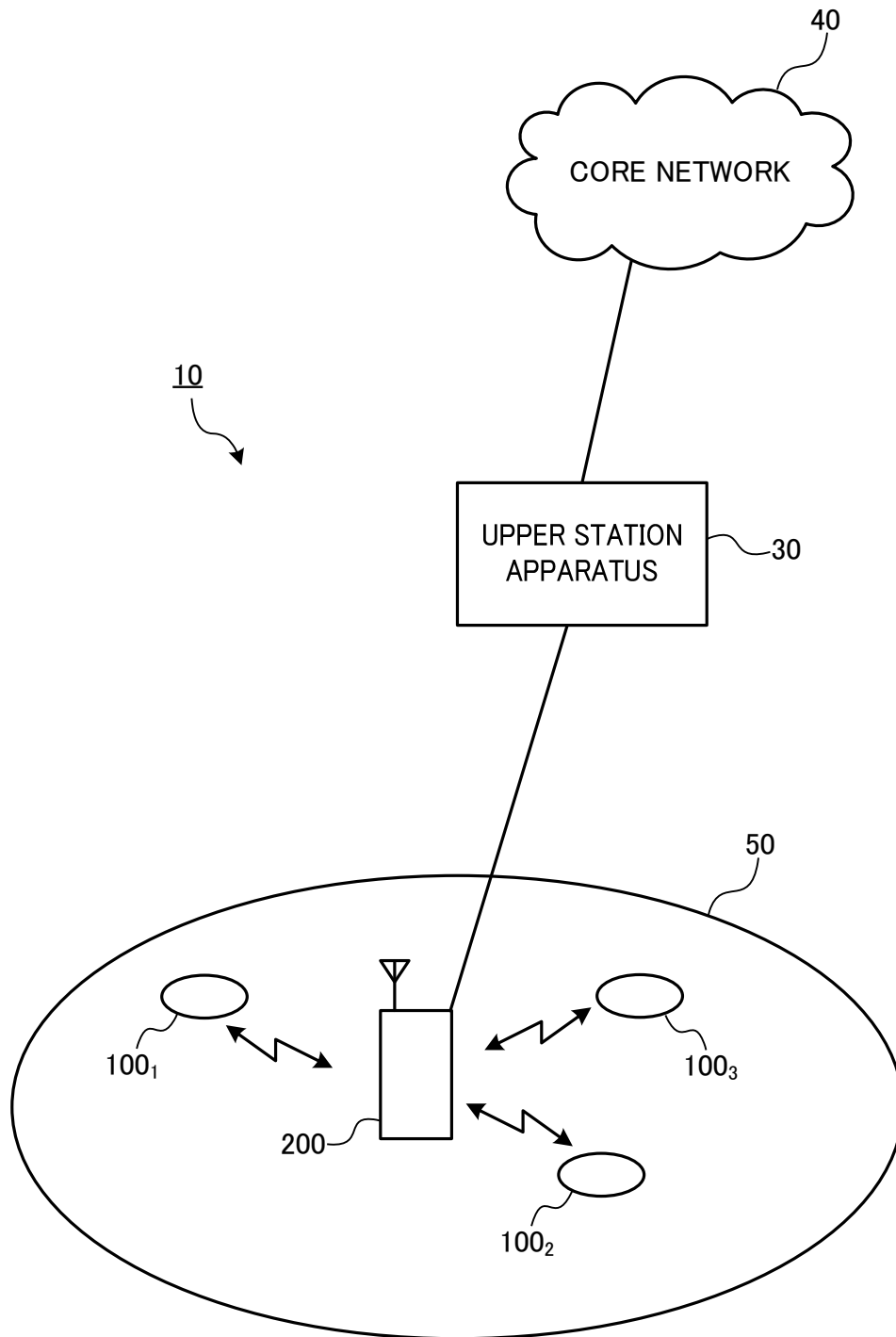


FIG. 7

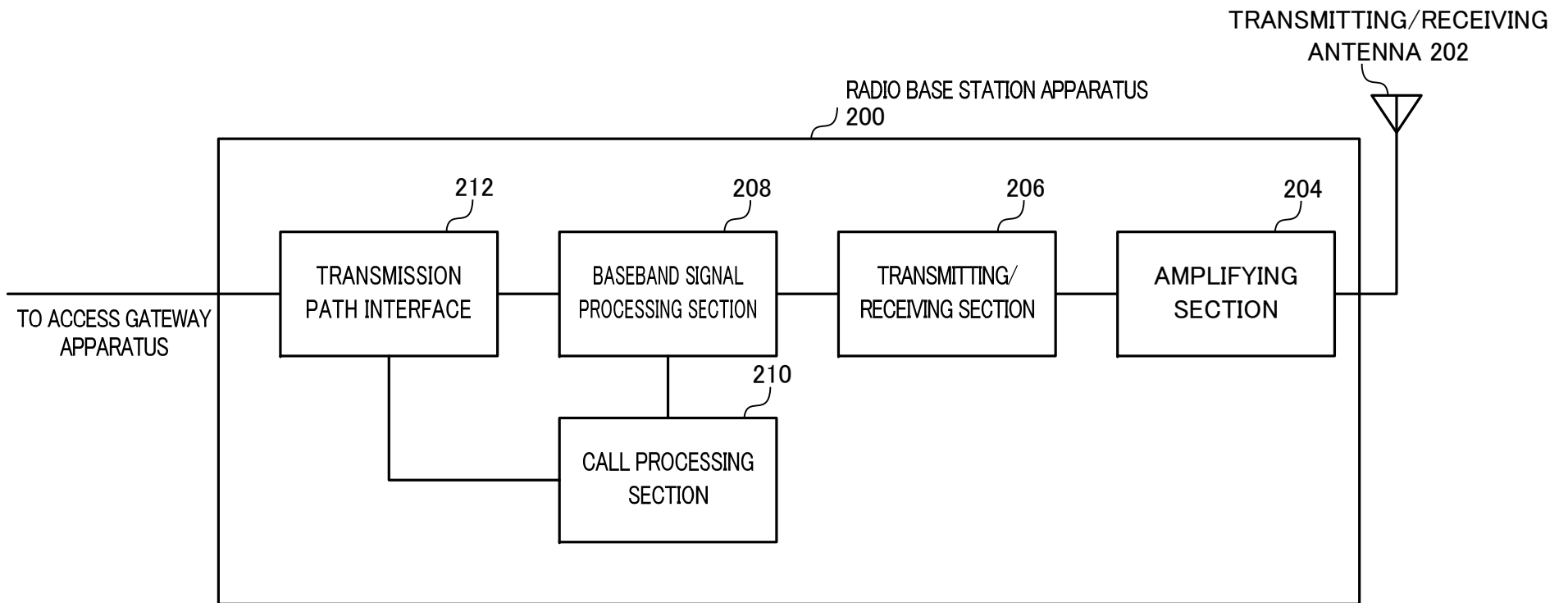


FIG. 8

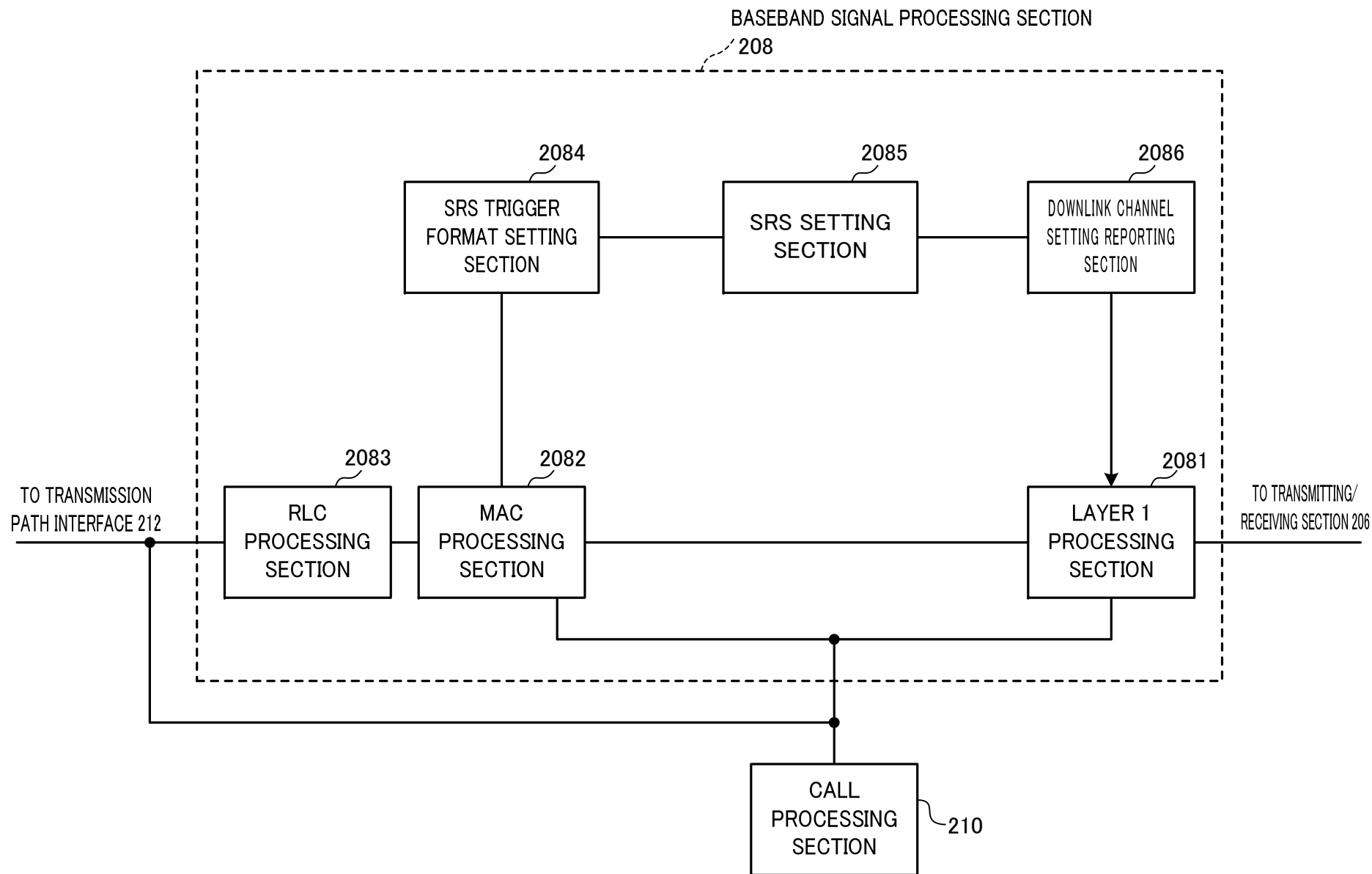


FIG. 9

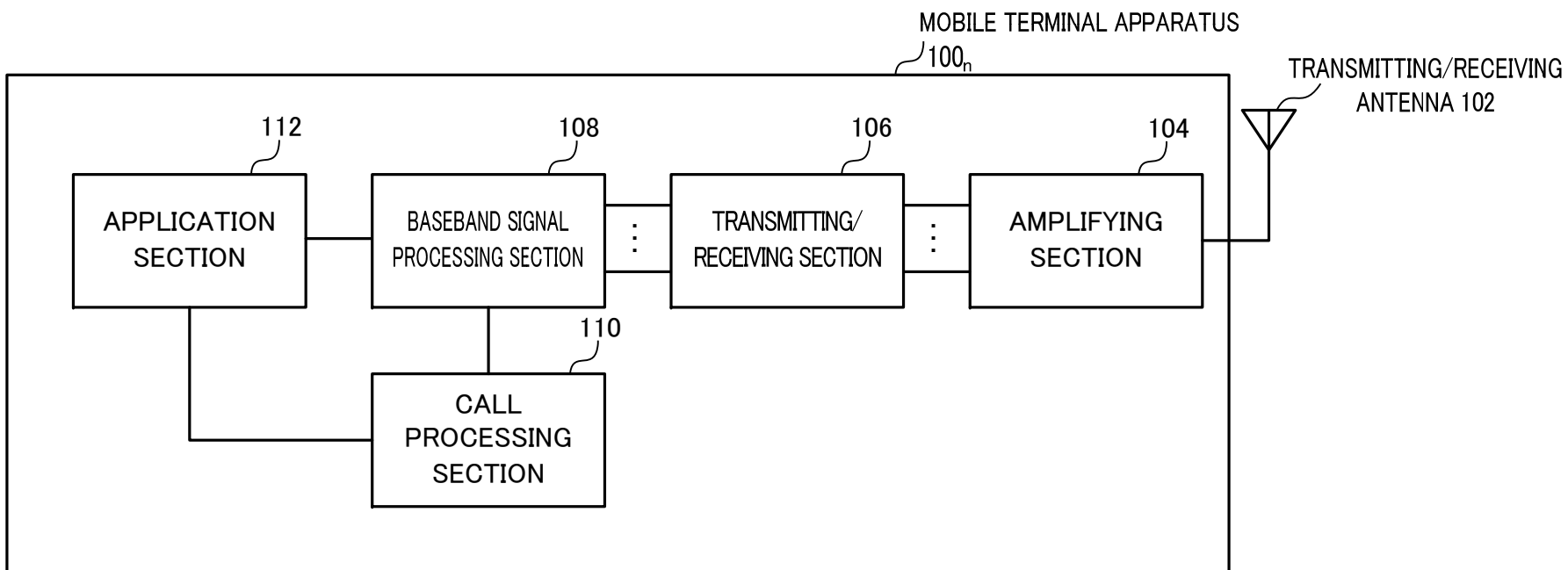


FIG. 10

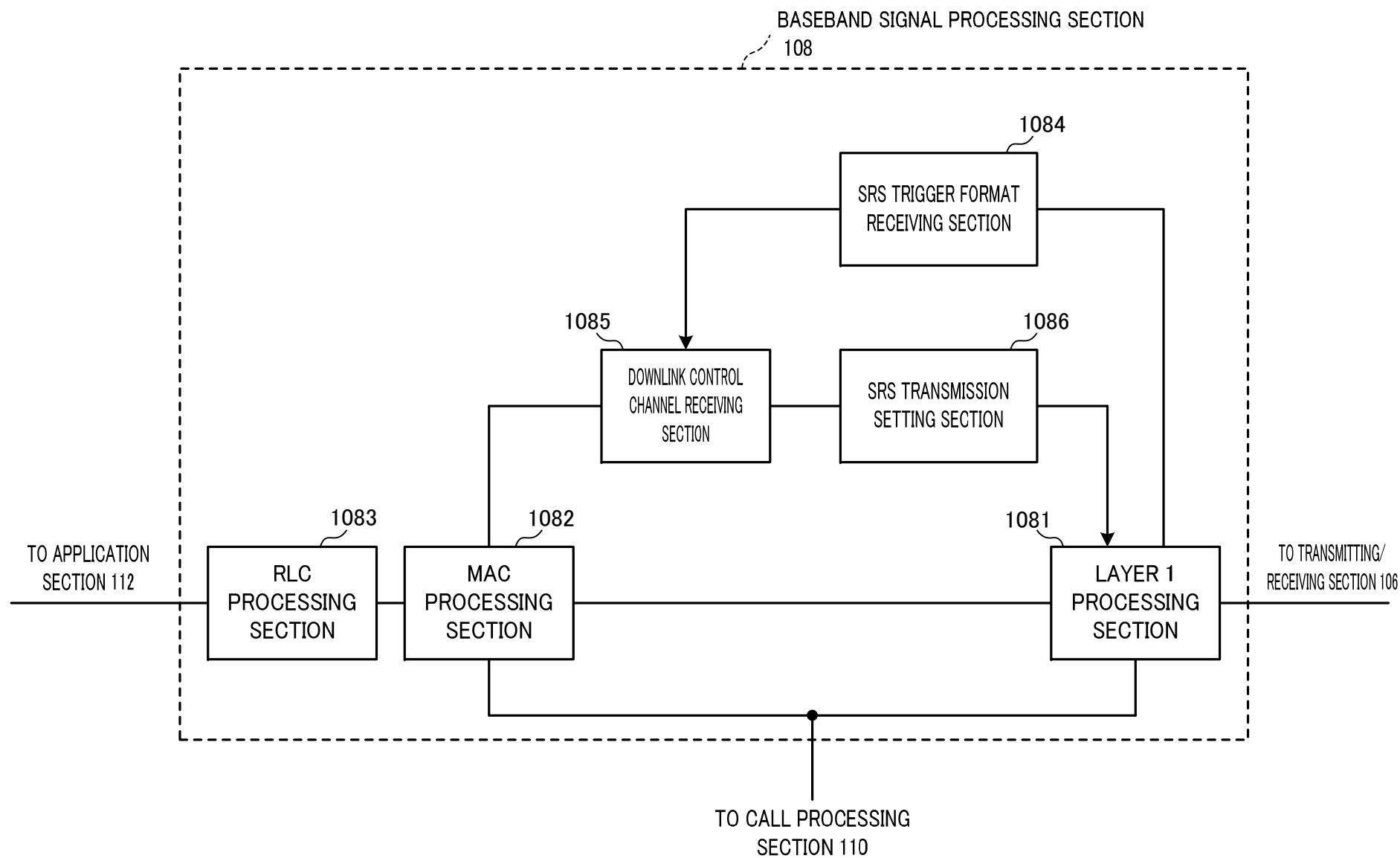


FIG. 11

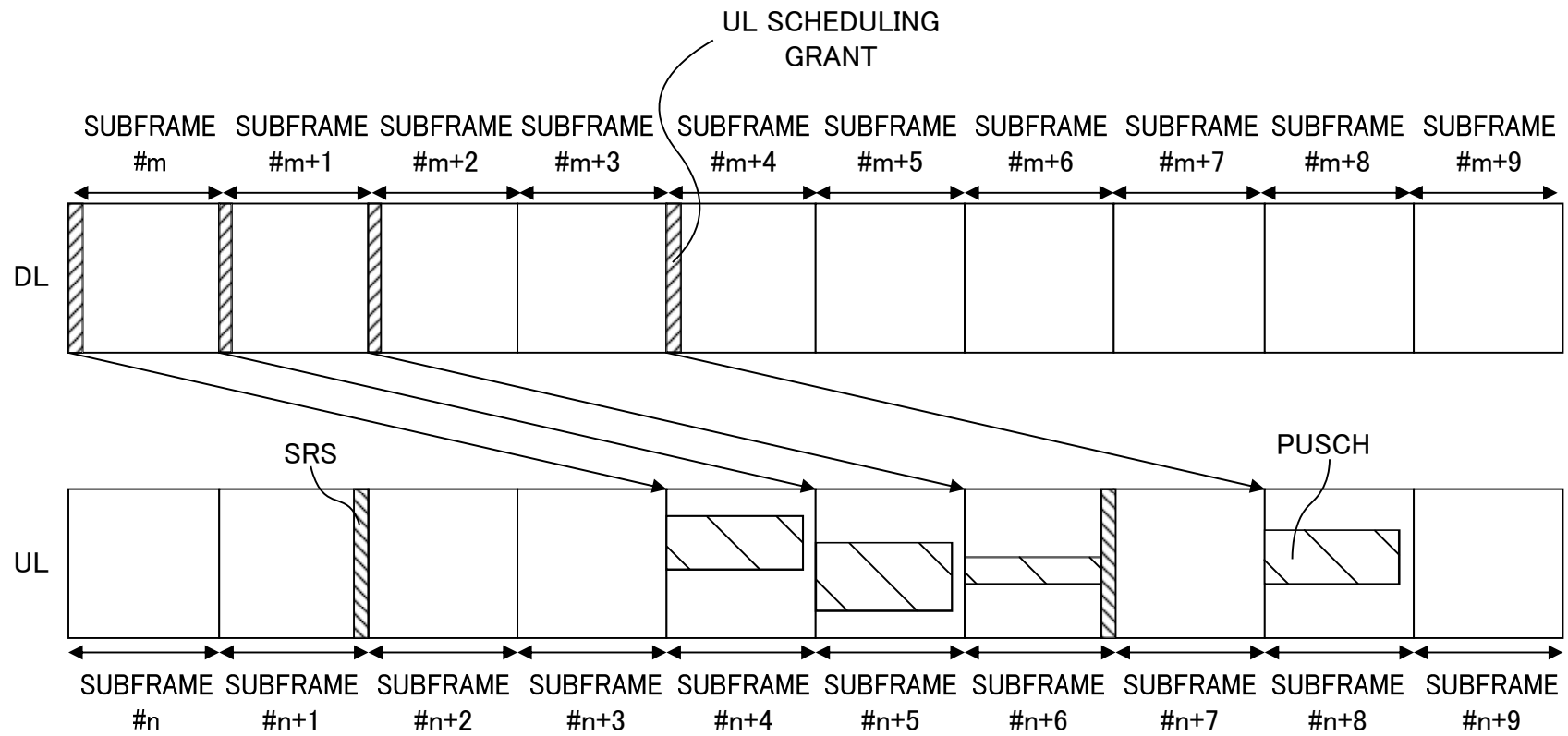


FIG. 12

BIT	SRS TRANSMISSION CONTENT
0	A-SRS NOT TRANSMITTED
1	TRANSMIT BY DEFAULT X

FIG. 13A DCI FORMAT 0

BIT	SRS TRANSMISSION CONTENT
00	A-SRS NOT TRANSMITTED
01	TRANSMIT BY DEFAULT a
10	TRANSMIT BY DEFAULT b
11	TRANSMIT BY DEFAULT c

FIG. 13B DCI FORMAT 4
(WHEN THERE ARE TWO BITS)

BIT	SRS TRANSMISSION CONTENT
00	A-SRS NOT TRANSMITTED
01	TRANSMIT BY DEFAULT <i>a</i>
10	TRANSMIT BY DEFAULT <i>b</i>
11	TRANSMIT BY DEFAULT <i>c</i>

FIG. 14A WHEN THERE ARE TWO BITS

BIT	SRS TRANSMISSION CONTENT
000	A-SRS NOT TRANSMITTED
001	TRANSMIT BY DEFAULT
010	TRANSMIT BY SHIFTING CS FROM DEFAULT BY VALUE OF X
011	TRANSMIT BY SHIFTING CS FROM DEFAULT BY VALUE OF Y
100	TRANSMIT BY SHIFTING CS FROM DEFAULT BY VALUE OF Z
101	TRANSMIT BY DIFFERENT COMB FROM DEFAULT
110	TRANSMIT BY DIFFERENT COMB FROM DEFAULT + SHIFTING CS BY VALUE OF X
111	TRANSMIT BY DIFFERENT COMB FROM DEFAULT + SHIFTING CS BY VALUE OF Y

FIG. 14B WHEN THERE ARE THREE BITS

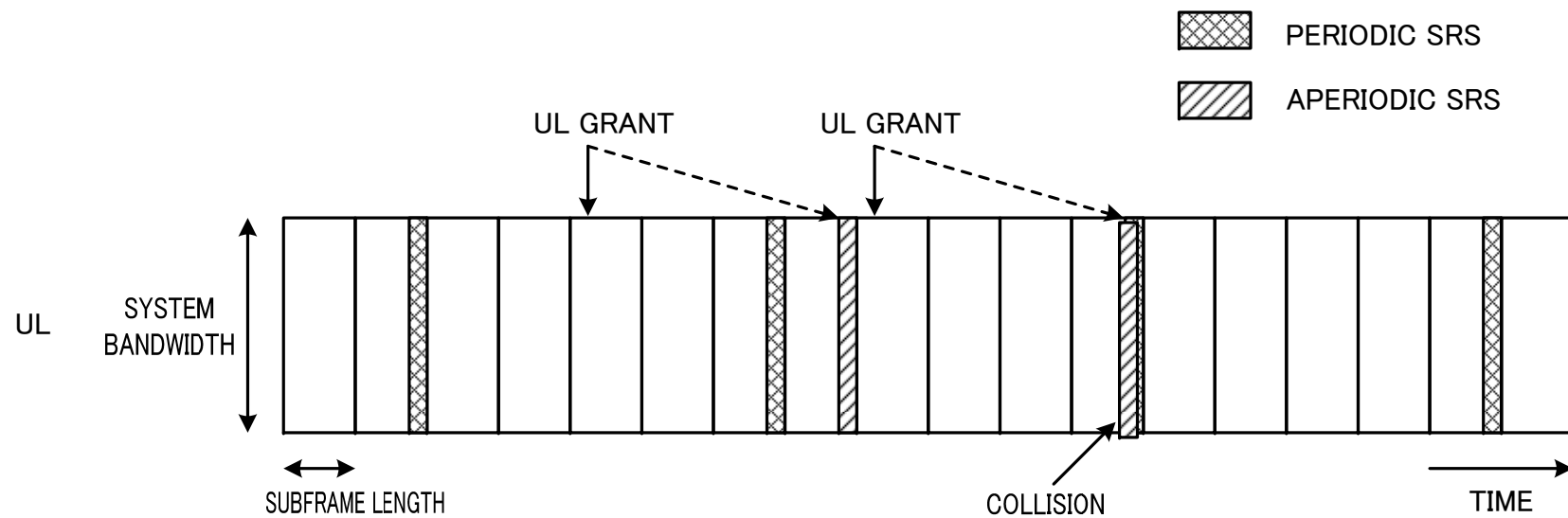


FIG. 15