CLAIMS

1. An apparatus for wireless communications, comprising: a processing system configured to generate frames for transmission during a sector sweep procedure, each frame including one or more address fields being determined based on at least one of a transmitter address of the apparatus or a receiver address of an intended recipient of the generated frames and having fewer bits than the transmitter address or the receiver address, and wherein each frame lacks a sector sweep feedback field; and an interface configured to output the frames for transmission during the sector sweep procedure.

2. The apparatus of claim 1, wherein each frame also includes a sector sweep field that indicates both a sector ID value and a sector sweep countdown value.

3. The apparatus of claim 2, wherein the sector ID value and sector sweep countdown value are the same.

4. The apparatus of claim 1, wherein each frame also includes a sector sweep field that has a first number of one or more bits to indicate a countdown value for a sector ID and a second number of one or more bits to indicate a countdown for an antenna configuration.

5. The apparatus of claim 1, further comprising: a transmitter configured to transmit the frames during the sector sweep procedure, wherein the apparatus is configured as a wireless node.

6. An apparatus for wireless communications, comprising: a processing system configured to generate frames for transmission during a sector sweep procedure, each frame including one or more address fields being determined based on at least one of a transmitter address of the apparatus or a receiver address of an intended recipient of the generated frames and having fewer bits than the transmitter address or the receiver address, and wherein each frame also includes a frame check sequence (FCS) comprising a length of less than 4 bytes; and an interface configured to output the frames for transmission during the sector sweep procedure.

7. An apparatus for wireless communications, comprising: a processing system configured to generate frames for transmission during a sector sweep procedure, each frame including one or more address fields being determined based on at least one of a transmitter address of the apparatus or a receiver address of an intended recipient of the generated frames and having fewer bits than the transmitter address and the receiver address combined, wherein the processing system is configured to determine the one or more address fields based on a hash function applied to at least one of the transmitter address or the receiver address; and an interface configured to output the frames for transmission during the sector sweep procedure.

8. An apparatus for wireless communications, comprising: a processing system configured to generate frames for transmission during a sector sweep procedure, each frame including one or more address fields being determined based on at least one of a transmitter address of the apparatus or a receiver address of an intended recipient of the generated frames and having fewer bits than the transmitter address or the receiver address, wherein the processing system is configured to generate the one or more address fields based, at least in part, on one or more scrambler seeds to be used to scramble the frames prior to transmission; and an interface configured to output the frames for transmission during the sector sweep procedure.

9. The apparatus of claim 8, wherein the one or more address fields, in at least one of the frames, comprise: a first address value that is independent of a scrambler seed; and a second address value generated based on the first address value and a scrambler seed.

10. An apparatus for wireless communications, comprising: a processing system configured to generate frames for transmission during a sector sweep procedure, each frame including one or more address fields being determined based on at least one of a transmitter address of the apparatus or a receiver address of an intended recipient of the generated frames and having fewer bits than the transmitter address and the receiver address combined, wherein the processing system is configured to generate the one or more address fields in each frame based, at least in part, on check values of a header portion of the frame; and an interface configured to output the frames for transmission during the sector sweep procedure.

11. An apparatus for wireless communications, comprising: a processing system configured to generate frames for transmission during a sector sweep procedure, each frame including one or more address fields being determined based on at least one of a transmitter address of the apparatus or a receiver address of an intended recipient of the generated frames and having fewer bits than the transmitter address or the receiver address, wherein each of the frames comprises a field having a frame format type with a value indicating the one or more address fields have fewer bits than the transmitter address or the receiver address; and an interface configured to output the frames for transmission during the sector sweep procedure.

12. An apparatus for wireless communications, comprising: an interface configured to obtain frames during a sector sweep procedure, each frame including one or more address fields having fewer bits than a transmitter address of a transmitter of the frame or a receiver address of an intended recipient of the frame; and a processing system configured to determine at least one of the transmitter address or the receiver address, based on the one or more address fields and additional information, and to process a remaining portion of the frame based on the determination.

13. The apparatus of claim 12, wherein: the additional information comprises one or more addresses stored at the apparatus; and a value of the one or more address fields indicates one of the stored addresses.

14. The apparatus of claim 12, wherein the processing system is configured to discard one of the frames if at least one of: a receiver address determined based on the one or more address fields of that frame does not match an address of the apparatus; or a transmitter address determined based on the one or more address fields of that frame does not match an address of a desired transmitter.

15. The apparatus of claim 12, wherein: each frame also includes a sector sweep field; and the processing system is configured to determine both a sector ID value and a sector sweep countdown value based on the sector sweep field and update a status of the sector sweep procedure based on the sector ID value and the sector sweep countdown value.

16. The apparatus of claim 15, wherein the sector ID value and sector sweep countdown value are the same.

17. The apparatus of claim 12, wherein: each frame also includes a frame check sequence (FCS) comprising a length of less than 4 bytes; and the processing system is configured to generate, for each frame, an FCS based on the frame and to discard the frame if the generated FCS does not match the FCS included in the frame.

18. The apparatus of claim 12, wherein: the processing system is configured to apply a hash function to at least one of the transmitter address or the receiver address and to compare results obtained from the application to the one or more addresses; and the determination is based on the comparison.

19. The apparatus of claim 12, wherein: each of the frames comprises a field having a frame format type; and the processing system is configured to identify, based on a value of the frame format type, that the one or more address fields has fewer bits than at least one of the transmitter address or the receiver address and to process the one or more address fields based on the identification.

20. The apparatus of claim 19, wherein the processing system is further configured to process at least one of a sector sweep field of one or more of the obtained frames or a frame check sequence (FCS) field of one or more of the obtained frames based on the identification.

21. The apparatus of claim 12, wherein the determination is further based on one or more scrambler seeds.

22. The apparatus of claim 21, wherein: the one or more address fields, in at least one of the frames, comprise a first address value that is independent of a scrambler seed and a second address value generated based on the first address value and a scrambler seed; and the processing system is configured to determine the scrambler seed based on the second value, wherein the one or more scrambler seeds comprise the determined scrambler seed.

23. The apparatus of claim 12, further comprising: a receiver configured to receive the frames during the sector sweep procedure, wherein the apparatus is configured as a wireless node.