

ABSTRACT OF THE DISCLOSURE

PRECISION, HIGH SPEED DELAY SYSTEM FOR
5 PROVIDING DELAYED CLOCK EDGES WITH
NEW DELAY VALUES EVERY CLOCK PERIOD

A precision delay system (67, 70, 120) allowing clock edges to be delayed with new
delay values every clock period T. The internal delay elements (61, 62, 73, 101, 102) are
10 reprogrammed every clock cycle with reprogramming transients suppressed by clock
independent blanking circuitry (75, 121). The system (67, 70, 120) allows the use of delay
elements (61, 62, 73, 101, 102) with a maximum delay of one-half ($T/2$) the clock period to
continuously span a full clock cycle T delay range with full cycle-by-cycle reprogramming.

15 Figure 7



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