

ABSTRACT OF THE DISCLOSURE

Apparatus for developing synchronization of an intermediate layer of signal such as the transport or multiplex layer of a multi-layered compressed video signal, includes at the encoding end of the system a modulo K counter (23) which is
5 clocked responsive to a system clock (22), and the count valued is embedded (13) in the signal at the transport layer according to a predetermined schedule. At the receiving end of the system a similar counter is responsive to a controlled receiver clock signal (26) and the count value of this counter is sampled (18) at the
10 arrival of the count values embedded in the transport layer. The differences of successive sampled count values of the receiver counter are compared with the differences of corresponding successive values of the embedded count values in the transport layer to provide a signal to control (27) the receiver clock signal.

15

