

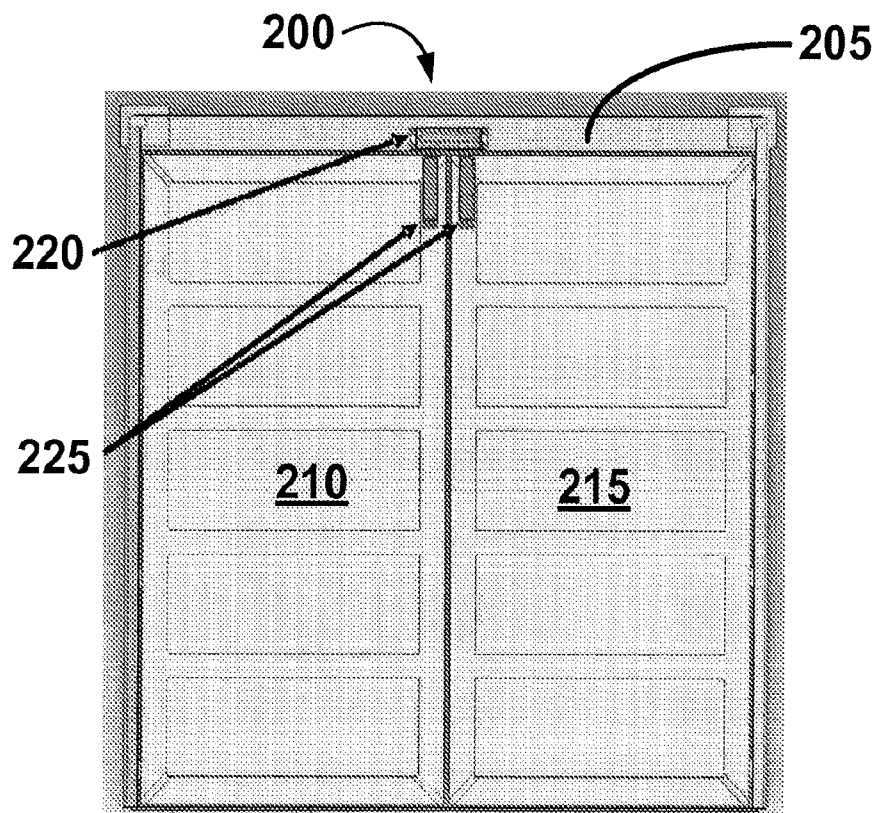
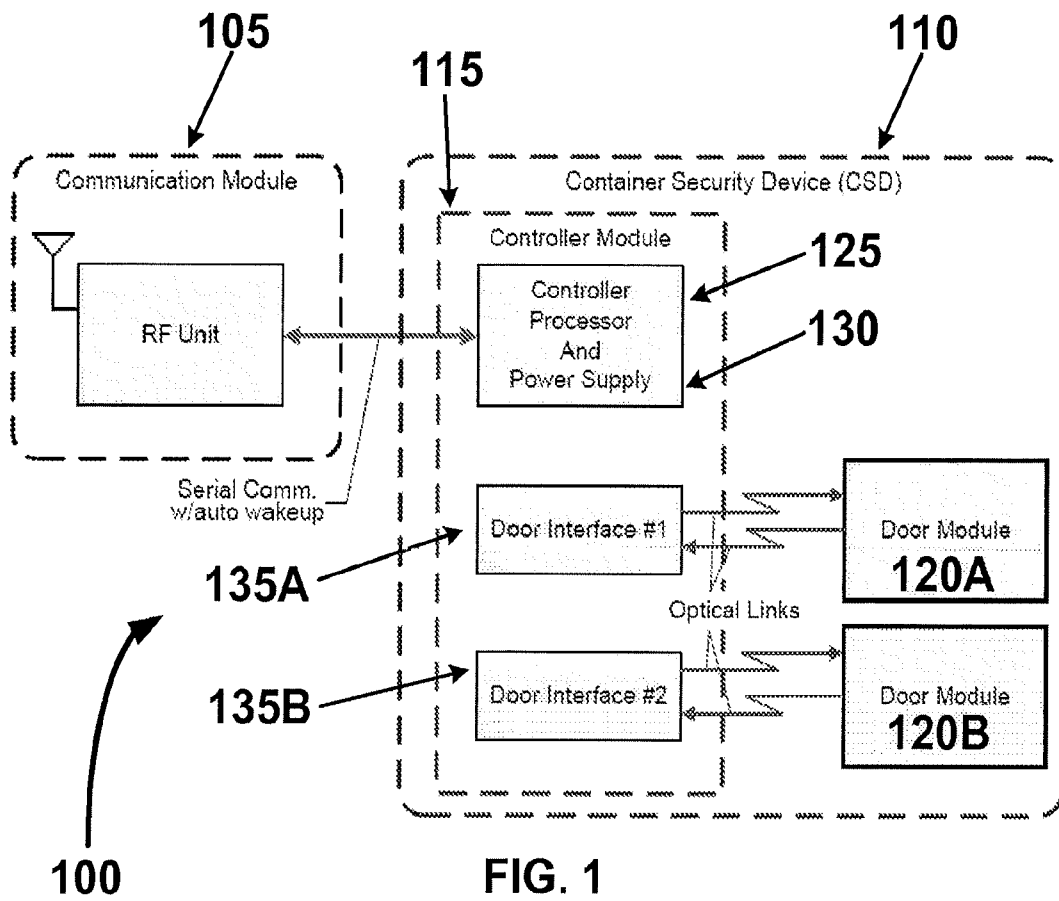


FIG. 2

FIG. 3

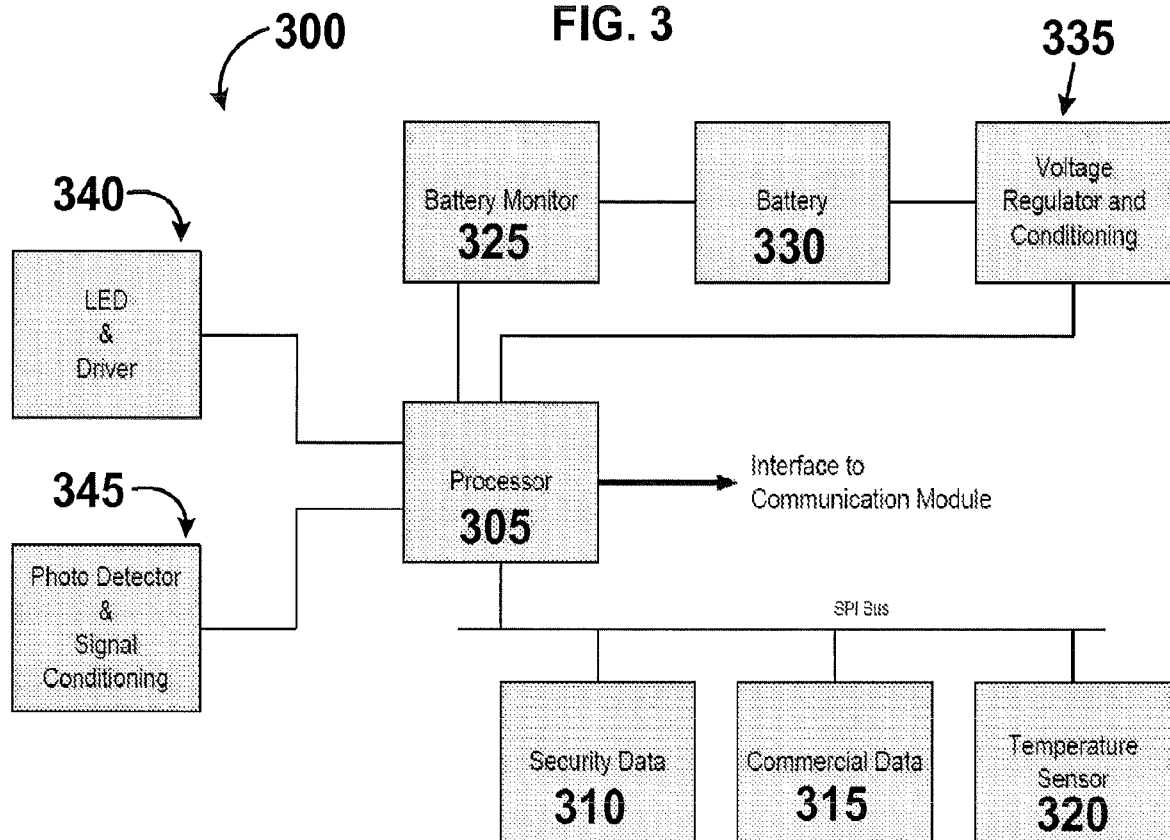


FIG. 4

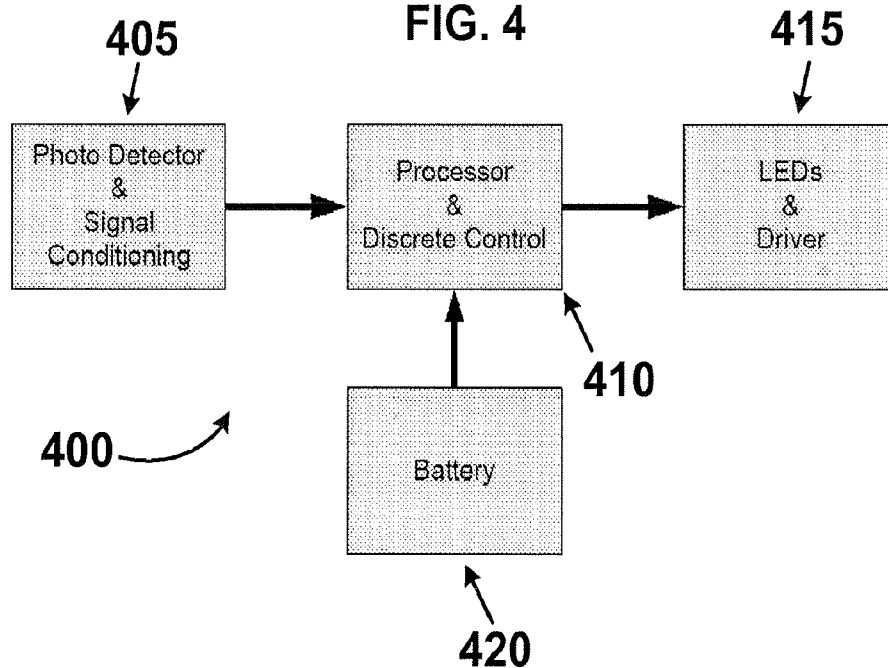


FIG. 5

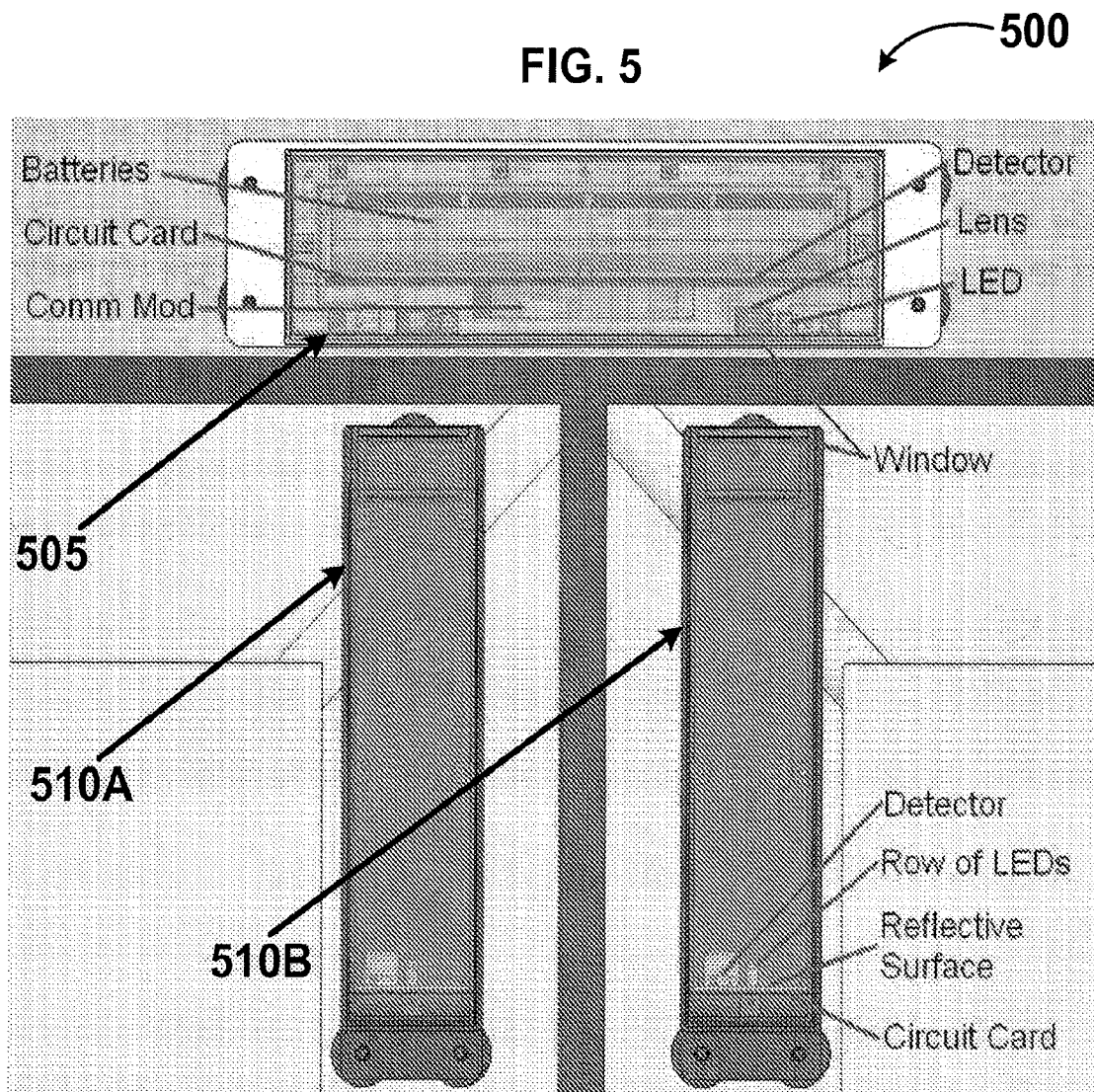


FIG. 6

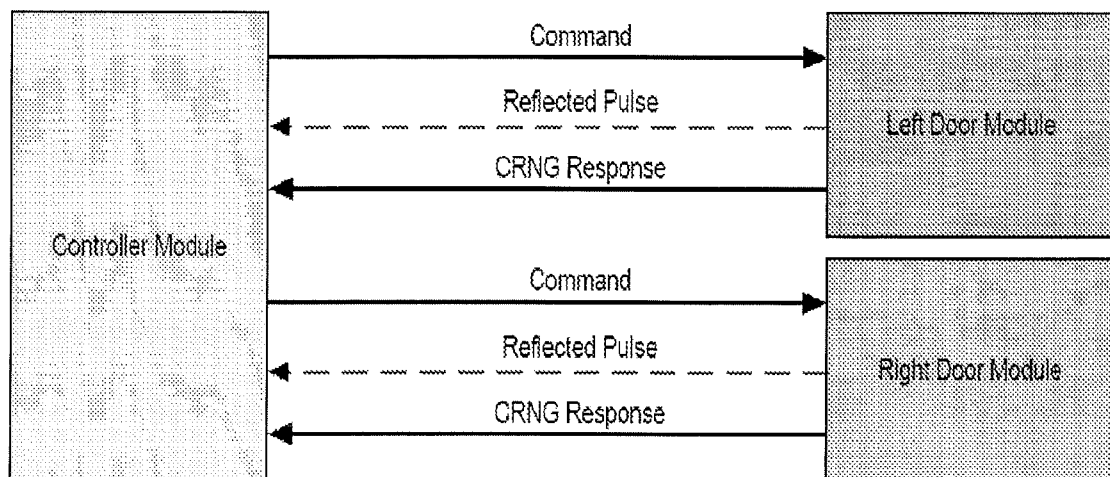


FIG. 7

Source Module	Receiving Module	Measured Quantities	Purpose
Controller	Door	Pulse timing	Command communications
Door	Controller	Optical power	Detect optical path change – dust, tampering
		Pulse timing	Response communications
		Irradiance centroid position	Detection of lateral movement of door
Controller	Controller	Optical power	Detection of door module presence
		Irradiance centroid position	Detection of angular movement of door

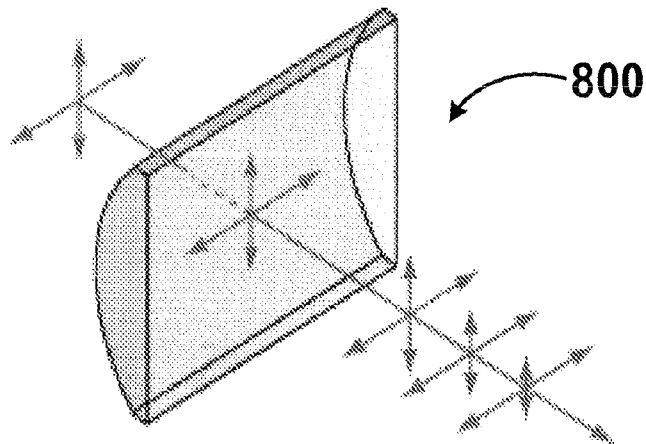
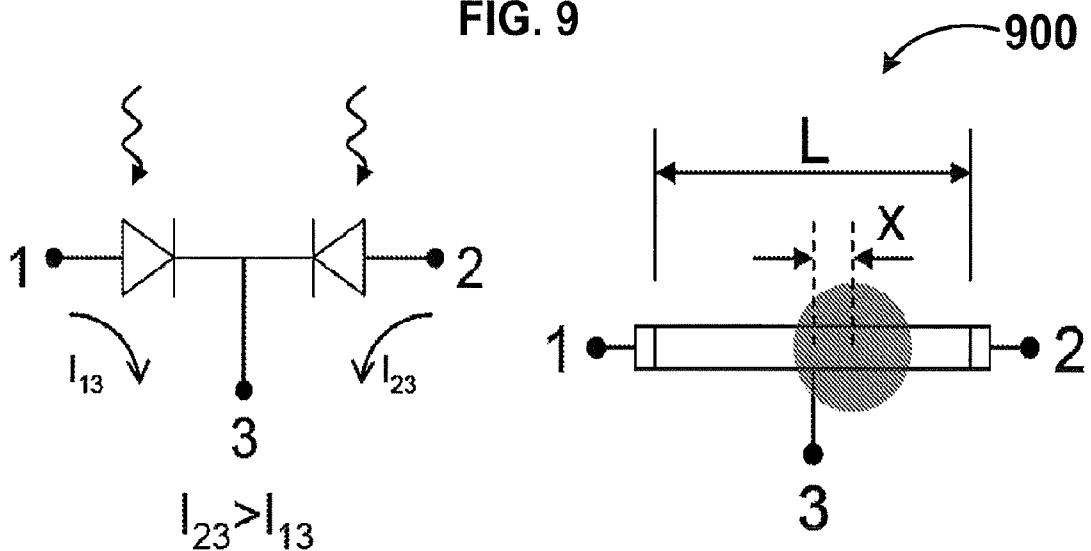
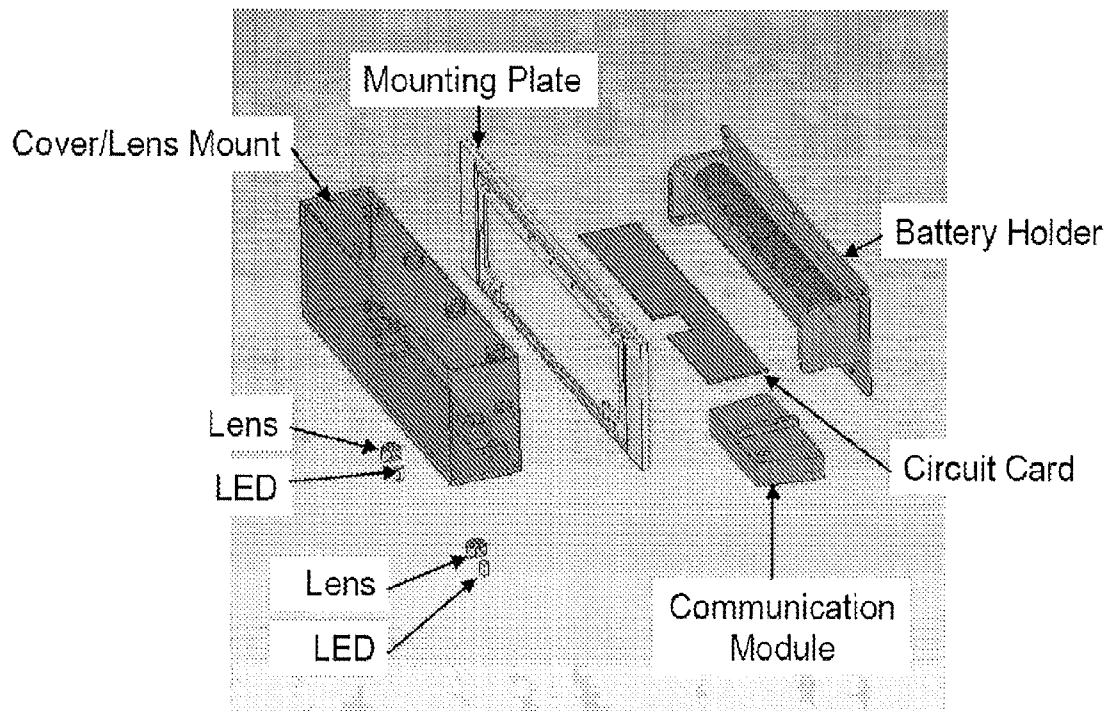
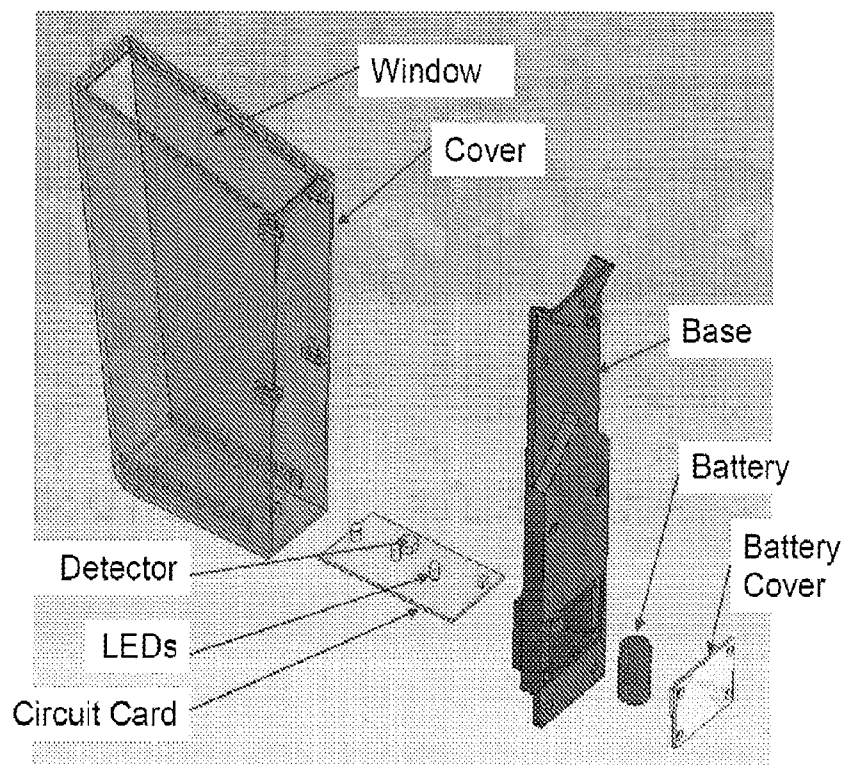
FIG. 8**FIG. 9**

FIG. 10

1000

**FIG. 11**

1100



1200

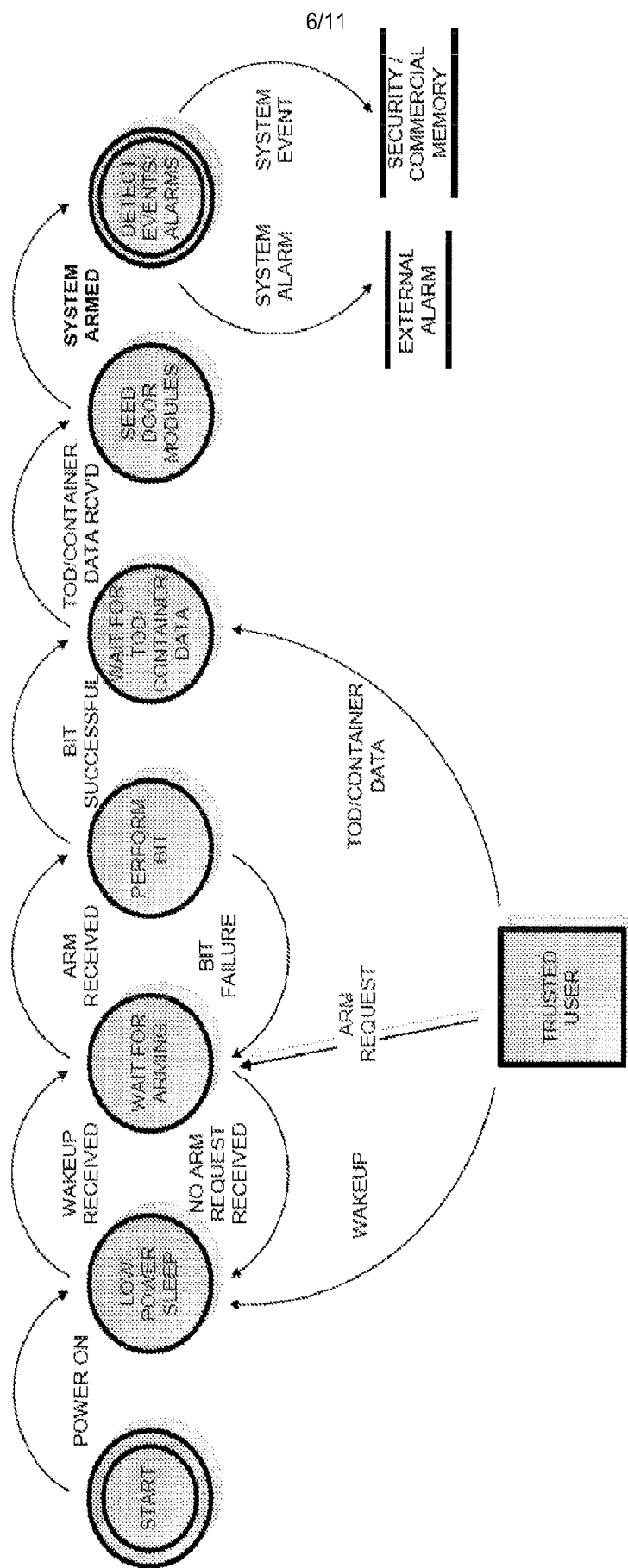


FIG. 12

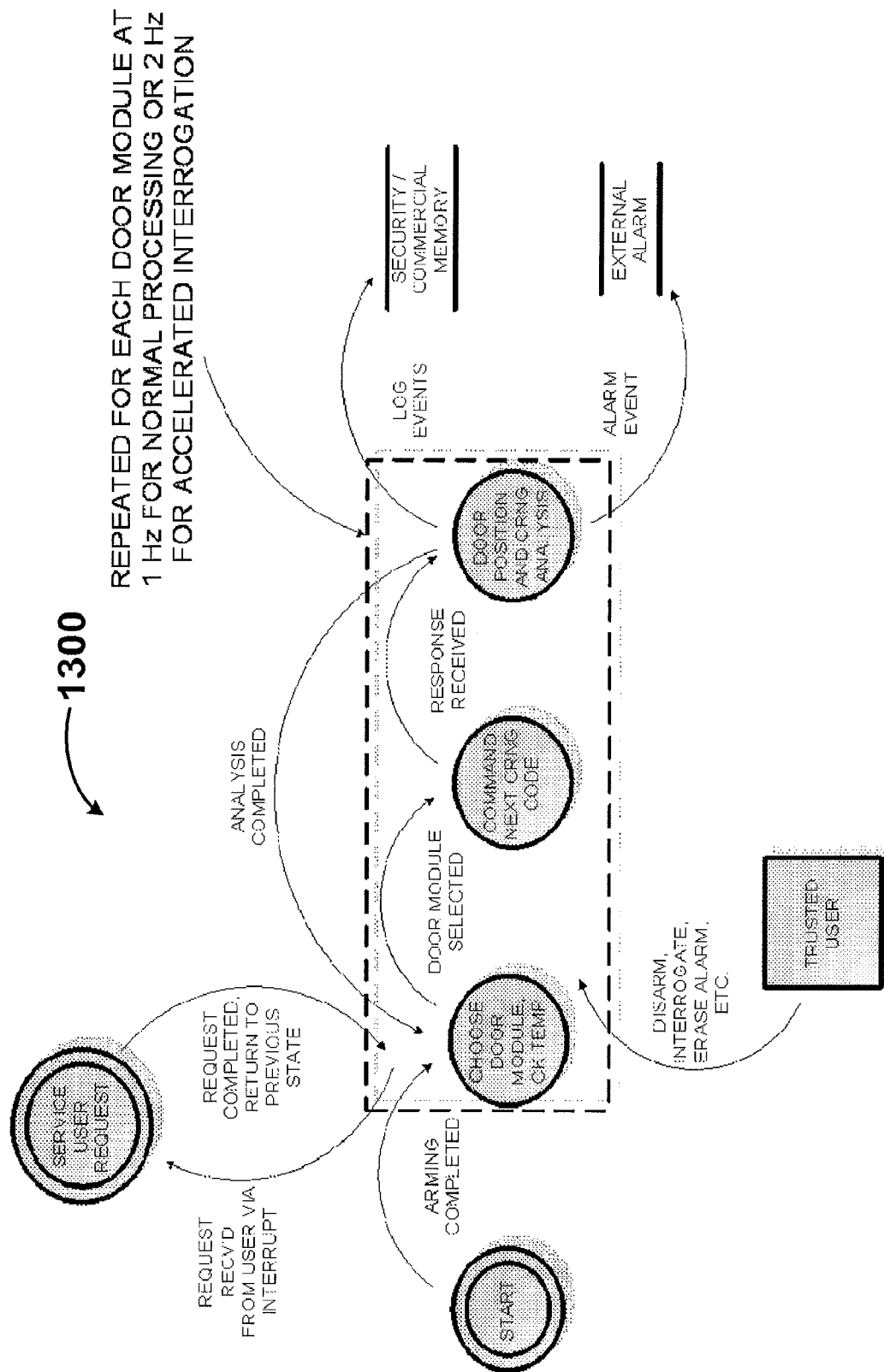


FIG. 13

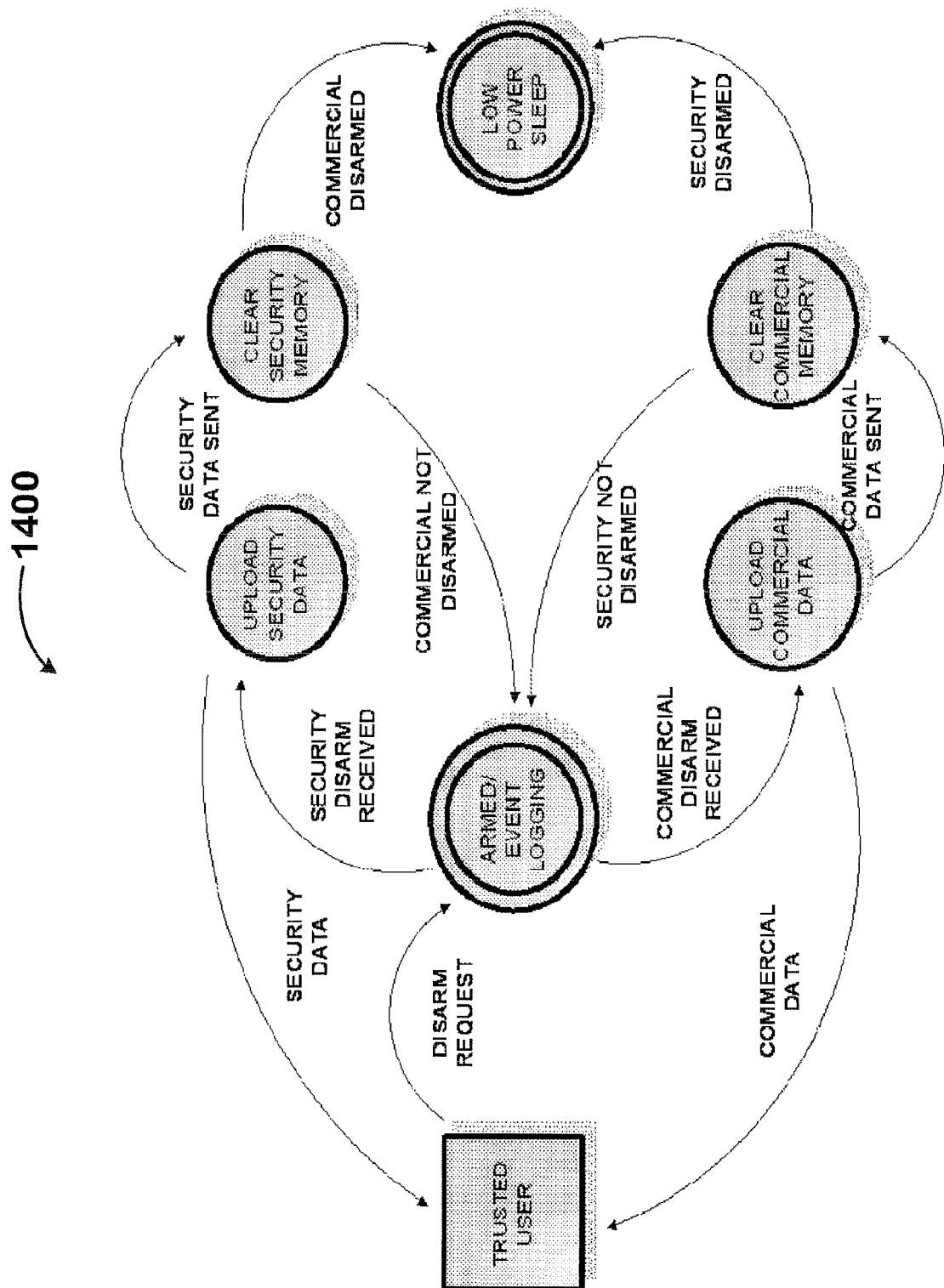
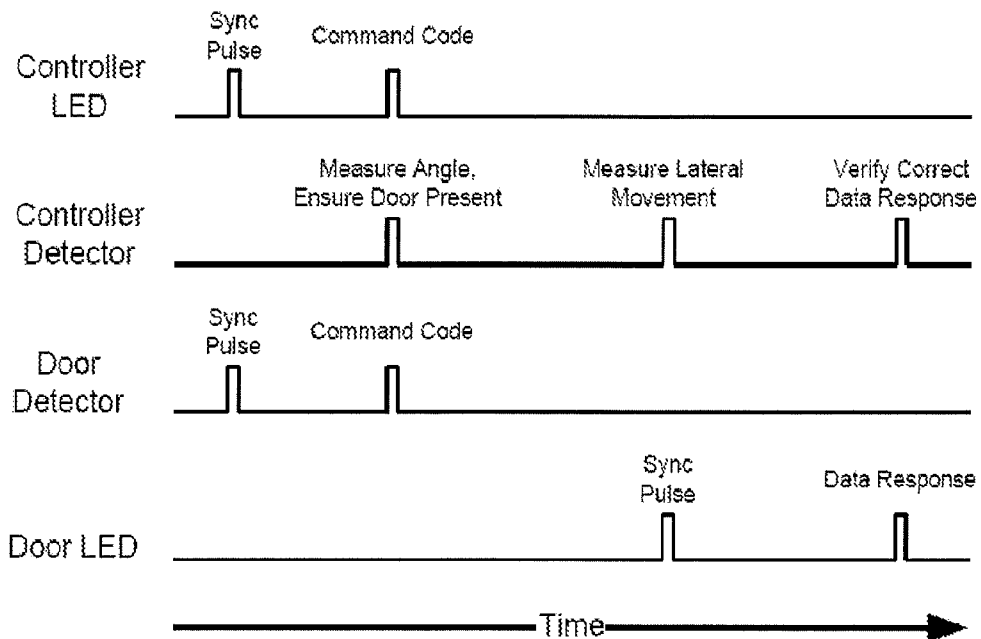


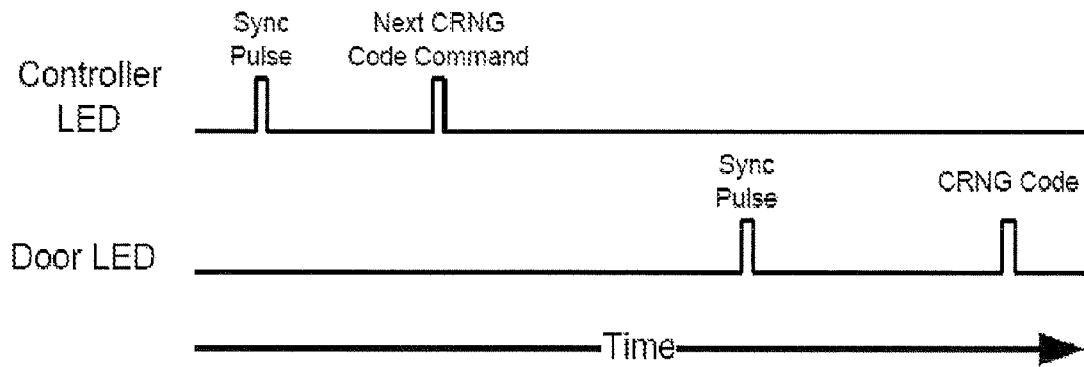
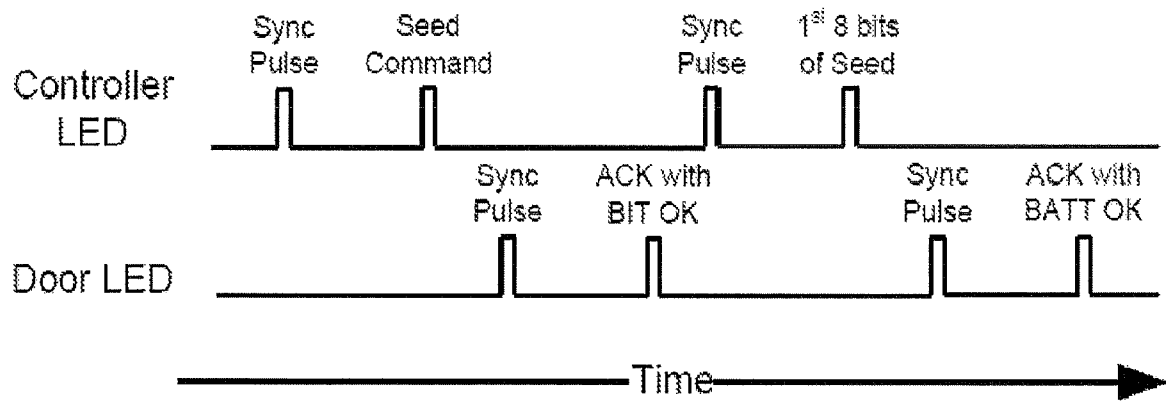
FIG. 14

FIG. 15**FIG. 16**

Time Slot	Definition	Comments
0	Sync Pulse	Instruct door module to prepare for next command
1 – 225	Delay	Allows controller time to enable LED reflection measurement and door module time to prepare for command/data pulse
226 – 255	Commands	Interrogate, Accept Seed, Report CRNG byte, Report Status, Test LEDs/Timing, Use LED, Report Firmware Version, Future Expansion
256 – 511	Data Value	Eight bit portion of 32-bit Seed Value

FIG. 17

Time Slot	Definition	Comments
0	Sync Pulse	Provide baseline for time slot definitions
1 – 225	Delay	Processing time
226 – 255	Status	ACK with battery status, ACK with BIT status, Future Expansion
256 – 511	Data Value	Eight bit portion of 32-bit CRNG code, Door module firmware version

**FIG. 18****FIG. 19**

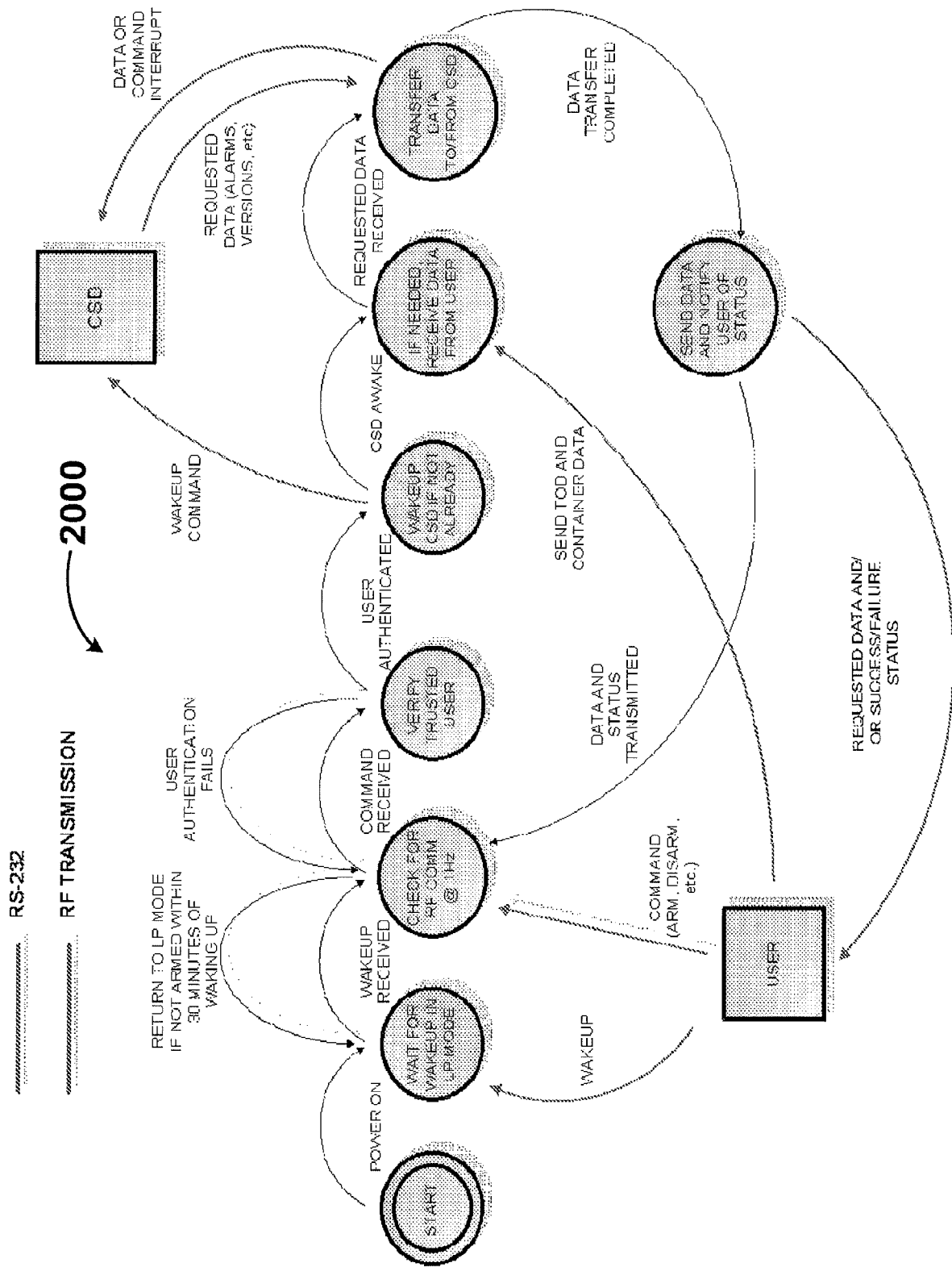


FIG. 20