

VERTICAL PANNING FOR INTERLACED VIDEO

Abstract

A vertical panning system comprises: a source (12) of respective signals having a vertical synchronizing frequency and a horizontal synchronizing frequency; another source (24) of
5 signal having a multiple ($2f_H$) of the horizontal synchronizing frequency; a video display (30) having an interlaced field mode of operation; a horizontal display control circuit (28) coupled to the video display and responsive to the supplied horizontal
synchronizing frequency signal; and, a vertical display control
10 circuit (22) coupled to the video display and responsive to a second vertical synchronizing frequency signal (VOUT). The system is characterized by a digital phase delay circuit (14), responsive to the supplied multiple horizontal synchronizing frequency signal, the supplied vertical synchronizing frequency
15 signal and to a panning control signal (PCS), having as an output said second vertical synchronizing frequency signal (VOUT) delayed in phase relative to the supplied vertical synchronizing frequency signal by any one of a plurality of delays differing in increments corresponding in duration to horizontal half line
20 intervals;

FIGURE 1