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(54) Title: ARCHITECTURES FOR IMAGER ARRAYS AND ARRAY CAMERAS

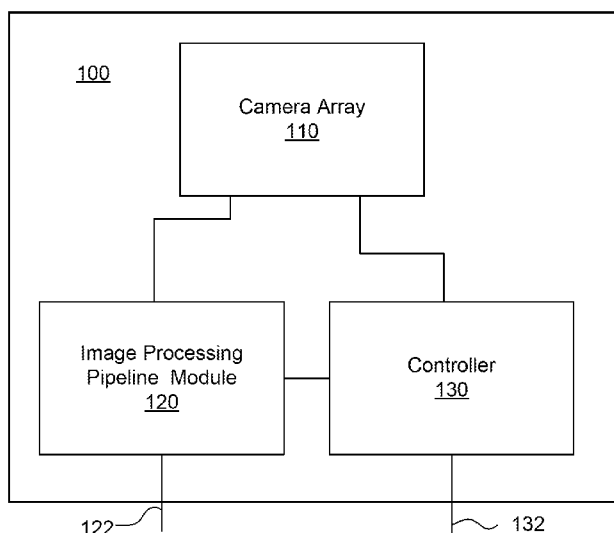


FIG. 1

(57) Abstract: Architectures for imager arrays configured for use in array cameras in accordance with embodiments of the invention are described. One embodiment of the invention includes a plurality of focal planes, where each focal plane comprises a two dimensional arrangement of pixels having at least two pixels in each dimension and each focal plane is contained within a region of the imager array that does not contain pixels from another focal plane, control circuitry configured to control the capture of image information by the pixels within the focal planes, where the control circuitry is configured so that the capture of image information by the pixels in at least two of the focal planes is separately controllable, and sampling circuitry configured to convert pixel outputs into digital pixel data.

110 Réseau de caméras
120 Module de pipeline de traitement d'image
130 Dispositif de commande

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