

Audio Encoder and Decoder using a Frequency Domain Processor, a Time Domain Processor, and a Cross Processor for Continuous Initialization

5 Specification

The present invention relates to audio signal encoding and decoding and, in particular, to audio signal processing using parallel frequency domain and time domain encoder/decoder processors.

The perceptual coding of audio signals for the purpose of data reduction for efficient storage or transmission of these signals is a widely used practice. In particular when lowest bit rates are to be achieved, the employed coding leads to a reduction of audio quality that often is primarily caused by a limitation at the encoder side of the audio signal bandwidth to be transmitted. Here, typically the audio signal is low-pass filtered such that no spectral waveform content remains above a certain pre-determined cut-off frequency.

In contemporary codecs well-known methods exist for the decoder-side signal restoration through audio signal Bandwidth Extension (BWE), e.g. Spectral Band Replication (SBR) that operates in frequency domain or so-called Time Domain Bandwidth Extension (TD-BWE) being is a post-processor in speech coders that operates in time domain.

Additionally, several combined time domain/frequency domain coding concepts exist such as concepts known under the term AMR-WB+ or USAC.

All these combined time domain/coding concepts have in common that the frequency domain coder relies on bandwidth extension technologies which incur a band limitation into the input audio signal and the portion above a cross-over frequency or border frequency is encoded with a low resolution coding concept and synthesized on the decoder-side. Hence, such concepts mainly rely on a pre-processor technology on the encoder side and a corresponding post-processing functionality on the decoder-side.

Typically, the time domain encoder is selected for useful signals to be encoded in the time domain such as speech signals and the frequency domain encoder is selected for non-speech signals, music signals, etc. However, specifically for non-speech signals having prominent harmonics in the high frequency band, the prior art frequency domain encoders

have a reduced accuracy and, therefore, a reduced audio quality due to the fact that such prominent harmonics can only be separately parametrically encoded or are eliminated at all in the encoding/decoding process.

- 5 Furthermore, concepts exist in which the time domain encoding/decoding branch additionally relies on the bandwidth extension which also parametrically encodes an upper frequency range while a lower frequency range is typically encoded using an ACELP or any other CELP related coder, for example a speech coder. This bandwidth extension functionality increases the bitrate efficiency but, on the other hand, introduces further
- 10 inflexibility due to the fact that both encoding branches, i.e., the frequency domain encoding branch and the time domain encoding branch are band limited due to the bandwidth extension procedure or spectral band replication procedure operating above a certain crossover frequency substantially lower than the maximum frequency included in the input audio signal.

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Relevant topics in the state-of-art comprise

- SBR as a post-processor to waveform decoding [1-3]
- MPEG-D USAC core switching [4]
- MPEG-H 3D IGF [5]

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The following papers and patents describe methods that are considered to constitute prior art for the application:

- [1] M. Dietz, L. Liljeryd, K. Kjörning and O. Kunz, "Spectral Band Replication, a novel
- 25 approach in audio coding," in 112th AES Convention, Munich, Germany, 2002.

[2] S. Meltzer, R. Böhm and F. Henn, "SBR enhanced audio codecs for digital broadcasting such as "Digital Radio Mondiale" (DRM)," in 112th AES Convention, Munich, Germany, 2002.

- [3] T. Ziegler, A. Ehret, P. Ekstrand and M. Lutzky, "Enhancing mp3 with SBR: Features and Capabilities of the new mp3PRO Algorithm," in 112th AES Convention,
- 30 Munich, Germany, 2002.

[4] MPEG-D USAC Standard.

[5] PCT/EP2014/065109.

- 35 In MPEG-D USAC, a switchable core coder is described. However, in USAC, the band-limited core is restricted to always transmit a low-pass filtered signal. Therefore, certain

music signals that contain prominent high frequency content e.g. full-band sweeps, triangle sounds, etc. cannot be reproduced faithfully.

It is an object of the present invention to provide an improved concept for audio coding.

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This object is achieved by an audio coder encoder of claim 1, an audio decoder of claim 10, a method of audio encoding of claim 15, a method of audio decoding of claim 16 or a computer program of claim 17.

- 10 The present invention is based on the finding that a time domain encoding/decoding processor can be combined with a frequency domain encoding/decoding processor having a gap filling functionality but this gap filling functionality for filling spectral holes is operated over the whole band of the audio signal or at least above a certain gap filling frequency. Importantly, the frequency domain encoding/decoding processor is particularly
- 15 in the position to perform accurate or wave form or spectral value encoding/decoding up to the maximum frequency and not only until a crossover frequency. Furthermore, the full-band capability of the frequency domain encoder for encoding with the high resolution allows an integration of the gap filling functionality into the frequency domain encoder.
- 20 In one aspect, full band gap filling is combined with a time-domain encoding/decoding processor. In embodiments, the sampling rates in both branches are equal or the sampling rate in the time-domain encoder branch is lower than in the frequency domain branch.
- 25 In another aspect, a frequency domain encoder/decoder operating without gap filling but performing a full band core encoding/decoding is combined with a time-domain encoding processor and a cross processor is provided for continuous initialization of the time-domain encoding/decoding processor. In this aspect, the sampling rates can be as in the other aspect, or the sampling rates in the frequency domain branch are even lower than in
- 30 the time-domain branch.

Hence, in accordance with the present invention by using the full-band spectral encoder/decoder processor, the problems related to the separation of the bandwidth extension on the one hand and the core coding on the other hand can be addressed and

35 overcome by performing the bandwidth extension in the same spectral domain in which the core decoder operates. Therefore, a full rate core decoder is provided which encodes

and decodes the full audio signal range. This does not require the need for a downsampler on the encoder side and an upsampler on the decoder side. Instead, the whole processing is performed in the full sampling rate or full-bandwidth domain. In order to obtain a high coding gain, the audio signal is analyzed in order to find a first set of first spectral portions which has to be encoded with a high resolution, where this first set of first spectral portions may include, in an embodiment, tonal portions of the audio signal. On the other hand, non-tonal or noisy components in the audio signal constituting a second set of second spectral portions are parametrically encoded with low spectral resolution. The encoded audio signal then only requires the first set of first spectral portions encoded in a waveform-preserving manner with a high spectral resolution and, additionally, the second set of second spectral portions encoded parametrically with a low resolution using frequency "tiles" sourced from the first set. On the decoder side, the core decoder, which is a full-band decoder, reconstructs the first set of first spectral portions in a waveform-preserving manner, i.e., without any knowledge that there is any additional frequency regeneration. However, the so generated spectrum has a lot of spectral gaps. These gaps are subsequently filled with the Intelligent Gap Filling (IGF) technology by using a frequency regeneration applying parametric data on the one hand and using a source spectral range, i.e., first spectral portions reconstructed by the full rate audio decoder on the other hand.

In further embodiments, spectral portions, which are reconstructed by noise filling only rather than bandwidth replication or frequency tile filling, constitute a third set of third spectral portions. Due to the fact that the coding concept operates in a single domain for the core coding/decoding on the one hand and the frequency regeneration on the other hand, the IGF is not only restricted to fill up a higher frequency range but can fill up lower frequency ranges, either by noise filling without frequency regeneration or by frequency regeneration using a frequency tile at a different frequency range.

Furthermore, it is emphasized that an information on spectral energies, an information on individual energies or an individual energy information, an information on a survive energy or a survive energy information, an information a tile energy or a tile energy information, or an information on a missing energy or a missing energy information may comprise not only an energy value, but also an (e.g. absolute) amplitude value, a level value or any other value, from which a final energy value can be derived. Hence, the information on an energy may e.g. comprise the energy value itself, and/or a value of a level and/or of an amplitude and/or of an absolute amplitude.

A further aspect is based on the finding that the correlation situation is not only important for the source range but is also important for the target range. Furthermore, the present invention acknowledges the situation that different correlation situations can occur in the source range and the target range. When, for example, a speech signal with high frequency noise is considered, the situation can be that the low frequency band comprising the speech signal with a small number of overtones is highly correlated in the left channel and the right channel, when the speaker is placed in the middle. The high frequency portion, however, can be strongly uncorrelated due to the fact that there might be a different high frequency noise on the left side compared to another high frequency noise or no high frequency noise on the right side. Thus, when a straightforward gap filling operation would be performed that ignores this situation, then the high frequency portion would be correlated as well, and this might generate serious spatial segregation artifacts in the reconstructed signal. In order to address this issue, parametric data for a reconstruction band or, generally, for the second set of second spectral portions which have to be reconstructed using a first set of first spectral portions is calculated to identify either a first or a second different two-channel representation for the second spectral portion or, stated differently, for the reconstruction band. On the encoder side, a two-channel identification is, therefore calculated for the second spectral portions, i.e., for the portions, for which, additionally, energy information for reconstruction bands is calculated. A frequency regenerator on the decoder side then regenerates a second spectral portion depending on a first portion of the first set of first spectral portions, i.e., the source range and parametric data for the second portion such as spectral envelope energy information or any other spectral envelope data and, additionally, dependent on the two-channel identification for the second portion, i.e., for this reconstruction band under reconsideration.

The two-channel identification is preferably transmitted as a flag for each reconstruction band and this data is transmitted from an encoder to a decoder and the decoder then decodes the core signal as indicated by preferably calculated flags for the core bands. Then, in an implementation, the core signal is stored in both stereo representations (e.g. left/right and mid/side) and, for the IGF frequency tile filling, the source tile representation is chosen to fit the target tile representation as indicated by the two-channel identification flags for the intelligent gap filling or reconstruction bands, i.e., for the target range.

It is emphasized that this procedure not only works for stereo signals, i.e., for a left channel and the right channel but also operates for multi-channel signals. In the case of multi-channel signals, several pairs of different channels can be processed in that way such as a left and a right channel as a first pair, a left surround channel and a right surround as the second pair and a center channel and an LFE channel as the third pair. Other pairings can be determined for higher output channel formats such as 7.1, 11.1 and so on.

A further aspect is based on the finding that the audio quality of the reconstructed signal can be improved through IGF since the whole spectrum is accessible to the core encoder so that, for example, perceptually important tonal portions in a high spectral range can still be encoded by the core coder rather than parametric substitution. Additionally, a gap filling operation using frequency tiles from a first set of first spectral portions which is, for example, a set of tonal portions typically from a lower frequency range, but also from a higher frequency range if available, is performed. For the spectral envelope adjustment on the decoder side, however, the spectral portions from the first set of spectral portions located in the reconstruction band are not further post-processed by e.g. the spectral envelope adjustment. Only the remaining spectral values in the reconstruction band which do not originate from the core decoder are to be envelope adjusted using envelope information. Preferably, the envelope information is a full-band envelope information accounting for the energy of the first set of first spectral portions in the reconstruction band and the second set of second spectral portions in the same reconstruction band, where the latter spectral values in the second set of second spectral portions are indicated to be zero and are, therefore, not encoded by the core encoder, but are parametrically coded with low resolution energy information.

It has been found that absolute energy values, either normalized with respect to the bandwidth of the corresponding band or not normalized, are useful and very efficient in an application on the decoder side. This especially applies when gain factors have to be calculated based on a residual energy in the reconstruction band, the missing energy in the reconstruction band and frequency tile information in the reconstruction band.

Furthermore, it is preferred that the encoded bitstream not only covers energy information for the reconstruction bands but, additionally, scale factors for scale factor bands extending up to the maximum frequency. This ensures that for each reconstruction band, for which a certain tonal portion, i.e., a first spectral portion is available, this first set of first

spectral portion can actually be decoded with the right amplitude. Furthermore, in addition to the scale factor for each reconstruction band, an energy for this reconstruction band is generated in an encoder and transmitted to a decoder. Furthermore, it is preferred that the reconstruction bands coincide with the scale factor bands or in case of energy grouping,
5 at least the borders of a reconstruction band coincide with borders of scale factor bands.

A further implementation of this invention applies a tile whitening operation. Whitening of a spectrum removes the coarse spectral envelope information and emphasizes the spectral fine structure which is of foremost interest for evaluating tile similarity. Therefore, a
10 frequency tile on the one hand and/or the source signal on the other hand are whitened before calculating a cross correlation measure. When only the tile is whitened using a predefined procedure, a whitening flag is transmitted indicating to the decoder that the same predefined whitening process shall be applied to the frequency tile within IGF.

15 Regarding the tile selection, it is preferred to use the lag of the correlation to spectrally shift the regenerated spectrum by an integer number of transform bins. Depending on the underlying transform, the spectral shifting may require addition corrections. In case of odd lags, the tile is additionally modulated through multiplication by an alternating temporal sequence of -1/1 to compensate for the frequency-reversed representation of every other
20 band within the MDCT. Furthermore, the sign of the correlation result is applied when generating the frequency tile.

Furthermore, it is preferred to use tile pruning and stabilization in order to make sure that artifacts created by fast changing source regions for the same reconstruction region or
25 target region are avoided. To this end, a similarity analysis among the different identified source regions is performed and when a source tile is similar to other source tiles with a similarity above a threshold, then this source tile can be dropped from the set of potential source tiles since it is highly correlated with other source tiles. Furthermore, as a kind of tile selection stabilization, it is preferred to keep the tile order from the previous frame if
30 none of the source tiles in the current frame correlate (better than a given threshold) with the target tiles in the current frame.

A further aspect is based on the finding that an improved quality and reduced bitrate specifically for signals comprising transient portions as they occur very often in audio
35 signals is obtained by combining the Temporal Noise Shaping (TNS) or Temporal Tile Shaping (TTS) technology with high frequency reconstruction. The TNS/TTS processing

on the encoder-side being implemented by a prediction over frequency reconstructs the time envelope of the audio signal. Depending on the implementation, i.e., when the temporal noise shaping filter is determined within a frequency range not only covering the source frequency range but also the target frequency range to be reconstructed in a frequency regeneration decoder, the temporal envelope is not only applied to the core audio signal up to a gap filling start frequency, but the temporal envelope is also applied to the spectral ranges of reconstructed second spectral portions. Thus, pre-echoes or post-echoes that would occur without temporal tile shaping are reduced or eliminated. This is accomplished by applying an inverse prediction over frequency not only within the core frequency range up to a certain gap filling start frequency but also within a frequency range above the core frequency range. To this end, the frequency regeneration or frequency tile generation is performed on the decoder-side before applying a prediction over frequency. However, the prediction over frequency can either be applied before or subsequent to spectral envelope shaping depending on whether the energy information calculation has been performed on the spectral residual values subsequent to filtering or to the (full) spectral values before envelope shaping.

The TTS processing over one or more frequency tiles additionally establishes a continuity of correlation between the source range and the reconstruction range or in two adjacent reconstruction ranges or frequency tiles.

In an implementation, it is preferred to use complex TNS/TTS filtering. Thereby, the (temporal) aliasing artifacts of a critically sampled real representation, like MDCT, are avoided. A complex TNS filter can be calculated on the encoder-side by applying not only a modified discrete cosine transform but also a modified discrete sine transform in addition to obtain a complex modified transform. Nevertheless, only the modified discrete cosine transform values, i.e., the real part of the complex transform is transmitted. On the decoder-side, however, it is possible to estimate the imaginary part of the transform using MDCT spectra of preceding or subsequent frames so that, on the decoder-side, the complex filter can be again applied in the inverse prediction over frequency and, specifically, the prediction over the border between the source range and the reconstruction range and also over the border between frequency-adjacent frequency tiles within the reconstruction range.

The inventive audio coding system efficiently codes arbitrary audio signals at a wide range of bitrates. Whereas, for high bitrates, the inventive system converges to transparency, for

low bitrates perceptual annoyance is minimized. Therefore, the main share of available
bitrate is used to waveform code just the perceptually most relevant structure of the signal
in the encoder, and the resulting spectral gaps are filled in the decoder with signal content
that roughly approximates the original spectrum. A very limited bit budget is consumed to
5 control the parameter driven so-called spectral Intelligent Gap Filling (IGF) by dedicated
side information transmitted from the encoder to the decoder.

In further embodiments, the time domain encoding/decoding processor relies on a lower
sampling rate and the corresponding bandwidth extension functionality.

10 In further embodiments, a cross-processor is provided in order to initialize the time domain
encoder/decoder with initialization data derived from the currently processed frequency
domain encoder/decoder signal. This allows that when the currently processed audio
signal portion is processed by the frequency domain encoder, the parallel time domain
15 encoder is initialized so that when a switch from the frequency domain encoder to a time
domain encoder takes place, this time domain encoder can immediately start processing
since all the initialization data relating to earlier signals are already there due to the cross-
processor. This cross-processor is preferably applied on the encoder-side and,
additionally, on the decoder-side and preferably uses a frequency-time transform which
20 additionally performs a very efficient downsampling from the higher output or input
sampling rate into the lower time domain core coder sampling rate by only selecting a
certain low band portion of the domain signal together with a certain reduced transform
size. Thus, a sample rate conversion from the high sampling rate to the low sampling rate
is very efficiently performed and this signal obtained by the transform with the reduced
25 transform size can then be used for initializing the time domain encoder/decoder so that
the time domain encoder/decoder is ready to immediately perform time domain encoding
when this situation is signaled by a controller and the immediately preceding audio signal
portion was encoded in the frequency domain.

30 As outlined, the cross-processor embodiment may rely on gap filling in the frequency
domain or not. Hence, a time- and frequency domain encoder/decoder are combined via
the cross-processor, and the frequency domain encoder/decoder may rely on gap filling or
not. Specifically, certain embodiments as outlined are preferred:

35 These embodiments employ gap filling in the frequency domain and have the following
sampling rate figures and may or may not rely on the cross-processor technology:

Input SR = 8 kHz, ACELP (time domain) SR = 12.8 kHz.

Input SR = 16 kHz, ACELP SR = 12.8 kHz.

Input SR = 16 kHz, ACELP SR = 16.0 kHz

5 Input SR = 32.0 kHz, ACELP SR = 16.0 kHz

Input SR = 48 kHz, ACELP SR = 16 kHz

These embodiments may or may not employ gap filling in the frequency domain and have the following sampling rate figures and rely on the cross-processor technology:

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TCX SR is lower than the ACELP SR (8 kHz vs. 12.8 kHz), or where TCX and ACELP run both at 16.0 kHz, and where any gap filling is not used.

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Hence, preferred embodiments of the present invention allow a seamless switching of a perceptual audio coder comprising spectral gap filling and a time domain encoder with or without bandwidth extension.

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Hence, the present invention relies on methods that are not restricted to removing the high frequency content above a cut-off frequency in the frequency domain encoder from the audio signal but rather signal-adaptively removes spectral band-pass regions leaving spectral gaps in the encoder and subsequently reconstructs these spectral gaps in the decoder. Preferably, an integrated solution such as intelligent gap filling is used that efficiently combines full-bandwidth audio coding and spectral gap filling particularly in the MDCT transform domain.

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Hence, the present invention provides an improved concept for combining speech coding and a subsequent time domain bandwidth extension with a full-band wave form decoding comprising spectral gap filling into a switchable perceptual encoder/decoder.

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Hence, in contrast to already existing methods, the new concept utilizes full-band audio signal wave form coding in the transform domain coder and at the same time allows a seamless switching to a speech coder preferably followed by a time domain bandwidth extension.

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Further embodiments of the present invention avoid the explained problems that occur due to a fixed band limitation. The concept enables the switchable combination of a full-band wave form coder in the frequency domain equipped with a spectral gap filling and a lower sampling rate speech coder and a time domain bandwidth extension. Such a coder is capable of wave form coding the aforementioned problematic signals providing full audio bandwidth up to the Nyquist frequency of the audio input signal. Nevertheless, seamless instant switching between both coding strategies is guaranteed particularly by the embodiments having the cross-processor. For this seamless switching, the cross-processor represents a cross connection at both encoder and decoder between the full-band capable full-rate (input sampling rate) frequency domain encoder and the low-rate ACELP coder having a lower sampling rate to properly initialize the ACELP parameters and buffers particularly within the adaptive codebook, the LPC filter or the resampling stage, when switching from the frequency domain coder such as TCX to the time domain encoder such as ACELP.

The present invention is subsequently discussed with respect to the accompanying drawings in which:

Fig. 1a illustrates an apparatus for encoding an audio signal;

Fig. 1b illustrates a decoder for decoding an encoded audio signal matching with the encoder of Fig. 1a;

Fig. 2a illustrates a preferred implementation of the decoder;

Fig. 2b illustrates a preferred implementation of the encoder;

Fig. 3a illustrates a schematic representation of a spectrum as generated by the spectral domain decoder of Fig. 1b;

Fig. 3b illustrates a table indicating the relation between scale factors for scale factor bands and energies for reconstruction bands and noise filling information for a noise filling band;

- Fig. 4a illustrates the functionality of the spectral domain encoder for applying the selection of spectral portions into the first and second sets of spectral portions;
- 5 Fig. 4b illustrates an implementation of the functionality of Fig. 4a;
- Fig. 5a illustrates a functionality of an MDCT encoder;
- Fig. 5b illustrates a functionality of the decoder with an MDCT technology;
- 10 Fig. 5c illustrates an implementation of the frequency regenerator;
- Fig. 6 illustrates an implementation of an audio encoder;
- 15 Fig. 7a illustrates a cross-processor within the audio encoder;
- Fig. 7b illustrates an implementation of an inverse or frequency-time transform additionally providing a sampling rate reduction within the cross-processor;
- 20 Fig. 8 illustrates a preferred implementation of the controller of Fig. 6;
- Fig. 9 illustrates a further embodiment of the time domain encoder having bandwidth extension functionalities;
- 25 Fig. 10 illustrates a preferred usage of a preprocessor;
- Fig. 11a illustrates a schematic implementation of the audio decoder;
- Fig. 11b illustrates a cross-processor within the decoder for providing initialization data for the time domain decoder;
- 30 Fig. 12 illustrates a preferred implementation of the time domain decoding processor of Fig. 11a;
- 35 Fig. 13 illustrates a further implementation of the time domain bandwidth extension;

Fig. 14a illustrates a preferred implementation of an audio encoder;

Fig. 14b illustrates a preferred implementation of an audio decoder;

- 5 Fig. 14c illustrates an inventive implementation of a time domain decoder with sample rate conversion and bandwidth extension.

Fig. 6 illustrates an audio encoder for encoding an audio signal comprising a first encoding processor 600 for encoding a first audio signal portion in a frequency domain.

- 10 The first encoding processor 600 comprises a time frequency converter 602 for converting the first input audio signal portion into a frequency domain representation having spectral lines up to a maximum frequency of the input signal. Furthermore, the first encoding processor 600 comprises an analyzer 604 for analyzing the frequency domain representation up to the maximum frequency to determine first spectral regions to be
15 encoded with a first spectral representation and to determine second spectral regions to be encoded with a second spectral resolution being lower than the first spectral resolution. In particular, the full-band analyzer 604 determines which frequency lines or spectral values in the time frequency converter spectrum are to be encoded spectral-line wise and which other spectral portions are to be encoded in a parametric way and these latter
20 spectral values are then reconstructed on the decoder-side with the gap filling procedure. The actual encoding operation is performed by a spectral encoder 606 for encoding the first spectral regions or spectral portions with the first resolution and for parametrically encoding the second spectral regions or portions with the second spectral resolution.

- 25 The audio encoder of Fig. 6 additionally comprises a second encoding processor 610 for encoding the audio signal portion in a time domain. Additionally, the audio encoder comprises a controller 620 configured for analyzing the audio signal at an audio signal input 601 and for determining which portion of the audio signal is the first audio signal portion encoded in the frequency domain and which portion of the audio signal is the
30 second audio signal portion encoded in the time domain. Furthermore, an encoded signal former 630 which can be, for example, implemented as a bit stream multiplexer is provided which is configured for forming an encoded audio signal comprising a first encoded signal portion for the first audio signal portion and a second encoded signal portion for the second audio signal portion. Importantly, the encoded signal only has either
35 a frequency domain representation or a time domain representation from one and the same audio signal portion.

Hence, the controller 620 makes sure that for a single audio signal portion only a time domain representation or a frequency domain representation is in the encoded signal. This can be accomplished by the controller 620 in several ways. One way would be that, for one and the same audio signal portion, both representations arrive at block 630 and the controller 620 controls the encoded signal former 630 to only introduce one of both representations into the encoded signal. Alternatively, however, the controller 620 can control an input into the first encoding processor and an input into the second encoding processor so that, based on the analysis of the corresponding signal portion, only one of both blocks 600 or 610 is activated to actually perform the full encoding operation and the other block is deactivated.

This deactivation can be a deactivation or, as illustrated with respect to, for example, Fig. 7a, is only a kind of "initialization" mode where the other encoding processor is only active to receive and process initialization data in order to initialize internal memories but any specific encoding operation is not performed at all. This activation can be done by a certain switch at the input which is not illustrated in Fig. 6 or, preferably, by control lines 621 and 622. Hence, in this embodiment, the second encoding processor 610 does not output anything when the controller 620 has determined that the current audio signal portion should be encoded by the first encoding processor but the second encoding processor is nevertheless provided with initialization data to be active for an instant switching in the future. On the other hand, the first encoding processor is configured to not need any data from the past to update any internal memories and, therefore, when the current audio signal portion is to be encoded by the second encoding processor 610 then the controller 620 can control the first ending encoding processor 600 via control line 621 to be inactive at all. This means that the first encoding processor 600 does not need to be in an initialization state or waiting state but can be in a complete deactivation state. This is preferable particularly for mobile devices where power consumption and, therefore, battery life is an issue.

In the further specific implementation of the second encoding processor operating in the time domain, the second encoding processor comprises a downsampler 900 or sampling rate converter for converting the audio signal portion into a representation with a lower sampling rate, wherein the lower sampling rate is lower than a sampling rate at the input into the first encoding processor. This is illustrated in Fig. 9. In particular, when the input audio signal comprises a low band and a high band, it is preferred that the lower sampling

rate representation at the output of block 900 only has the low band of the input audio signal portion and this low band is then encoded by a time domain low band encoder 910 which is configured for time-domain encoding the lower sampling rate representation provided by block 900. Furthermore, a time domain bandwidth extension encoder 920 is provided for parametrically encoding the high band. To this end, the time domain bandwidth extension encoder 920 receives at least the high band of the input audio signal or the low band and the high band of the input audio signal.

In a further embodiment of the present invention the audio encoder additionally comprises, although not illustrated in Fig. 6 but illustrated in Fig. 10, a preprocessor 1000 configured for preprocessing the first audio signal portion and the second audio signal portion. Preferably, the preprocessor 100 comprises two branches, where the first branch runs at 12.8 kHz, and performs the signal analysis which is later on used in the noise estimator, VAD etc. The second branch runs at the ACELP sampling rate, i.e. depending on the configuration 12.8 or 16.0 kHz. In case the ACELP sampling rate is 12.8 kHz, most processing in this branch is in practice skipped and instead the first branch is used.

Particularly, the preprocessor comprises a transient detector 1020, and the first branch is "opened" by a resampler 1021 to e.g. 12.8 kHz, followed by a preemphasis stage 1005a, an LPC analyzer 1002a, a weighted analysis filtering stage 1022a, and an FFT/ Noise estimator/ Voice Activity Detection (VAD) or Pitch Search stage 1007.

The second branch is "opened" by a resampler 1004 to e.g. 12.8 kHz or 16 kHz, i.e., to the ACELP Sampling Rate, followed by a preemphasis stage 1005b, an LPC analyzer 1002b, a weighted analysis filtering stage 1022b, and a TCX LTP parameter extraction stage 1024. Block 1024 provides its output to the bitstream multiplexor. Block 1002 is connected to an LPC quantizer 1010 controlled by the ACELP/TCX decision, and the block 1010 is also connected to the bitstream multiplexor.

Other embodiments can alternatively comprise only a single branch or more branches. In an embodiment, this preprocessor comprises a prediction analyzer for determining prediction coefficients. This prediction analyzer can be implemented as an LPC (linear prediction coding) analyzer for determining LPC coefficients. However, other analyzers can be implemented as well. Furthermore, the preprocessor in the alternative embodiment may comprise a prediction coefficient quantizer, wherein this device receives prediction coefficient data from the prediction analyzer.

Preferably, however, the LPC quantizer is not necessarily part of the preprocessor, and it is implemented as part of the main encoding routine, i.e. not part of the preprocessor.

5 Furthermore, the preprocessor may additionally comprise an entropy coder for generating an encoded version of the quantized prediction coefficients. It is important to note that the encoded signal former 630 or the specific implementation, i.e., the bit stream multiplexer 630 makes sure that the encoded version of the quantized prediction coefficients is included into the encoded audio signal 632. Preferably, the LPC coefficients are not
10 directly quantized but are converted into an ISF representation, for example, or any other representation better suited for quantization. This conversion is preferably performed either by the determine LPC coefficients block or is performed within the block for quantizing the LPC coefficients.

15 Furthermore, the preprocessor may comprise a resampler for resampling an audio input signal at an input sampling rate into a lower sampling rate for the time domain encoder. When the time domain encoder is an ACELP encoder having a certain ACELP sampling rate then the down sampling is performed to preferably either 12.8 kHz or 16 kHz. The input sampling rate can be any of a particular number of sampling rates such as 32 kHz or
20 an even higher sampling rate. On the other hand, the sampling rate of the time domain encoder will be predetermined by certain restrictions and the resampler 1004 performs this resampling and outputs the lower sampling rate representation of the input signal. Hence, the resampler can perform a similar functionality and can even be one and the same element as the downsampler 900 illustrated in the context of Fig. 9.

25 Furthermore, it is preferred to apply a pre-emphasis in the pre-emphasis block. The pre-emphasis processing is well-known in the art of time domain encoding and is described in literature referring to the AMR-WB+ processing and the pre-emphasis is particularly configured for compensating for a spectral tilt and, therefore, allows a better calculation of
30 LPC parameters at a given LPC order.

Furthermore, the preprocessor may additionally comprise a TCX-LTP parameter extraction for controlling an LTP post filter illustrated at 1420 in Fig. 14b. Furthermore, the preprocessor may additionally comprise other functionalities illustrated at 1007 and these
35 other functionalities may comprise a pitch search functionality, a voice activity detection

(VAD) functionality or any other functionalities known in the art of time domain or speech coding.

5 As illustrated, the result of block 1024 is input into the encoded signal, i.e., is in the embodiment of Fig. 14a, input into the bit stream multiplexer 630. Furthermore, if required, data from block 1007 can also be introduced into the bit stream multiplexer or can, alternatively, be used for the purpose of time domain encoding in the time domain encoder.

10 Hence, to summarize, common to both paths is a preprocessing operation 1000 in which commonly used signal processing operations are performed. These comprise a resampling to an ACELP sampling rate (12.8 or 16 kHz) for one parallel path and this resampling is always performed. Furthermore, a TCX LTP parameter extraction illustrated at block 1006 is performed and, additionally, a pre-emphasis and a determination of LPC
15 coefficients is performed. As outlined, the pre-emphasis compensates for the spectral tilt and, therefore, makes the calculation of LPC parameters at a given LPC order more efficient.

Subsequently, reference is made to Fig. 8 in order to illustrate a preferred implementation
20 of the controller 620. The controller receives, at an input, the audio signal portion under consideration. Preferably, as illustrated in Fig. 14a, the controller receives any signal available in the preprocessor 1000 which can either be the original input signal at the input sampling rate or a resampled version at the lower time domain encoder sampling rate or a signal obtained subsequent to the pre-emphasis processing in block 1005.

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Based on this audio signal portion, the controller 620 addresses a frequency domain encoder simulator 621 and a time domain encoder simulator 622 in order to calculate for each encoder possibility an estimated signal to noise ratio. Subsequently, the selector 623 selects the encoder which has provided the better signal to noise ratio, naturally under the
30 consideration of a predefined bit rate. The selector then identifies the corresponding encoder via the control output. When it is determined that the audio signal portion under consideration is to be encoded using the frequency domain encoder, the time domain encoder is set into an initialization state or in other embodiments not requiring a very instant switching in a completely deactivated state. However, when it is determined that
35 the audio signal portion under consideration is to be encoded by the time domain encoder, the frequency domain encoder is then deactivated.

Subsequently, a preferred implementation of the controller illustrated in Fig. 8 is illustrated. The decision whether ACELP or TCX path should be chosen is performed in the switching decision by simulating the ACELP and TCX encoder and switch to the better performing branch. For this, the SNR of the ACELP and TCX branch are estimated based on an ACELP and TCX encoder/decoder simulation. The TCX encoder/decoder simulation is performed without TNS/TTS analysis, IGF encoder, quantization-loop/arithmetic coder, or without any TCX decoder. Instead, the TCX SNR is estimated using an estimation of the quantizer distortion in the shaped MDCT domain. The ACELP encoder/decoder simulation is performed using only a simulation of the adaptive codebook and innovative codebook. The ACELP SNR is simply estimated by computing the distortion introduced by a LTP filter in the weighted signal domain (adaptive codebook) and scaling this distortion by a constant factor (innovative codebook). Thus, the complexity is greatly reduced compared to an approach where TCX and ACELP encoding is executed in parallel. The branch with the higher SNR is chosen for the subsequent complete encoding run.

In case the TCX branch is chosen, a TCX decoder is run in each frame which outputs a signal at the ACELP sampling rate. This is used to update the memories used for the ACELP encoding path (LPC residual, Mem w0, Memory deemphasis), to enable instant switching from TCX to ACELP. The memory update is performed in each TCX path.

Alternatively, a full analysis by synthesis process can be performed, i.e., both encoder simulators 621, 622 implement the actual encoding operations and the results are compared by the selector 623. Alternatively, again, a complete feed forward calculation can be done by performing a signal analysis. For example, when it is determined that the signal is a speech signal by a signal classifier the time domain encoder is selected and when it is determined that the signal is a music signal then the frequency domain encoder is selected. Other procedures in order to distinguish between both encoders based on a signal analysis of the audio signal portion under consideration can also be applied.

Preferably, the audio encoder additionally comprises a cross-processor 700 illustrated in Fig. 7a. When the frequency domain encoder 600 is active, the cross-processor 700 provides initialization data to the time domain encoder 610 so that the time domain encoder is ready for a seamless switch in a future signal portion. In other words, when the current signal portion is determined to be encoded using the frequency domain encoder,

and when it is determined by the controller that the immediately following audio signal portion is to be encoded by the time domain encoder 610 then, without the cross-processor, such an immediate seamless switch would not be possible. The cross-processor, however, provides a signal derived from the frequency domain encoder 600 to the time domain encoder 610 for the purpose of initializing memories in the time domain encoder since the time domain encoder 610 has a dependency of a current frame from the input or encoded signal of an immediately in time preceding frame.

Hence, the time domain encoder 610 is configured to be initialized by the initialization data in order to encode an audio signal portion following an earlier audio signal portion encoded by the frequency domain encoder 600 in an efficient manner.

In particular, the cross-processor comprises a frequency-time converter for converting a frequency domain representation into a time domain representation which can be forwarded to the time domain encoder directly or after some further processing. This converter is illustrated in Fig. 14a as an IMDCT (inverse modified discrete cosine transform) block. This block 702, however, has a different transform size compared to the time-frequency converter block 602 indicated in Fig. 14a block (modified discrete cosine transform block). As indicated in block 602, in some embodiments, the time-frequency converter 602 operates at the input sampling rate and the inverse modified discrete cosine transform 702 operates at the lower ACELP sampling rate.

In other embodiments, such as narrow-band operating modes with 8 kHz input sampling rate, the TCX branch operates at 8 kHz, whereas ACELP still runs at 12.8 kHz. I.e. the ACELP SR is not always lower than the TCX sampling rate. For 16 kHz input sampling rate (wideband), there are also scenarios where ACELP runs at the same sampling rate as TCX, i.e. both at 16 kHz. In a super wideband mode (SWB) the input sampling rate is at 32 or 48 kHz.

The ratio of the time domain coder sampling rate or ACELP sampling rate and the frequency domain coder sampling rate or input sampling rate can be calculated and is a downsampling factor DS illustrated in Fig. 7b. The downsampling factor is greater than 1 when the output sampling rate of the downsampling operation is lower than the input sampling rate. When, however, there is an actual upsampling, then the downsampling rate is lower than 1 and an actual upsampling is performed.

For a downsampling factor greater than one, i.e., for an actual downsampling, the block 602 has a large transform size and the IMDCT block 702 has a small transform size. As illustrated in Fig. 7b, the IMDCT block 702 therefore comprises a selector 726 for selecting the lower spectral portion of an input into the IMDCT block 702. The portion of the full-band spectrum is defined by the downsampling factor DS. For example, when the lower sampling rate is 16 kHz and the input sampling rate is 32 kHz then the downsampling factor is 2.0 and, therefore, the selector 726 selects the lower half of the full-band spectrum. When the spectrum has, for example, 1024 MDCT lines then the selector selects the lower 512 MDCT lines.

This low frequency portion of the full-band spectrum is input into a small size transform and foldout block 720, as illustrated in Fig. 7b. The transform size is also selected in accordance with the downsampling factor and is 50% of the transform size in block 602. A synthesis windowing with a window with a small number of coefficients is then performed. The number of coefficients of the synthesis window is equal to the inverse of the downsampling factor multiplied by the number of coefficients of the analysis window used by block 602. Finally, an overlap add operation is performed with a smaller number of operations per block and the number of operations per block is again the number of operations per block in a full rate implementation MDCT multiplied by the inverse of the downsampling factor.

Thus, a very efficient downsampling operation can be applied since the downsampling is included in the IMDCT implementation. In this context, it is emphasized that the block 702 can be implemented by an IMDCT but can also be implemented by any other transform or filterbank implementation which can be suitably sized in the actual transform kernel and other transform related operations.

For a downsampling factor lower than one, i.e., for an actual upsampling, the notation in Fig. 7, blocks 720, 722, 724, 726 has to be reversed. Block 726 selects the full band spectrum and additionally zeroes for upper spectral lines not included in the full band spectrum. Block 720 has a transform size greater than block 710, and block 722 has a window with a number of coefficients greater than in block 712 and also block 724 has a number of operations greater than in block 714.

The block 602 has a small transform size and the IMDCT block 702 has a large transform size. As illustrated in Fig. 7b, the IMDCT block 702 therefore comprises a selector 726 for

selecting the full spectral portion of an input into the IMDCT block 702 and for the additional high band required for the output, zeroes or noise are selected and placed into the required upper band. The portion of the full-band spectrum is defined by the downsampling factor DS. For example, when the higher sampling rate is 16 kHz and the input sampling rate is 8 kHz then the downsampling factor is 0.5 and, therefore, the selector 726 selects the full-band spectrum and additionally selects preferably zeroes or small energy random noise for the upper portion not included in the full band frequency domain spectrum. When the spectrum has, for example, 1024 MDCT lines then the selector selects the 1024 MDCT lines and for the additional 1024 MDCT lines zeroes are preferably selected.

This frequency portion of the full-band spectrum is input into a then large size transform and foldout block 720, as illustrated in Fig. 7b. The transform size is also selected in accordance with the downsampling factor and is 200% of the transform size in block 602. As synthesis windowing with a window with a higher number of coefficients is then performed. The number of coefficients of the synthesis window is equal to the inverse downsampling factor divided by the number of coefficients of the analysis window used by block 602. Finally, an overlap add operation is performed with a higher number of operations per block and the number of operations per block is again the number of operations per block in a full rate implementation MDCT multiplied by the inverse of the downsampling factor.

Thus, a very efficient upsampling operation can be applied since the upsampling is included in the IMDCT implementation. In this context, it is emphasized that the block 702 can be implemented by an IMDCT but can also be implemented by any other transform or filterbank implementation which can be suitably sized in the actual transform kernel and other transform related operations.

Generally, it is outlined that a definition of a sample rate in the frequency domain needs some explanation. Spectral bands are often downsampled. Hence, the notion of an effective sampling rate or an "associated" sample or sampling rate is used. In case of a filterbank/transform the effective sample rate would be defined as $Fs_{eff} = \text{subbandsamplerate} * \text{num_subbands}$

In a further embodiment illustrated in Fig. 14a, the time-frequency converter comprises additional functionalities in addition to the analyzer. The analyzer 604 of Fig. 6 may

comprise in the embodiment of Fig. 14a a temporal noise shaping/temporal tile shaping analysis block 604a operating as discussed in the context of Fig. 2b block 222 for the TNS/TTS analysis block 604a and illustrated with respect to Fig. 2b for the tonal mask 226 which corresponds to the IGF encoder 604b in Fig. 14a.

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Furthermore, the frequency domain encoder preferably comprises a noise shaping block 606a. The noise shaping block 606a is controlled by quantized LPC coefficients as generated by block 1010. The quantized LPC coefficients used for noise shaping 606a perform a spectral shaping of the high resolution spectral values or spectral lines directly
10 encoded (rather than parametrically encoded) and the result of block 606a is similar to the spectrum of a signal subsequent to an LPC filtering stage operating in the time domain such as an LPC analysis filtering block 704 to be described later on. Furthermore, the result of the noise shaping block 606a is then quantized and entropy coded as indicated by block 606b. The result of block 606b corresponds to the encoded first audio signal
15 portion or a frequency domain coded audio signal portion (together with other side information).

The cross-processor 700 comprises a spectral decoder for calculating a decoded version of the first encoded signal portion. In the embodiment of Fig. 14a, the spectral decoder
20 701 comprises an inverse noise shaping block 703, an optional gap filling decoder 704, a TNS/TTS synthesis block 705 and the IMDCT block 702 discussed before. These blocks undo the specific operations performed by blocks 602 to 606b. In particular, a noise shaping block 703 undoes the noise shaping performed by block 606a based on the quantized LPC coefficients 1010. The IGF decoder 704 operates as discussed with
25 respect to Fig. 2A, blocks 202 and 206 and the TNS/TTS synthesis block 705 operates as discussed in the context of block 210 of Fig. 2A and the spectral decoder additionally comprises the IMDCT block 702. Furthermore, the cross processor 700 in Fig. 14a additionally or alternatively comprises a delay stage 707 for feeding a delayed version of the decoded version obtained by the spectral decoder 701 in a de-emphasis stage 617 of
30 the second encoding processor for the purpose of initializing the de-emphasis stage 617.

Furthermore, the cross-processor 700 may comprise in addition or alternatively a weighted prediction coefficient analysis filtering stage 708 for filtering the decoded version and for feeding a filtered decoded version to a codebook determinator 613 indicated as
35 "MMSE" in Fig. 14a of the second encoding processor for initializing this block. Additionally or alternatively, the cross-processor comprises the LPC analysis filtering

stage for filtering the decoded version of the first encoded signal portion output by the spectral decoder 700 to an adaptive codebook stage 612 for initialization of the block 612. In addition, or alternatively, the cross-processor also comprises a pre-emphasis stage 709 for performing a pre-emphasis processing to the decoded version output by a spectral decoder 701 before the LPC filtering. The pre-emphasis stage output can also be fed to a further delay stage 710 for the purpose of initializing an LPC synthesis filtering block 616 within the time domain encoder 610.

The time domain encoder processor 610 comprises, as illustrated in Fig. 14a, a pre-emphasis operating on the lower ACELP sampling rate. As illustrated, this pre-emphasis is the pre-emphasis performed in the preprocessing stage 1000 and has reference number 1005. The pre-emphasis data is input into an LPC analysis filtering stage 611 operating in the time domain and this filter is controlled by the quantized LPC coefficients 1010 obtained by the preprocessing stage 1000. As known from AMR-WB+ or USAC or other CELP encoders, the residual signal generated by block 611 is provided to an adaptive codebook 612 and, furthermore, the adaptive codebook 612 is connected to an innovative codebook stage 614 and the codebook data from the adaptive codebook 612 and from the innovative codebook are input into the bitstream multiplexer as illustrated.

Furthermore, an ACELP gains/coding stage 615 is provided in series to the innovative codebook stage 614 and the result of this block is input into a codebook determinator 613 indicated as MMSE in Fig. 14a. This block cooperates with the innovative codebook block 614. Furthermore, the time domain encoder additionally comprises a decoder portion having an LPC synthesis filtering block 616, a de-emphasis block 617 and an adaptive bass post filter stage 618 for calculating parameters for an adaptive bass post filter which is, however, applied at the decoder-side. Without any adaptive bass post filtering on the decoder side, blocks 616, 617, 618 would not be necessary for the time domain encoder 610.

As illustrated, several blocks of the time domain decoder depend on previous signals and these blocks are the adaptive codebook block 612, the codebook determinator 613, the LPC synthesis filtering block 616 and the de-emphasis block 617. These blocks are provided with data from the cross-processor derived from the frequency domain encoding processor data in order to initialize these blocks for the purpose of being ready for an instant switch from the frequency domain encoder to the time domain encoder. As can also be seen from Fig. 14a, any dependence on earlier data is not necessary for the

frequency domain encoder. Therefore, the cross-processor 700 does not provide any memory initialization data from the time domain encoder to the frequency domain encoder. However, for other implementations of the frequency domain encoder, where dependencies from the past exist and where memory initialization data is required, the cross-processor 700 is configured to operate in both directions.

The preferred audio decoder in Fig. 14b is described in the following: The waveform decoder part consists of a full-band TCX decoder path with IGF both operating at the input sampling rate of the codec. In parallel, an alternative ACELP decoder path at lower sampling rate exists that is reinforced further downstream by a TD-BWE.

For ACELP initialization when switching from TCX to ACELP, a cross path (consisting of a shared TCX decoder frontend but additionally providing output at the lower sampling rate and some post-processing) exists that performs the inventive ACELP initialization. Sharing the same sampling rate and filter order between TCX and ACELP in the LPCs allows for an easier and more efficient ACELP initialization.

For visualizing the switching, two switches are sketched in 14b. While the second switch 1160 downstream chooses between TCX/IGF or ACELP/TD-BWE output, the first switch 1480 either pre-updates the buffers in the resampling QMF stage downstream the ACELP path by the output of the cross path or simply passes on the ACELP output.

Subsequently, audio decoder implementations in accordance with aspects of the present invention are discussed in the context of Figs. 11a-14c.

An audio decoder for decoding an encoded audio signal 1101 comprises a first decoding processor 1120 for decoding a first encoded audio signal portion in a frequency domain. The first decoding processor 1120 comprises a spectral decoder 1122 for decoding first spectral regions with a high spectral resolution and for synthesizing second spectral regions using a parametric representation of the second spectral regions and at least a decoded first spectral region to obtain a decoded spectral representation. The decoded spectral representation is a full-band decoded spectral representation as discussed in the context of Fig. 6 and as also discussed in the context of Fig. 1a. Generally, the first decoding processor, therefore, comprises a full-band implementation with a gap filling procedure in the frequency domain. The first decoding processor 1120 furthermore

comprises a frequency-time converter 1124 for converting the decoded spectral representation into a time domain to obtain a decoded first audio signal portion.

Furthermore, the audio decoder comprises a second decoding processor 1140 for
5 decoding the second encoded audio signal portion in the time domain to obtain a decoded second signal portion. Furthermore, the audio decoder comprises a combiner 1160 for combining the decoded first signal portion and the decoded second signal portion to obtain a decoded audio signal. The decoded signal portions are combined in sequence which is also illustrated in Fig. 14b by a switch implementation 1160 representing an
10 embodiment of the combiner 1160 of Fig. 11a.

Preferably, the second decoding processor 1140 contains a time domain bandwidth extension processor 1220 and comprises, as illustrated in Fig. 12, a time domain low band decoder 1200 for decoding a low band time domain signal. This implementation
15 furthermore comprises an upsampler 1210 for upsampling the low band time domain signal. Additionally, a time domain bandwidth extension decoder 1220 is provided for synthesizing a high band of the output audio signal. Furthermore, a mixer 1230 is provided for mixing a synthesized high band of the time domain output signal and an upsampled low band time domain signal to obtain the time domain encoder output.
20 Hence, block 1140 in Fig. 11a can be implemented by the functionality of Fig. 12 in a preferred embodiment.

Fig. 13 illustrates a preferred embodiment of the time domain bandwidth extension decoder 1220 of Fig. 12. Preferably, a time domain upsampler 1221 is provided which
25 receives, as an input, an LPC residual signal from a time domain low band decoder included within block 1140 and illustrated at 1200 in Fig. 12 and further illustrated in the context of Fig. 14b. The time domain upsampler 1221 generates an upsampled version of the LPC residual signal. This version is then input into a non-linear distortion block 1222 which generates, based on its input signal, an output signal having higher frequency
30 values. A non-linear distortion can be a copy-up, a mirroring, a frequency shift or a non-linear computing operation or device such as a diode or a transistor operated in the non-linear region. The output signal of block 1222 is input into an LPC synthesis filtering block 1223 which is controlled by LPC data used for the low band decoder as well or by specific envelope data generated by the time domain bandwidth extension block 920 on the
35 encoder-side of Fig 14a, for example. The output of the LPC synthesis block is then input

into a bandpass or highpass filter 1224 to finally obtain the high band, which is then input into the mixer 1230 as illustrated in Fig. 12.

Subsequently, a preferred implementation of the upsampler 1210 of Fig. 12 is discussed in the context of Fig. 14b. The upsampler preferably comprises an analysis filterbank operating at a first time domain low band decoder sampling rate. A specific implementation of such an analysis filterbank is a QMF analysis filterbank 1471 illustrated in Fig. 14b. Furthermore, the upsampler comprises a synthesis filterbank 1473 operating at a second output sampling rate being higher than the first time domain low band sampling rate. Hence, the QMF synthesis filterbank 1473 which is a preferred implementation of the general filterbank operates at the output sampling rate. When the downsampling factor DS as discussed in the context of Fig. 7b is 0.5, then the QMF analysis filterbank 1471 has, e.g. only 32 filterbank channels and the QMF synthesis filterbank 1473 has e.g. 64 QMF channels, but the higher half of the filterbank channels, i.e., the upper 32 filterbank channels are fed with zeroes or noise, while the lower 32 filterbank channels are fed with the corresponding signals provided by the QMF analysis filterbank 1471. Preferably, however, a bandpass filtering 1472 is performed within the QMF filterbank domain in order to make sure that the QMF synthesis output 1473 is an upsampled version of the ACELP decoder output, but without any artifacts above the maximum frequency of the ACELP decoder.

Further processing operations can be performed within the QMF domain in addition or instead of the bandpass filtering 1472. If no processing is performed at all, then the QMF analysis and the QMF synthesis constitute an efficient upsampler 1210.

Subsequently, the construction of the individual elements in Fig. 14b are discussed in more detail.

The full-band frequency domain decoder 1120 comprises a first decoding block 1122a for decoding the high resolution spectral coefficients and for additionally performing noise filling in the low band portion as known, for example, from the USAC technology. Furthermore, the full-band decoder comprises an IGF processor 1122b for filling the spectral holes using synthesized spectral values which have been encoded only parametrically and, therefore, encoded with a low resolution on the encoder-side. Then, in block 1122c, an inverse noise shaping is performed and the result is input into a TNS/TTS synthesis block 705 which provides, as a final output, an input to a frequency-time

converter 1124, which is preferably implemented as an inverse modified discrete cosine transform operating at the output, i.e., high sampling rate.

Furthermore, a harmonic or LTP post-filter is used which is controlled by data obtained by the TCX LTP parameter extraction block 1006 in Fig. 14a. The result is then the decoded first audio signal portion at the output sampling rate and as can be seen from Fig. 14b, this data has the high sampling rate and, therefore, any further frequency enhancement is not necessary at all due to the fact that the decoding processor is a frequency domain full-band decoder preferably operating using the intelligent gap filling technology discussed in the context of Figs. 1a-5C.

Several elements in Fig. 14b are quite similar to the corresponding blocks in the cross-processor 700 of Fig. 14a, particularly with respect to the IGF decoder 704 corresponding to IGF processing 1122b and the inverse noise shaping operation controlled by quantized LPC coefficients 1145 corresponds to the inverse noise shaping 703 of Fig. 14a and the TNS/TTS synthesis block 705 in Fig. 14b corresponds to the block TNS/TTS synthesis 705 in Fig. 14a. Importantly, however, the IMDCT block 1124 in Fig. 14b operates at the high sampling rate while the IMDCT block 702 in Fig. 14a operates at a low sampling rate. Hence, the block 1124 in Fig. 14b comprises the large sized transform and fold-out block 710, the synthesis window in block 712 and the overlap-add stage 714 with the corresponding large number of operations, large number of window coefficients and a large transform size compared to the corresponding features 720, 722, 724 in Fig. 7b, which are operated in block 701, and as will be outlined later on, in block 1171 of the cross-processor 1170 in Fig. 14b as well.

The time domain decoding processor 1140 preferably comprises the ACELP or time domain low band decoder 1200 comprising an ACELP decoder stage 1149 for obtaining decoded gains and the innovative codebook information. Additionally, an ACELP adaptive codebook stage 1141 is provided and a subsequent ACELP post-processing stage 1142 and a final synthesis filter such as LPC synthesis filter 1143, which is again controlled by the quantized LPC coefficients 1145 obtained from the bitstream demultiplexer 1100 corresponding to the encoded signal parser 1100 in Fig. 11a. The output of the LPC synthesis filter 1143 is input into a de-emphasis stage 1144 for canceling or undoing the processing introduced by the pre-emphasis stage 1005 of the pre-processor 1000 of Fig. 14a. The result is the time domain output signal at a low sampling rate and a low band and in case the frequency domain output is required, the switch 1480 is in the indicated

position and the output of the de-emphasis stage 1144 is introduced into the upsampler 1210 and then mixed with the high bands from the time domain bandwidth extension decoder 1220.

- 5 In accordance with embodiments of the present invention, the audio decoder additionally comprises the cross-processor 1170 illustrated in Fig. 11b and in Fig. 14b for calculating, from the decoded spectral representation of the first encoded audio signal portion, initialization data of the second decoding processor so that the second decoding processor is initialized to decode the encoded second audio signal portion following in
10 time the first audio signal portion in the encoded audio signal, i.e., such that the time domain decoding processor 1140 is ready for an instant switch from one audio signal portion to the next without any loss in quality or efficiency.

Preferably, the cross-processor 1170 comprises an additional frequency-time converter
15 1171 operating at a lower sampling rate than the frequency-time converter of the first decoding processor in order to obtain a further decoded first signal portion in the time domain to be used as the initialization signal or for which any initialization data can be derived. Preferably, this IMDCT or low sampling rate frequency-time converter is implemented as illustrated in Fig. 7b, item 726 (selector), item 720 (small-size transform
20 and fold-out), synthesis windowing with a smaller number of window coefficients as indicated in 722 and an overlap-add stage with a smaller number of operations as indicated at 724. Hence, the IMDCT block 1124 in the frequency domain full-band decoder is implemented as indicated by block 710, 712, 714, and the IMDCT block 1171 is implemented as indicated in Fig. 7b by block 726, 720, 722, 724. Again, the
25 downsampling factor is the ratio between the time domain coder sampling rate or the low sampling rate and the higher frequency domain coder sampling rate or output sampling rate and this downsampling factor can be any number greater than 0 and lower than 1.

As illustrated in Fig. 14b, the cross-processor 1170 further comprises, alone or in addition
30 to other elements, a delay stage 1172 for delaying the further decoded first signal portion and for feeding the delayed decoded first signal portion into a de-emphasis stage 1144 of the second decoding processor for initialization. Furthermore, the cross-processor comprises, in addition or alternatively, a pre-emphasis filter 1173 and a delay stage 1175 for filtering and delaying a further decoded first signal portion and for providing the
35 delayed output of block 1175 into an LPC synthesis filtering stage 1143 of the ACELP decoder for the purpose of initialization.

Furthermore, the cross-processor may comprise alternatively or in addition to the other mentioned elements an LPC analysis filter 1174 for generating a prediction residual signal from the further decoded first signal portion or a pre-emphasized further decoded first signal portion and for feeding the data into a codebook synthesizer of the second decoding processor and preferably, into the adaptive codebook stage 1141. Furthermore, the output of the frequency-time converter 1171 with the low sampling rate is also input into the QMF analysis stage 1471 of the upsampler 1210 for the purpose of initialization, i.e., when the currently decoded audio signal portion is delivered by the frequency domain full-band decoder 1120.

The preferred audio decoder is described in the following: The waveform decoder part consists of a full-band TCX decoder path with IGF both operating at the input sampling rate of the codec. In parallel, an alternative ACELP decoder path at lower sampling rate exists that is reinforced further downstream by a TD-BWE.

For ACELP initialization when switching from TCX to ACELP, a cross path (consisting of a shared TCX decoder frontend but additionally providing output at the lower sampling rate and some post-processing) exists that performs the inventive ACELP initialization. Sharing the same sampling rate and filter order between TCX and ACELP in the LPCs allows for an easier and more efficient ACELP initialization.

For visualizing the switching, two switches are sketched in Fig. 14b. While the second switch 1160 downstream chooses between TCX/IGF or ACELP/TD-BWE output, the first switch 1480 either pre-updates the buffers in the resampling QMF stage downstream the ACELP path by the output of the cross path or simply passes on the ACELP output.

To summarize, preferred aspects of the invention which can be used alone or in combination relate to a combination of an ACELP and TD-BWE coder with a full-band capable TCX/IGF technology preferably associated with using a cross signal.

A further specific feature is a cross signal path for the ACELP initialization to enable seamless switching.

A further aspect is that a short IMDCT is fed with a lower part of high-rate long MDCT coefficients to efficiently implement a sample rate conversion in the cross-path.

A further feature is an efficient realization of the cross-path partly shared with a full-band TCX/IGF in the decoder.

- 5 A further feature is the cross signal path for the QMF initialization to enable seamless switching from TCX to ACELP.

An additional feature is a cross-signal path to the QMF allowing compensating the delay gap between ACELP resampled output and a filterbank-TCX/IGF output when switching
10 from ACELP to TCX.

A further aspect is that an LPC is provided for both the TCX and the ACELP coder at the same sampling rate and filter order, although the TCX/IGF encoder/decoder is full-band capable.

15

Subsequently, Fig. 14c is discussed as a preferred implementation of a time domain decoder operating either as a stand-alone decoder or in the combination with the full-band capable frequency domain decoder.

- 20 Generally, the time domain decoder comprises an ACELP decoder, a subsequently connected resampler or upsampler and a time domain bandwidth extension functionality. Particularly, the ACELP decoder comprises an ACELP decoding stage for restoring gains and the innovative codebook 1149, an ACELP-adaptive codebook stage 1141, an ACELP post-processor 1142, an LPC synthesis filter 1143 controlled by quantized LPC
25 coefficients from a bitstream demultiplexer or encoded signal parser and the subsequently connected de-emphasis stage 1144. Preferably, the decoded time domain signal being at an ACELP sampling rate is input, alongside with control data from the bitstream, into a time domain bandwidth extension decoder 1220, which provides a high band at the outputs.

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- In order to upsample the de-emphasis 1144 output, an upsampler comprising the QMF analysis block 1471, and the QMF synthesis block 1473 are provided. Within the filterbank domain defined by blocks 1471 and 1473, a bandpass filter is preferably applied. Particularly, as has been discussed before, the same functionalities can also be used
35 which have been discussed with respect to the same reference numbers. Furthermore, the time domain bandwidth extension decoder 1220 can be implemented as illustrated in

Fig. 13 and, generally, comprises an upsampling of the ACELP residual signal or time domain residual signal at the ACELP sampling rate finally to an output sampling rate of the bandwidth extended signal.

- 5 Subsequently, further details with respect to the frequency domain encoder and decoder being full-band capable are discussed with respect to Figs. 1A-5C.

Fig. 1a illustrates an apparatus for encoding an audio signal 99. The audio signal 99 is input into a time spectrum converter 100 for converting an audio signal having a sampling rate into a spectral representation 101 output by the time spectrum converter. The spectrum 101 is input into a spectral analyzer 102 for analyzing the spectral representation 101. The spectral analyzer 101 is configured for determining a first set of first spectral portions 103 to be encoded with a first spectral resolution and a different second set of second spectral portions 105 to be encoded with a second spectral resolution. The second spectral resolution is smaller than the first spectral resolution. The second set of second spectral portions 105 is input into a parameter calculator or parametric coder 104 for calculating spectral envelope information having the second spectral resolution. Furthermore, a spectral domain audio coder 106 is provided for generating a first encoded representation 107 of the first set of first spectral portions having the first spectral resolution. Furthermore, the parameter calculator/parametric coder 104 is configured for generating a second encoded representation 109 of the second set of second spectral portions. The first encoded representation 107 and the second encoded representation 109 are input into a bit stream multiplexer or bit stream former 108 and block 108 finally outputs the encoded audio signal for transmission or storage on a storage device.

Typically, a first spectral portion such as 306 of Fig. 3a will be surrounded by two second spectral portions such as 307a, 307b. This is not the case in e.g. HE-AAC, where the core coder frequency range is band limited.

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Fig. 1b illustrates a decoder matching with the encoder of Fig. 1a. The first encoded representation 107 is input into a spectral domain audio decoder 112 for generating a first decoded representation of a first set of first spectral portions, the decoded representation having a first spectral resolution. Furthermore, the second encoded representation 109 is input into a parametric decoder 114 for generating a second decoded representation of a

35

second set of second spectral portions having a second spectral resolution being lower than the first spectral resolution.

5 The decoder further comprises a frequency regenerator 116 for regenerating a reconstructed second spectral portion having the first spectral resolution using a first spectral portion. The frequency regenerator 116 performs a tile filling operation, i.e., uses a tile or portion of the first set of first spectral portions and copies this first set of first spectral portions into the reconstruction range or reconstruction band having the second spectral portion and typically performs spectral envelope shaping or another operation as
10 indicated by the decoded second representation output by the parametric decoder 114, i.e., by using the information on the second set of second spectral portions. The decoded first set of first spectral portions and the reconstructed second set of spectral portions as indicated at the output of the frequency regenerator 116 on line 117 is input into a spectrum-time converter 118 configured for converting the first decoded representation
15 and the reconstructed second spectral portion into a time representation 119, the time representation having a certain high sampling rate.

Fig. 2b illustrates an implementation of the Fig. 1a encoder. An audio input signal 99 is input into an analysis filterbank 220 corresponding to the time spectrum converter 100 of
20 Fig. 1a. Then, a temporal noise shaping operation is performed in TNS block 222. Therefore, the input into the spectral analyzer 102 of Fig. 1a corresponding to a block tonal mask 226 of Fig. 2b can either be full spectral values, when the temporal noise shaping/ temporal tile shaping operation is not applied or can be spectral residual values, when the TNS operation as illustrated in Fig. 2b, block 222 is applied. For two-channel
25 signals or multi-channel signals, a joint channel coding 228 can additionally be performed, so that the spectral domain encoder 106 of Fig. 1a may comprise the joint channel coding block 228. Furthermore, an entropy coder 232 for performing a lossless data compression is provided which is also a portion of the spectral domain encoder 106 of Fig. 1a.

30 The spectral analyzer/tonal mask 226 separates the output of TNS block 222 into the core band and the tonal components corresponding to the first set of first spectral portions 103 and the residual components corresponding to the second set of second spectral portions 105 of Fig. 1a. The block 224 indicated as IGF parameter extraction encoding corresponds to the parametric coder 104 of Fig. 1a and the bitstream multiplexer 230
35 corresponds to the bitstream multiplexer 108 of Fig. 1a.

Preferably, the analysis filterbank 222 is implemented as an MDCT (modified discrete cosine transform filterbank) and the MDCT is used to transform the signal 99 into a time-frequency domain with the modified discrete cosine transform acting as the frequency analysis tool.

5

The spectral analyzer 226 preferably applies a tonality mask. This tonality mask estimation stage is used to separate tonal components from the noise-like components in the signal. This allows the core coder 228 to code all tonal components with a psycho-acoustic module.

10

This method has certain advantages over the classical SBR [1] in that the harmonic grid of a multi-tone signal is preserved by the core coder while only the gaps between the sinusoids is filled with the best matching "shaped noise" from the source region.

15

In case of stereo channel pairs an additional joint stereo processing is applied. This is necessary, because for a certain destination range the signal can a highly correlated panned sound source. In case the source regions chosen for this particular region are not well correlated, although the energies are matched for the destination regions, the spatial image can suffer due to the uncorrelated source regions. The encoder analyses each destination region energy band, typically performing a cross-correlation of the spectral values and if a certain threshold is exceeded, sets a joint flag for this energy band. In the decoder the left and right channel energy bands are treated individually if this joint stereo flag is not set. In case the joint stereo flag is set, both the energies and the patching are performed in the joint stereo domain. The joint stereo information for the IGF regions is signaled similar the joint stereo information for the core coding, including a flag indicating in case of prediction if the direction of the prediction is from downmix to residual or vice versa.

20

25

The energies can be calculated from the transmitted energies in the L/R-domain.

30

$$\begin{aligned} midNrg[k] &= leftNrg[k] + rightNrg[k]; \\ sideNrg[k] &= leftNrg[k] - rightNrg[k]; \end{aligned}$$

with k being the frequency index in the transform domain.

35

Another solution is to calculate and transmit the energies directly in the joint stereo domain for bands where joint stereo is active, so no additional energy transformation is needed at the decoder side.

- 5 The source tiles are always created according to the Mid/Side-Matrix:

$$midTile[k] = 0.5 \cdot (leftTile[k] + rightTile[k])$$

10 $sideTile[k] = 0.5 \cdot (leftTile[k] - rightTile[k])$

Energy adjustment:

15 $midTile[k] = midTile[k] * midNrg[k];$

$$sideTile[k] = sideTile[k] * sideNrg[k];$$

Joint stereo -> LR transformation:

20

If no additional prediction parameter is coded:

$$leftTile[k] = midTile[k] + sideTile[k]$$

25 $rightTile[k] = midTile[k] - sideTile[k]$

If an additional prediction parameter is coded and if the signalled direction is from mid to side:

$$sideTile[k] = sideTile[k] - predictionCoeff \cdot midTile[k]$$

30 $leftTile[k] = midTile[k] + sideTile[k]$

$$rightTile[k] = midTile[k] - sideTile[k]$$

If the signalled direction is from side to mid:

$$\begin{aligned}
midTile1[k] &= midTile[k] - predictionCoeff \cdot sideTile[k] \\
leftTile[k] &= midTile1[k] - sideTile[k] \\
rightTile[k] &= midTile1[k] + sideTile[k]
\end{aligned}$$

5 This processing ensures that from the tiles used for regenerating highly correlated destination regions and panned destination regions, the resulting left and right channels still represent a correlated and panned sound source even if the source regions are not correlated, preserving the stereo image for such regions.

10 In other words, in the bitstream, joint stereo flags are transmitted that indicate whether L/R or M/S as an example for the general joint stereo coding shall be used. In the decoder, first, the core signal is decoded as indicated by the joint stereo flags for the core bands. Second, the core signal is stored in both L/R and M/S representation. For the IGF tile filling, the source tile representation is chosen to fit the target tile representation as indicated by the joint stereo information for the IGF bands.

15 Temporal Noise Shaping (TNS) is a standard technique and part of AAC. TNS can be considered as an extension of the basic scheme of a perceptual coder, inserting an optional processing step between the filterbank and the quantization stage. The main task of the TNS module is to hide the produced quantization noise in the temporal masking region of transient like signals and thus it leads to a more efficient coding scheme. First, TNS calculates a set of prediction coefficients using "forward prediction" in the transform domain, e.g. MDCT. These coefficients are then used for flattening the temporal envelope of the signal. As the quantization affects the TNS filtered spectrum, also the quantization noise is temporarily flat. By applying the invers TNS filtering on decoder side, the
20 quantization noise is shaped according to the temporal envelope of the TNS filter and therefore the quantization noise gets masked by the transient.

IGF is based on an MDCT representation. For efficient coding, preferably long blocks of approx. 20 ms have to be used. If the signal within such a long block contains transients,
30 audible pre- and post-echoes occur in the IGF spectral bands due to the tile filling.

This pre-echo effect is reduced by using TNS in the IGF context. Here, TNS is used as a temporal tile shaping (TTS) tool as the spectral regeneration in the decoder is performed on the TNS residual signal. The required TTS prediction coefficients are calculated and
35 applied using the full spectrum on encoder side as usual. The TNS/TTS start and stop frequencies are not affected by the IGF start frequency $f_{IGFstart}$ of the IGF tool. In

comparison to the legacy TNS, the TTS stop frequency is increased to the stop frequency of the IGF tool, which is higher than $f_{IGFstart}$. On decoder side the TNS/TTS coefficients are applied on the full spectrum again, i.e. the core spectrum plus the regenerated spectrum plus the tonal components from the tonality mask (see Fig. 7e). The application of TTS is necessary to form the temporal envelope of the regenerated spectrum to match the envelope of the original signal again.

In legacy decoders, spectral patching on an audio signal corrupts spectral correlation at the patch borders and thereby impairs the temporal envelope of the audio signal by introducing dispersion. Hence, another benefit of performing the IGF tile filling on the residual signal is that, after application of the shaping filter, tile borders are seamlessly correlated, resulting in a more faithful temporal reproduction of the signal.

In an IGF encoder, the spectrum having undergone TNS/TTS filtering, tonality mask processing and IGF parameter estimation is devoid of any signal above the IGF start frequency except for tonal components. This sparse spectrum is now coded by the core coder using principles of arithmetic coding and predictive coding. These coded components along with the signaling bits form the bitstream of the audio.

Fig. 2a illustrates the corresponding decoder implementation. The bitstream in Fig. 2a corresponding to the encoded audio signal is input into the demultiplexer/decoder which would be connected, with respect to Fig. 1b, to the blocks 112 and 114. The bitstream demultiplexer separates the input audio signal into the first encoded representation 107 of Fig. 1b and the second encoded representation 109 of Fig. 1b. The first encoded representation having the first set of first spectral portions is input into the joint channel decoding block 204 corresponding to the spectral domain decoder 112 of Fig. 1b. The second encoded representation is input into the parametric decoder 114 not illustrated in Fig. 2a and then input into the IGF block 202 corresponding to the frequency regenerator 116 of Fig. 1b. The first set of first spectral portions required for frequency regeneration are input into IGF block 202 via line 203. Furthermore, subsequent to joint channel decoding 204 the specific core decoding is applied in the tonal mask block 206 so that the output of tonal mask 206 corresponds to the output of the spectral domain decoder 112. Then, a combination by combiner 208 is performed, i.e., a frame building where the output of combiner 208 now has the full range spectrum, but still in the TNS/TTS filtered domain. Then, in block 210, an inverse TNS/TTS operation is performed using TNS/TTS filter information provided via line 109, i.e., the TTS side information is preferably included in the first encoded representation generated by the spectral domain encoder 106 which

can, for example, be a straightforward AAC or USAC core encoder, or can also be included in the second encoded representation. At the output of block 210, a complete spectrum until the maximum frequency is provided which is the full range frequency defined by the sampling rate of the original input signal. Then, a spectrum/time conversion is performed in the synthesis filterbank 212 to finally obtain the audio output signal.

Fig. 3a illustrates a schematic representation of the spectrum. The spectrum is subdivided in scale factor bands SCB where there are seven scale factor bands SCB1 to SCB7 in the illustrated example of Fig. 3a. The scale factor bands can be AAC scale factor bands which are defined in the AAC standard and have an increasing bandwidth to upper frequencies as illustrated in Fig. 3a schematically. It is preferred to perform intelligent gap filling not from the very beginning of the spectrum, i.e., at low frequencies, but to start the IGF operation at an IGF start frequency illustrated at 309. Therefore, the core frequency band extends from the lowest frequency to the IGF start frequency. Above the IGF start frequency, the spectrum analysis is applied to separate high resolution spectral components 304, 305, 306, 307 (the first set of first spectral portions) from low resolution components represented by the second set of second spectral portions. Fig. 3a illustrates a spectrum which is exemplarily input into the spectral domain encoder 106 or the joint channel coder 228, i.e., the core encoder operates in the full range, but encodes a significant amount of zero spectral values, i.e., these zero spectral values are quantized to zero or are set to zero before quantizing or subsequent to quantizing. Anyway, the core encoder operates in full range, i.e., as if the spectrum would be as illustrated, i.e., the core decoder does not necessarily have to be aware of any intelligent gap filling or encoding of the second set of second spectral portions with a lower spectral resolution.

Preferably, the high resolution is defined by a line-wise coding of spectral lines such as MDCT lines, while the second resolution or low resolution is defined by, for example, calculating only a single spectral value per scale factor band, where a scale factor band covers several frequency lines. Thus, the second low resolution is, with respect to its spectral resolution, much lower than the first or high resolution defined by the line-wise coding typically applied by the core encoder such as an AAC or USAC core encoder.

Regarding scale factor or energy calculation, the situation is illustrated in Fig. 3b. Due to the fact that the encoder is a core encoder and due to the fact that there can, but does not necessarily have to be, components of the first set of spectral portions in each band, the core encoder calculates a scale factor for each band not only in the core range below the

IGF start frequency 309, but also above the IGF start frequency until the maximum frequency $f_{IGFstop}$ which is smaller or equal to the half of the sampling frequency, i.e., $f_{s/2}$. Thus, the encoded tonal portions 302, 304, 305, 306, 307 of Fig. 3a and, in this embodiment together with the scale factors SCB1 to SCB7 correspond to the high resolution spectral data. The low resolution spectral data are calculated starting from the IGF start frequency and correspond to the energy information values E_1 , E_2 , E_3 , E_4 , which are transmitted together with the scale factors SF4 to SF7.

Particularly, when the core encoder is under a low bitrate condition, an additional noise-filling operation in the core band, i.e., lower in frequency than the IGF start frequency, i.e., in scale factor bands SCB1 to SCB3 can be applied in addition. In noise-filling, there exist several adjacent spectral lines which have been quantized to zero. On the decoder-side, these quantized to zero spectral values are re-synthesized and the re-synthesized spectral values are adjusted in their magnitude using a noise-filling energy such as NF_2 illustrated at 308 in Fig. 3b. The noise-filling energy, which can be given in absolute terms or in relative terms particularly with respect to the scale factor as in USAC corresponds to the energy of the set of spectral values quantized to zero. These noise-filling spectral lines can also be considered to be a third set of third spectral portions which are regenerated by straightforward noise-filling synthesis without any IGF operation relying on frequency regeneration using frequency tiles from other frequencies for reconstructing frequency tiles using spectral values from a source range and the energy information E_1 , E_2 , E_3 , E_4 .

Preferably, the bands, for which energy information is calculated coincide with the scale factor bands. In other embodiments, an energy information value grouping is applied so that, for example, for scale factor bands 4 and 5, only a single energy information value is transmitted, but even in this embodiment, the borders of the grouped reconstruction bands coincide with borders of the scale factor bands. If different band separations are applied, then certain re-calculations or synchronization calculations may be applied, and this can make sense depending on the certain implementation.

Preferably, the spectral domain encoder 106 of Fig. 1a is a psycho-acoustically driven encoder as illustrated in Fig. 4a. Typically, as for example illustrated in the MPEG2/4 AAC standard or MPEG1/2, Layer 3 standard, the to be encoded audio signal after having been transformed into the spectral range (401 in Fig. 4a) is forwarded to a scale factor calculator 400. The scale factor calculator is controlled by a psycho-acoustic model additionally receiving the to be quantized audio signal or receiving, as in the MPEG1/2

Layer 3 or MPEG AAC standard, a complex spectral representation of the audio signal. The psycho-acoustic model calculates, for each scale factor band, a scale factor representing the psycho-acoustic threshold. Additionally, the scale factors are then, by cooperation of the well-known inner and outer iteration loops or by any other suitable
5 encoding procedure adjusted so that certain bitrate conditions are fulfilled. Then, the to be quantized spectral values on the one hand and the calculated scale factors on the other hand are input into a quantizer processor 404. In the straightforward audio encoder operation, the to be quantized spectral values are weighted by the scale factors and, the weighted spectral values are then input into a fixed quantizer typically having a
10 compression functionality to upper amplitude ranges. Then, at the output of the quantizer processor there do exist quantization indices which are then forwarded into an entropy encoder typically having specific and very efficient coding for a set of zero-quantization indices for adjacent frequency values or, as also called in the art, a "run" of zero values.

15 In the audio encoder of Fig. 1a, however, the quantizer processor typically receives information on the second spectral portions from the spectral analyzer. Thus, the quantizer processor 404 makes sure that, in the output of the quantizer processor 404, the second spectral portions as identified by the spectral analyzer 102 are zero or have a representation acknowledged by an encoder or a decoder as a zero representation which
20 can be very efficiently coded, specifically when there exist "runs" of zero values in the spectrum.

Fig. 4b illustrates an implementation of the quantizer processor. The MDCT spectral values can be input into a set to zero block 410. Then, the second spectral portions are
25 already set to zero before a weighting by the scale factors in block 412 is performed. In an additional implementation, block 410 is not provided, but the set to zero cooperation is performed in block 418 subsequent to the weighting block 412. In an even further implementation, the set to zero operation can also be performed in a set to zero block 422 subsequent to a quantization in the quantizer block 420. In this implementation, blocks
30 410 and 418 would not be present. Generally, at least one of the blocks 410, 418, 422 are provided depending on the specific implementation.

Then, at the output of block 422, a quantized spectrum is obtained corresponding to what is illustrated in Fig. 3a. This quantized spectrum is then input into an entropy coder such
35 as 232 in Fig. 2b which can be a Huffman coder or an arithmetic coder as, for example, defined in the USAC standard.

The set to zero blocks 410, 418, 422, which are provided alternatively to each other or in parallel are controlled by the spectral analyzer 424. The spectral analyzer preferably comprises any implementation of a well-known tonality detector or comprises any different
5 kind of detector operative for separating a spectrum into components to be encoded with a high resolution and components to be encoded with a low resolution. Other such algorithms implemented in the spectral analyzer can be a voice activity detector, a noise detector, a speech detector or any other detector deciding, depending on spectral information or associated metadata on the resolution requirements for different spectral
10 portions.

Fig. 5a illustrates a preferred implementation of the time spectrum converter 100 of Fig. 1a as, for example, implemented in AAC or USAC. The time spectrum converter 100 comprises a windower 502 controlled by a transient detector 504. When the transient
15 detector 504 detects a transient, then a switchover from long windows to short windows is signaled to the windower. The windower 502 then calculates, for overlapping blocks, windowed frames, where each windowed frame typically has two N values such as 2048 values. Then, a transformation within a block transformer 506 is performed, and this block transformer typically additionally provides a decimation, so that a combined
20 decimation/transform is performed to obtain a spectral frame with N values such as MDCT spectral values. Thus, for a long window operation, the frame at the input of block 506 comprises two N values such as 2048 values and a spectral frame then has 1024 values. Then, however, a switch is performed to short blocks, when eight short blocks are performed where each short block has 1/8 windowed time domain values compared to a
25 long window and each spectral block has 1/8 spectral values compared to a long block. Thus, when this decimation is combined with a 50% overlap operation of the windower, the spectrum is a critically sampled version of the time domain audio signal 99.

Subsequently, reference is made to Fig. 5b illustrating a specific implementation of
30 frequency regenerator 116 and the spectrum-time converter 118 of Fig. 1b, or of the combined operation of blocks 208, 212 of Fig. 2a. In Fig. 5b, a specific reconstruction band is considered such as scale factor band 6 of Fig. 3a. The first spectral portion in this reconstruction band, i.e., the first spectral portion 306 of Fig. 3a is input into the frame builder/adjuster block 510. Furthermore, a reconstructed second spectral portion for the
35 scale factor band 6 is input into the frame builder/adjuster 510 as well. Furthermore, energy information such as E_3 of Fig. 3b for a scale factor band 6 is also input into block

510. The reconstructed second spectral portion in the reconstruction band has already been generated by frequency tile filling using a source range and the reconstruction band then corresponds to the target range. Now, an energy adjustment of the frame is performed to then finally obtain the complete reconstructed frame having the N values as, for example, obtained at the output of combiner 208 of Fig. 2a. Then, in block 512, an inverse block transform/interpolation is performed to obtain 248 time domain values for the for example 124 spectral values at the input of block 512. Then, a synthesis windowing operation is performed in block 514 which is again controlled by a long window/short window indication transmitted as side information in the encoded audio signal. Then, in block 516, an overlap/add operation with a previous time frame is performed. Preferably, MDCT applies a 50% overlap so that, for each new time frame of 2N values, N time domain values are finally output. A 50% overlap is heavily preferred due to the fact that it provides critical sampling and a continuous crossover from one frame to the next frame due to the overlap/add operation in block 516.

As illustrated at 301 in Fig. 3a, a noise-filling operation can additionally be applied not only below the IGF start frequency, but also above the IGF start frequency such as for the contemplated reconstruction band coinciding with scale factor band 6 of Fig. 3a. Then, noise-filling spectral values can also be input into the frame builder/adjuster 510 and the adjustment of the noise-filling spectral values can also be applied within this block or the noise-filling spectral values can already be adjusted using the noise-filling energy before being input into the frame builder/adjuster 510.

Preferably, an IGF operation, i.e., a frequency tile filling operation using spectral values from other portions can be applied in the complete spectrum. Thus, a spectral tile filling operation can not only be applied in the high band above an IGF start frequency but can also be applied in the low band. Furthermore, the noise-filling without frequency tile filling can also be applied not only below the IGF start frequency but also above the IGF start frequency. It has, however, been found that high quality and high efficient audio encoding can be obtained when the noise-filling operation is limited to the frequency range below the IGF start frequency and when the frequency tile filling operation is restricted to the frequency range above the IGF start frequency as illustrated in Fig. 3a.

Preferably, the target tiles (TT) (having frequencies greater than the IGF start frequency) are bound to scale factor band borders of the full rate coder. Source tiles (ST), from which information is taken, i.e., for frequencies lower than the IGF start frequency are not bound

by scale factor band borders. The size of the ST should correspond to the size of the associated TT.

Subsequently, reference is made to Fig. 5c illustrating a further preferred embodiment of the frequency regenerator 116 of Fig. 1b or the IGF block 202 of Fig. 2a. Block 522 is a frequency tile generator receiving, not only a target band ID, but additionally receiving a source band ID. Exemplarily, it has been determined on the encoder-side that the scale factor band 3 of Fig. 3a is very well suited for reconstructing scale factor band 7. Thus, the source band ID would be 2 and the target band ID would be 7. Based on this information, the frequency tile generator 522 applies a copy up or harmonic tile filling operation or any other tile filling operation to generate the raw second portion of spectral components 523. The raw second portion of spectral components has a frequency resolution identical to the frequency resolution included in the first set of first spectral portions.

Then, the first spectral portion of the reconstruction band such as 307 of Fig. 3a is input into a frame builder 524 and the raw second portion 523 is also input into the frame builder 524. Then, the reconstructed frame is adjusted by the adjuster 526 using a gain factor for the reconstruction band calculated by the gain factor calculator 528. Importantly, however, the first spectral portion in the frame is not influenced by the adjuster 526, but only the raw second portion for the reconstruction frame is influenced by the adjuster 526. To this end, the gain factor calculator 528 analyzes the source band or the raw second portion 523 and additionally analyzes the first spectral portion in the reconstruction band to finally find the correct gain factor 527 so that the energy of the adjusted frame output by the adjuster 526 has the energy E_4 when a scale factor band 7 is contemplated.

Furthermore, as illustrated in Fig. 3a, the spectral analyzer is configured to analyze the spectral representation up to a maximum analysis frequency being only a small amount below half of the sampling frequency and preferably being at least one quarter of the sampling frequency or typically higher.

As illustrated, the encoder operates without downsampling and the decoder operates without upsampling. In other words, the spectral domain audio coder is configured to generate a spectral representation having a Nyquist frequency defined by the sampling rate of the originally input audio signal.

Furthermore, as illustrated in Fig. 3a, the spectral analyzer is configured to analyze the spectral representation starting with a gap filling start frequency and ending with a maximum frequency represented by a maximum frequency included in the spectral representation, wherein a spectral portion extending from a minimum frequency up to the gap filling start frequency belongs to the first set of spectral portions and wherein a further spectral portion such as 304, 305, 306, 307 having frequency values above the gap filling frequency additionally is included in the first set of first spectral portions.

As outlined, the spectral domain audio decoder 112 is configured so that a maximum frequency represented by a spectral value in the first decoded representation is equal to a maximum frequency included in the time representation having the sampling rate wherein the spectral value for the maximum frequency in the first set of first spectral portions is zero or different from zero. Anyway, for this maximum frequency in the first set of spectral components a scale factor for the scale factor band exists, which is generated and transmitted irrespective of whether all spectral values in this scale factor band are set to zero or not as discussed in the context of Figs. 3a and 3b.

The IGF is, therefore, advantageous that with respect to other parametric techniques to increase compression efficiency, e.g. noise substitution and noise filling (these techniques are exclusively for efficient representation of noise like local signal content) the IGF allows an accurate frequency reproduction of tonal components. To date, no state-of-the-art technique addresses the efficient parametric representation of arbitrary signal content by spectral gap filling without the restriction of a fixed a-priori division in low band (LF) and high band (HF).

Subsequently, further optional features of the full band frequency domain first encoding processor and the full band frequency domain decoding processor incorporating the gap-filling operation, which can be implemented separately or together are discussed and defined.

Particularly, the spectral domain decoder 112 corresponding to block 1122a is configured to output a sequence of decoded frames of spectral values, a decoded frame being the first decoded representation, wherein the frame comprises spectral values for the first set of spectral portions and zero indications for the second spectral portions. The apparatus

for decoding furthermore comprises a combiner 208. The spectral values are generated by a frequency regenerator for the second set of second spectral portions, where both, the combiner and the frequency regenerator are included within block 1122b. Thus, by combining the second spectral portions and the first spectral portions a reconstructed spectral frame comprising spectral values for the first set of the first spectral portions and the second set of spectral portions are obtained and the spectrum-time converter 118 corresponding to the IMDCT block 1124 in Fig. 14b then converts the reconstructed spectral frame into the time representation.

As outlined, the spectrum-time converter 118 or 1124 is configured to perform an inverse modified discrete cosine transform 512, 514 and further comprises an overlap-add stage 516 for overlapping and adding subsequent time domain frames

Particularly, the spectral domain audio decoder 1122a is configured to generate the first decoded representation so that the first decoded representation has a Nyquist frequency defining a sampling rate being equal to a sampling rate of the time representation generated by the spectrum-time converter 1124.

Furthermore, the decoder 1112 or 1122a is configured to generate the first decoded representation so that a first spectral portion 306 is placed with respect to frequency between two second spectral portions 307a, 307b.

In a further embodiment, a maximum frequency represented by a spectral value for the maximum frequency in the first decoded representation is equal to a maximum frequency included in the time representation generated by the spectrum-time converter, wherein the spectral value for the maximum frequency in the first representation is zero or different from zero.

Furthermore, as illustrated in Fig. 3 the encoded first audio signal portion further comprises an encoded representation of a third set of third spectral portions to be reconstructed by noise filling, and the first decoding processor 1120 additionally includes a noise filler included in block 1122b for extracting noise filling information 308 from an encoded representation of the third set of third spectral portions and for applying a noise filling operation in the third set of third spectral portions without using a first spectral portion in a different frequency range.

Furthermore, the spectral domain audio decoder 112 is configured to generate the first decoded representation having the first spectral portions with the frequency values being greater than the frequency being equal to a frequency in the middle of the frequency range covered by the time representation output by the spectrum-time converter 118 or
5 1124.

Furthermore, the spectral analyzer or full-band analyzer 604 is configured to analyze the representation generated by the time-frequency converter 602 for determining a first set of first spectral portions to be encoded with the first high spectral resolution and the different
10 second set of second spectral portions to be encoded with a second spectral resolution which is lower than the first spectral resolution and, by means of the spectral analyzer, a first spectral portion 306 is determined, with respect to frequency, between two second spectral portions in Fig. 3 at 307a and 307b.

15 Particularly, the spectral analyzer is configured for analyzing the spectral representation up to a maximum analysis frequency being at least one quarter of a sampling frequency of the audio signal.

Particularly, the spectral domain audio encoder is configured to process a sequence of
20 frames of spectral values for a quantization and entropy coding, wherein, in a frame, spectral values of the second set of second portions are set to zero, or wherein, in the frame, spectral values of the first set of first spectral portions and the second set of the second spectral portions are present and wherein, during subsequent processing, spectral values in the second set of spectral portions are set to zero as exemplarily illustrated at
25 410, 418, 422.

The spectral domain audio encoder is configured to generate a spectral representation having a Nyquist frequency defined by the sampling rate of the audio input signal or the first portion of the audio signal processed by the first encoding processor operating in the
30 frequency domain.

The spectral domain audio encoder 606 is furthermore configured to provide the first encoded representation so that, for a frame of a sampled audio signal, the encoded representation comprises the first set of first spectral portions and the second set of
35 second spectral portions, wherein the spectral values in the second set of spectral portions are encoded as zero or noise values.

The full band analyzer 604 or 102 is configured to analyze the spectral representation starting with the gap-filling start frequency 209 and ending with a maximum frequency $f_{\max}$ represented by a maximum frequency included in the spectral representation and a spectral portion extending from a minimum frequency up to the gap-filling start frequency 309 belongs to the first set of first spectral portions.

Particularly, the analyzer is configured to apply a tonal mask processing at least of a portion of the spectral representation so that tonal components and non-tonal components are separated from each other, wherein the first set of the first spectral portions comprises the tonal components and wherein the second set of the second spectral portions comprises the non-tonal components.

Although the present invention has been described in the context of block diagrams where the blocks represent actual or logical hardware components, the present invention can also be implemented by a computer-implemented method. In the latter case, the blocks represent corresponding method steps where these steps stand for the functionalities performed by corresponding logical or physical hardware blocks.

Although some aspects have been described in the context of an apparatus, it is clear that these aspects also represent a description of the corresponding method, where a block or device corresponds to a method step or a feature of a method step. Analogously, aspects described in the context of a method step also represent a description of a corresponding block or item or feature of a corresponding apparatus. Some or all of the method steps may be executed by (or using) a hardware apparatus, like for example, a microprocessor, a programmable computer or an electronic circuit. In some embodiments, some one or more of the most important method steps may be executed by such an apparatus.

The inventive transmitted or encoded signal can be stored on a digital storage medium or can be transmitted on a transmission medium such as a wireless transmission medium or a wired transmission medium such as the Internet.

Depending on certain implementation requirements, embodiments of the invention can be implemented in hardware or in software. The implementation can be performed using a digital storage medium, for example a floppy disc, a DVD, a Blu-Ray, a CD, a ROM, a PROM, and EPROM, an EEPROM or a FLASH memory, having electronically readable

control signals stored thereon, which cooperate (or are capable of cooperating) with a programmable computer system such that the respective method is performed. Therefore, the digital storage medium may be computer readable.

- 5 Some embodiments according to the invention comprise a data carrier having electronically readable control signals, which are capable of cooperating with a programmable computer system, such that one of the methods described herein is performed.
- 10 Generally, embodiments of the present invention can be implemented as a computer program product with a program code, the program code being operative for performing one of the methods when the computer program product runs on a computer. The program code may, for example, be stored on a machine readable carrier.
- 15 Other embodiments comprise the computer program for performing one of the methods described herein, stored on a machine readable carrier.

In other words, an embodiment of the inventive method is, therefore, a computer program having a program code for performing one of the methods described herein, when the
20 computer program runs on a computer.

A further embodiment of the inventive method is, therefore, a data carrier (or a non-transitory storage medium such as a digital storage medium, or a computer-readable medium) comprising, recorded thereon, the computer program for performing one of the
25 methods described herein. The data carrier, the digital storage medium or the recorded medium are typically tangible and/or non-transitory.

A further embodiment of the invention method is, therefore, a data stream or a sequence of signals representing the computer program for performing one of the methods
30 described herein. The data stream or the sequence of signals may, for example, be configured to be transferred via a data communication connection, for example, via the internet.

A further embodiment comprises a processing means, for example, a computer or a
35 programmable logic device, configured to, or adapted to, perform one of the methods described herein.

A further embodiment comprises a computer having installed thereon the computer program for performing one of the methods described herein.

- 5 A further embodiment according to the invention comprises an apparatus or a system configured to transfer (for example, electronically or optically) a computer program for performing one of the methods described herein to a receiver. The receiver may, for example, be a computer, a mobile device, a memory device or the like. The apparatus or system may, for example, comprise a file server for transferring the computer program to
10 the receiver .

In some embodiments, a programmable logic device (for example, a field programmable gate array) may be used to perform some or all of the functionalities of the methods described herein. In some embodiments, a field programmable gate array may cooperate
15 with a microprocessor in order to perform one of the methods described herein. Generally, the methods are preferably performed by any hardware apparatus.

The above described embodiments are merely illustrative for the principles of the present invention. It is understood that modifications and variations of the arrangements and the
20 details described herein will be apparent to others skilled in the art. It is the intent, therefore, to be limited only by the scope of the impending patent claims and not by the specific details presented by way of description and explanation of the embodiments herein.

Claims

1. Audio encoder for encoding an audio signal, comprising:
 - 5 a first encoding processor for encoding a first audio signal portion in a frequency domain, wherein the first encoding processor comprises:
 - 10 a time frequency converter for converting the first audio signal portion into a frequency domain representation having spectral lines up to a maximum frequency of the first audio signal portion;
 - a spectral encoder for encoding the frequency domain representation;
 - 15 a second encoding processor for encoding a second different audio signal portion in the time domain,
 - wherein the second encoding processor has an associated second sampling rate,
 - wherein the first encoding processor has associated therewith a first sampling rate
 - 20 being different from the second sampling rate;
 - 25 a cross-processor for calculating, from the encoded spectral representation of the first audio signal portion, initialization data of the second encoding processor, so that the second encoding processing is initialized to encode the second audio signal portion immediately following the first audio signal portion in time in the audio signal; wherein the cross-processor comprises a frequency-time converter for generating a time domain signal at the second sampling rate, wherein the frequency time converter comprises:
 - 30 a selector for selecting a portion of a spectrum input into the frequency time converter in accordance with a ratio of the first sampling rate and the second sampling rate,
 - a transform processor having a transform length being different from a
 - 35 transform length of the time-frequency converter; and

a synthesis windower for windowing using a window having a different number of window coefficients compared to a window used by the time frequency converter;

5 a controller configured for analyzing the audio signal and for determining, which portion of the audio signal is the first audio signal portion encoded in the frequency domain and which portion of the audio signal is the second audio signal portion encoded in the time domain; and

10 an encoded signal former for forming an encoded audio signal comprising a first encoded signal portion for the first audio signal portion and a second encoded signal portion for the second audio signal portion.

2. Audio encoder of claim 1, wherein the input signal has a high band and a low band,
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wherein the second encoding processor comprises a sampling rate converter for converting the second audio signal portion to a lower sampling rate representation, the lower sampling rate being lower than a sampling rate of the audio signal, wherein the lower sampling rate representation does not include the high band of the input signal;
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a time domain low band encoder for time domain encoding the lower sampling rate representation; and
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a time domain bandwidth extension encoder for parametrically encoding the high band.

3. Audio encoder of claim 1 or 2, further comprising:
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a preprocessor configured for preprocessing the first audio signal portion and the second audio signal portion,

wherein the preprocessor comprises a prediction analyzer for determining prediction coefficients;
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wherein the encoded signal former is configured for introducing an encoded version of the prediction coefficients into the encoded audio signal.

5 4. Audio encoder of claims 1, 2 or 3,

wherein a preprocessor comprises a resampler for resampling the audio signal to a sampling rate of the second encoding processor; and

10 wherein a prediction analyzer is configured to determine the prediction coefficients using a resampled audio signal, or

wherein the preprocessor further comprises a long term prediction analysis stage for determining one or more long term prediction parameters for the first audio
15 signal portion.

5. Audio encoder of one of the preceding claims, wherein the cross-processor comprises:

20 a spectral decoder for calculating a decoded version of the first encoded signal portion;

a delay stage for feeding a delayed version of the decoded version into a de-emphasis stage of the second encoding processor for initialization;

25 a weighted prediction coefficient analysis filtering block for feeding a filter output into a codebook determinator of the second encoding processor for initialization;

30 an analysis filtering stage for filtering the decoded version or a pre-emphasized version and for feeding a filter residual into an adaptive codebook determinator of the second encoding processor for initialization; or

35 a pre-emphasis filter for filtering the decoded version and for feeding a delayed or pre-emphasized version to a synthesis filtering stage of the second encoding processor for initialization.

6. Audio encoder of one of the preceding claims,

wherein the first encoding processor is configured to perform a shaping of spectral values of the frequency domain representation using prediction coefficients derived from the first audio signal portion, and wherein the first encoding processor is furthermore configured to perform a quantization and entropy coding operation of shaped spectral values of the first spectral regions.

7. Audio encoder of any one of the preceding claims, wherein the cross-processor comprises:

a noise shaper for shaping quantized spectral values of the frequency domain representation using LPC coefficients derived from the first audio signal portion;

a spectral decoder for decoding spectrally shaped spectral portions of the frequency domain representation with a high spectral resolution to obtain a decoded spectral representation;

a frequency-time converter for converting the spectral representation into a time domain to obtain a decoded first audio signal portion, wherein a sampling rate associated with the decoded first audio signal portion is different from a sampling rate of the audio signal, and a sampling rate associated with an output signal of the frequency-time converter is different from a sampling rate associated with the audio signal input into the frequency-time converter.

8. Audio encoder of one of the preceding claims,

wherein the second encoding processor comprises at least one block of the following group of blocks:

a prediction analysis filter;

an adaptive codebook stage;

an innovative codebook stage;

an estimator for estimating an innovative codebook entry;

5 an ACELP/gain coding stage;

a prediction synthesis filtering stage;

a de-emphasis stage; and

10

a bass post-filter analysis stage.

9. Audio decoder for decoding an encoded audio signal, comprising:

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a first decoding processor for decoding a first encoded audio signal portion in a frequency domain, the first decoding processor comprising a frequency-time converter for converting a decoded spectral representation into a time domain to obtain a decoded first audio signal portion;

20

a second decoding processor for decoding a second encoded audio signal portion in the time domain to obtain a decoded second audio signal portion;

25

a cross-processor for calculating, from the decoded spectral representation of the first encoded audio signal portion, initialization data of the second decoding processor, so that the second decoding processor is initialized to decode the encoded second audio signal portion following in time the first audio signal portion in the encoded audio signal; and

30

a combiner for combining the decoded first spectral portion and the decoded second spectral portion to obtain a decoded audio signal,

wherein the cross-processor further comprises

35

a further frequency-time converter operating at a first effective sampling rate being different from a second effective sampling rate associated with the frequency-time converter of the first decoding

processor to obtain a further decoded first signal portion in the time domain,

5 wherein the signal output by the further frequency-time converter has the second sampling rate being different from the first sampling rate associated with an output of the frequency-time converter of the first decoding processor,

10 wherein the further frequency-time converter comprises a selector for selecting a portion of a spectrum input into the further frequency-time converter in accordance with a ratio of the first sampling rate and the second sampling rate;

15 a transform processor having a transform length being different from a transform length of the time-frequency converter of the first decoding processor; and

20 a synthesis windower using a window having a different number of coefficients compared to a window used by the frequency-time converter of the first decoding processor.

10. Audio decoder of claim 9, wherein the second decoding processor comprises:

25 a time domain low band decoder for decoding a low band time domain signal;

a resampler for resampling the low band time domain signal;

30 a time domain bandwidth extension decoder for synthesizing a high band of a time domain output signal; and

a mixer for mixing a synthesized high band of the time domain signal and a resampled low band time domain signal.

11. Audio decoder of one of the claims 9 to 10,

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wherein the first decoding processor comprises an adaptive long term prediction post-filter for post-filtering the first decoded first signal portion, wherein the filter is controlled by one or more long term prediction parameters included in the encoded audio signal.

5

12. Audio decoder of one of claims 9 to 11, wherein the cross-processor comprises:

a delay stage for delaying the further decoded first signal portion and for feeding a delayed version of the decoded first signal portion into a de-emphasis stage of the second decoding processor for initialization;

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a pre-emphasis filter and a delay stage for filtering and delaying the further decoded first signal portion and for feeding a delay stage output into a prediction synthesis filter of the second decoding processor for initialization;

15

a prediction analysis filter for generating a prediction residual signal from the further decoded first spectral portion or a pre-emphasized further decoded first signal portion and for feeding a prediction residual signal into a codebook synthesizer of the second decoding processor; or

20

a switch for feeding the further decoded first signal portion into an analysis stage of a resampler of the second decoding processor for initialization.

13. Audio decoder of one of claims 9 to 12,

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wherein the second decoding processor comprises at least one block of the group of blocks comprising:

a stage for decoding ACELP gains and an innovative codebook;

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an adaptive codebook synthesis stage;

an ACELP post-processor

;

a prediction synthesis filter; and

a de-emphasis stage.

5 14. Method of encoding an audio signal, comprising:

encoding a first audio signal portion in a frequency domain, comprising:

10 converting the first audio signal portion into a frequency domain
representation having spectral lines up to a maximum frequency of the first
audio signal portion;

encoding the frequency domain representation;

15 encoding a second different audio signal portion in the time domain;

wherein the encoding the second audio signal portion has an associated second
sampling rate,

20

wherein the encoding the first audio signal portion has associated therewith a first
sampling rate being different from the second sampling rate

25 calculating, from the encoded spectral representation of the first audio signal
portion, initialization data for the step of encoding the second different audio signal
portion, so that the step of encoding the second different audio signal portion is
initialized to encode the second audio signal portion immediately following the first
audio signal portion in time in the audio signal wherein the calculating comprises
generating, by a frequency-time converter, a time domain signal at the second
30 sampling rate, wherein the generating comprises:

selecting a portion of a spectrum input into the frequency-time converter in
accordance with a ratio of the first sampling rate and the second sampling
rate,

processing using a transform processor having a transform length being different from a transform length of a time-frequency converter used in the converting the first audio signal portion; and

5 synthesis windowing using a window having a different number of window coefficients compared to a window used by the time frequency converter used in the converting the first audio signal portion;

10 analyzing the audio signal and determining, which portion of the audio signal is the first audio signal portion encoded in the frequency domain and which portion of the audio signal is the second audio signal portion encoded in the time domain; and

15 forming an encoded audio signal comprising a first encoded signal portion for the first audio signal portion and a second encoded signal portion for the second audio signal portion.

15. Method of decoding an encoded audio signal, comprising:

20 decoding, by a first decoding processor, a first encoded audio signal portion in a frequency domain, the decoding comprising: converting, by a frequency-time converter, a decoded spectral representation into a time domain to obtain a decoded first audio signal portion;

25 decoding a second encoded audio signal portion in the time domain to obtain a decoded second audio signal portion;

30 calculating, from the decoded spectral representation of the first encoded audio signal portion, initialization data of the step of decoding the second encoded audio signal portion, so that the step of decoding the second encoded audio signal portion is initialized to decode the encoded second audio signal portion following in time the first audio signal portion in the encoded audio signal; and

combining the decoded first spectral portion and the decoded second spectral portion to obtain a decoded audio signal,

wherein the calculating further comprises

5 using a further frequency-time converter operating at a first effective sampling rate being different from a second effective sampling rate associated with the frequency-time converter of the first decoding processor to obtain a further decoded first signal portion in the time domain,

10 wherein the signal output by the further frequency-time converter has the second sampling rate being different from the first sampling rate associated with an output of the frequency-time converter of the first decoding processor,

15 wherein the using the further frequency-time converter comprises:

selecting a portion of a spectrum input into the further frequency-time converter in accordance with a ratio of the first sampling rate and the second sampling rate;

20 using a transform processor having a transform length being different from a transform length of the time-frequency converter of the first decoding processor; and

25 using a synthesis windower using a window having a different number of coefficients compared to a window used by the frequency-time converter of the first decoding processor.

16. Computer program for performing, when running on a computer or a processor, the method of claim 14 or claim 15.

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