

RESISTOR AND ITS MANUFACTURING METHOD**ABSTRACT**

A resistor comprising a substrate, a pair of first top electrode layers (22) disposed at least on the top face of the substrate (21), a resistance layer (23) disposed so as to electrically connect with the first top electrode layers (22), a protective layer (24) disposed so as to cover at least the resistance layer (23), and a pair of second top electrode layers (25) disposed at least on the top faces of the pair of first top electrode layers (22). At least one of the pairs of first top electrode layers (22) or second top electrode layers (25) partially extends to the substrate (21) side faces.

Fig. 1

