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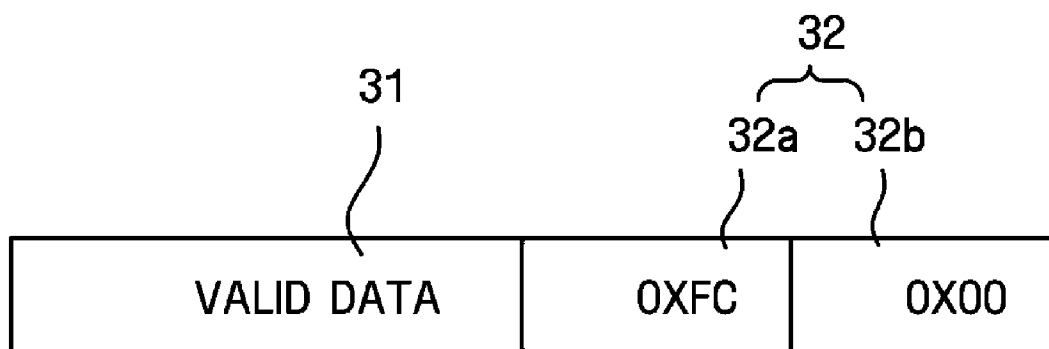
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(54) Title: APPARATUS AND METHOD FOR PROCESSING DATA OF FLASH MEMORY



(57) Abstract: Provided are an apparatus and method for processing data of flash memory. The apparatus includes a user requesting unit to request a data operation using a predetermined logical address, a transformation unit to transform the logical address into a physical address, and a control unit to record count data counting the number of predetermined bits of data, in an index region to indicate whether the data is valid when conducting the data operation.

WO 2006/054852 A1