

**SYSTEM AND METHOD FOR REDUCING POWER
CONSUMPTION IN AN ELECTRONIC CIRCUIT**

ABSTRACT OF THE DISCLOSURE

While dispatch circuitry operates in a first power mode, per cycle of the dispatch circuitry, up to N number of instructions are dispatched to execution circuitry for execution, where N is an integer number and $N > 1$. While the dispatch circuitry operates in a second power mode, per cycle of the dispatch circuitry, up to M number of instructions are dispatched to the execution circuitry for execution, where M is an integer number and $0 < M < N$.

This Sheet Has Been Replaced

