WHAT IS CLAIMED IS:

1. A three-dimensional (3D) video encoder comprising:

circuitry to generate a flag associated with a video block of multiview video, the video block corresponding to one of a depth component of a first view of the multiview video, a texture component of the first view of the multiview video, a depth component of a second view of the multiview video, the second view being different than the first view, or a texture component of the second view of the multiview video, the flag to indicate the video block is encoded based on inherited prediction parameters associated with a second video block of the multiview video, the second video block corresponding to another of the depth component of the first view, the texture component of the first view, the depth component of the second view, or the texture component of the second view, wherein the inherited prediction parameters comprise inter-prediction modes in response to the second video block including coding blocks encoded based on the inter-prediction modes, intra-prediction modes in response to the second video block including coding blocks encoded based on intra-prediction modes, and a combination of inter and intra-prediction modes in response to the second video block including both coding blocks encoded based on the inter modes and coding blocks encoded based on the intra modes;

circuitry to encode the video block based on the inherited prediction parameters;

circuitry to insert the encoded video block, the flag, information identifying the second block from which the prediction parameters were inherited is determinable, and the inherited prediction parameters into a bitstream representing 3D video.

2. The 3D video encoder of claim 1, wherein the video block is a texture view block comprising video information for rendering a picture including luminance and chroma samples and the second video block is a depth view block comprising information corresponding to the block of the texture view for forming 3D video comprising depth values on a per-pixel basis.

3. The 3D video encoder of claim 1, wherein the video block is larger than or smaller than the second video block.

4. The 3D video encoder of claim 1, wherein the flag indicates use of inherited prediction parameters on a per-sequence, per-view, per-slice, per-picture, per-coding unit (CU), or per-prediction unit (PU) level.

5. The 3D video encoder of claim 1, wherein the prediction parameters further comprise at least one of a picture coding structure, or a partition size and shape.

6. The 3D video encoder of claim 1, wherein the video block and the second video block both correspond to the same first view, the same first view comprising a same camera perspective or angle.

7. The 3D video encoder of claim 1, further comprising:

circuitry to determine whether to encode a respective video block based on inherited prediction parameters ; and

circuitry to encode the respective video block based on prediction parameters that are not inherited if it is determined not to encode based on inherited prediction parameters.

8. The 3D video encoder of claim 1, further comprising:

circuitry to encode a third video block corresponding to the first view based on second inherited prediction parameters associated with a fourth video block corresponding to the second view.

9. The 3D video encoder of claim 8, wherein the fourth video block includes coding blocks encoded based on the inter-prediction modes and the second inherited prediction parameters comprise at least one motion vector and at least one reference index.

10. A system comprising:

a display; and

a three-dimensional (3D) video encoder including:

circuitry to generate a flag associated with a video block of multiview video, the video block corresponding to one of a depth component of a first view of the multiview video, a texture component of the first view of the multiview video, a depth component of a second view of the multiview video, the second view being different than the first view, or a texture component of the second view of the multiview video, the flag to indicate the video block is encoded based on inherited prediction parameters associated with a second video block of the

multiview video, the second video block corresponding to another of the depth component of the first view, the texture component of the first view, the depth component of the second view, or the texture component of the second view, wherein the inherited prediction parameters comprise inter-prediction modes in response to the second video block including coding blocks encoded based on the inter-prediction modes, intra-prediction modes in response to the second video block including coding blocks encoded based on intra-prediction modes, and a combination of inter and intra-prediction modes in response to the second video block including both coding blocks encoded based on the inter modes and coding blocks encoded based on the intra modes;

circuitry to encode the video block based on the inherited prediction parameters;

circuitry to insert the encoded video block, the flag, information identifying the second block from which the prediction parameters were inherited is determinable, and the inherited prediction parameters into a bitstream representing 3D video.

11. The system of claim 10, wherein the flag indicates use of inherited prediction parameters on a per-sequence, per-view, per-slice, per-picture, per-coding unit (CU), or per-prediction unit (PU) level.

12. The system of claim 10, wherein the prediction parameters further comprise at least one of a picture coding structure, or a partition size and shape.

13. The system of claim 10, wherein the 3D video encoder further comprises:

circuitry to determine whether to encode a respective video block based on inherited prediction parameters ; and

circuitry to encode the respective video block based on prediction parameters that are not inherited if it is determined not to encode based on inherited prediction parameters.

14. The system of claim 10, wherein the 3D video encoder further comprises:

circuitry to encode a third video block corresponding to the first view based on second inherited prediction parameters associated with a fourth video block corresponding to the second view.

15. A system comprising:

a graphics subsystem; and

a three-dimensional (3D) video encoder including:

circuitry to generate a flag associated with a video block of multiview video, the video block corresponding to one of a depth component of a first view of the multiview video, a texture component of the first view of the multiview video, a depth component of a second view of the multiview video, the second view being different than the first view, or a texture component of the second view of the multiview video, the flag to indicate the video block is encoded based on inherited prediction parameters associated with a second video block of the

multiview video, the second video block corresponding to another of the depth component of the first view, the texture component of the first view, the depth component of the second view, or the texture component of the second view, wherein the inherited prediction parameters comprise inter-prediction modes in response to the second video block including coding blocks encoded based on the inter-prediction modes, intra-prediction modes in response to the second video block including coding blocks encoded based on intra-prediction modes, and a combination of inter and intra-prediction modes in response to the second video block including both coding blocks encoded based on the inter modes and coding blocks encoded based on the intra modes;

circuitry to encode the video block based on the inherited prediction parameters;

circuitry to insert the encoded video block, the flag, information identifying the second block from which the prediction parameters were inherited is determinable, and the inherited prediction parameters into a bitstream representing 3D video.

16. The system of claim 15, wherein the flag indicates use of inherited prediction parameters on a per-sequence, per-view, per-slice, per-picture, per-coding unit (CU), or per-prediction unit (PU) level.

17. The system of claim 15, wherein the prediction parameters further comprise at least one of a picture coding structure, or a partition size and shape.

18. The system of claim 15, wherein the 3D video encoder further comprises:

circuitry to determine whether to encode a respective video block based on inherited prediction parameters ; and

circuitry to encode the respective video block based on prediction parameters that are not inherited if it is determined not to encode based on inherited prediction parameters.

19. The system of claim 15, wherein the 3D video encoder further comprises:

circuitry to encode a third video block corresponding to the first view based on second inherited prediction parameters associated with a fourth video block corresponding to the second view.

20. A non-transitory computer readable medium having a computer program including instructions to cause a processor to:

generate a flag associated with a video block of multiview video, the video block corresponding to one of a depth component of a first view of the multiview video, a texture component of the first view of the multiview video, a depth component of a second view of the multiview video, the second view being different than the first view, or a texture component of the second view of the multiview video, the flag to indicate the video block is encoded based on inherited prediction parameters associated with a second video block of the multiview video, the second video block corresponding to another of the depth component of the first view, the texture component of the first view, the depth

component of the second view, or the texture component of the second view, wherein the inherited prediction parameters comprise inter-prediction modes in response to the second video block including coding blocks encoded based on the inter-prediction modes, intra-prediction modes in response to the second video block including coding blocks encoded based on intra-prediction modes, and a combination of inter and intra-prediction modes in response to the second video block including both coding blocks encoded based on the inter modes and coding blocks encoded based on the intra modes;

encode the video block based on the inherited prediction parameters;

insert the encoded video block, the flag, information identifying the second block from which the prediction parameters were inherited is determinable, and the inherited prediction parameters into a bitstream representing 3D video.

21. The computer readable medium of claim 20, wherein the flag indicates use of inherited prediction parameters on a per-sequence, per-view, per-slice, per-picture, per-coding unit (CU), or per-prediction unit (PU) level.

22. The computer readable medium of claim 20, wherein the prediction parameters further comprise at least one of a picture coding structure, or a partition size and shape.

23. The computer readable medium of claim 20, wherein the computer program further includes instructions to cause the processor to:

determine whether to encode a respective video block based on inherited prediction parameters; and

encode the respective video block based on prediction parameters that are not inherited if it is determined not to encode based on inherited prediction parameters.

24. The computer readable medium of claim 20, wherein the computer program further includes instructions to cause the processor to:

encode a third video block corresponding to the first view based on second inherited prediction parameters associated with a fourth video block corresponding to the second view.