

ABSTRACT

SEMICONDUCTOR MEMORY DEVICE AND ERASING METHOD OF THE SEMICONDUCTOR MEMORY DEVICE

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There are provided a semiconductor memory device and an erasing method of the semiconductor memory device. The semiconductor memory device includes: a plurality of word lines stacked between a source conductive pattern and a bit line; at least two drain select lines disposed between the plurality word lines and the bit line, the at least two drain select lines being spaced apart from each other in an extending direction of the bit line; and an erase control line disposed between the at least two drain select lines and the plurality of word lines.

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15 FIG. 4