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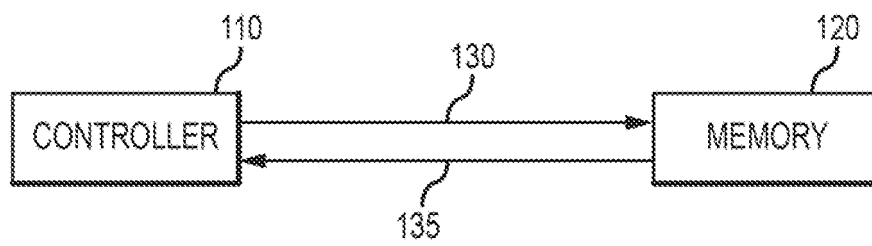
100

FIGURE 1A

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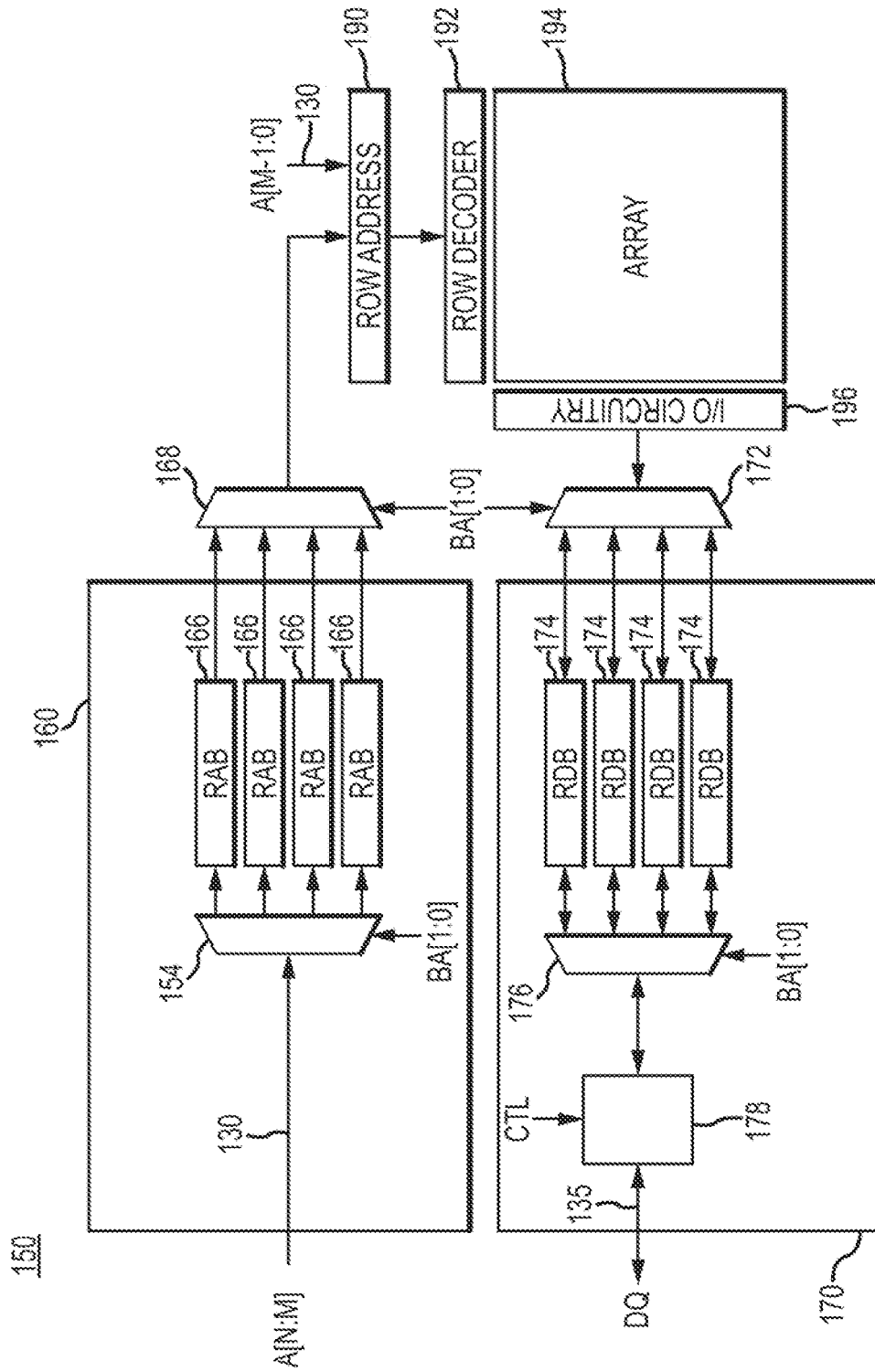


FIGURE 1B

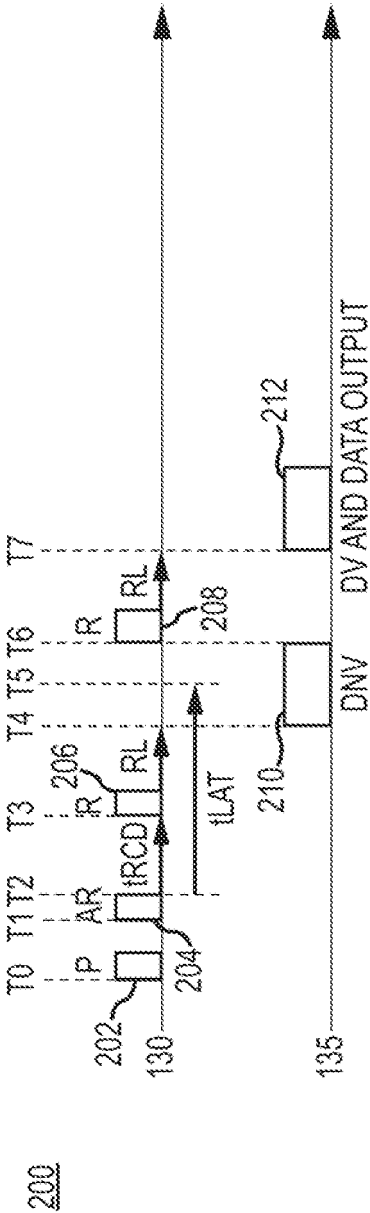


FIGURE 2A

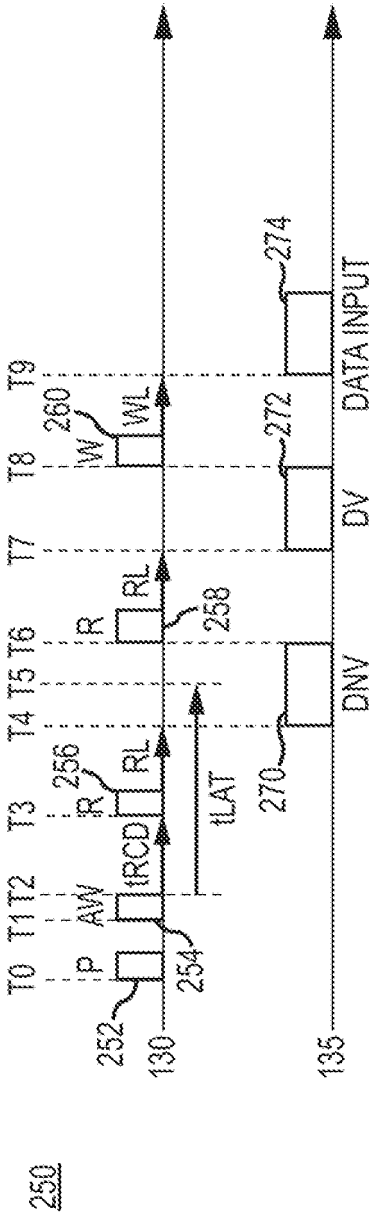


FIGURE 2B

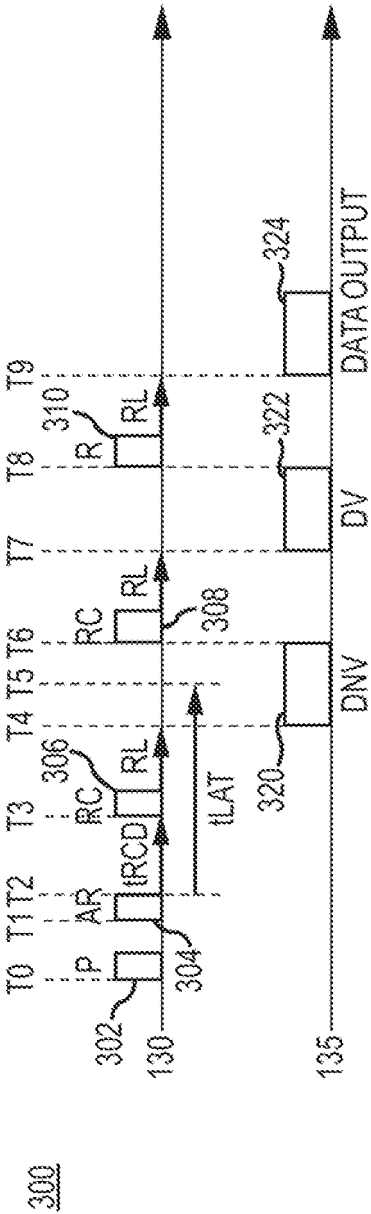


FIGURE 3A

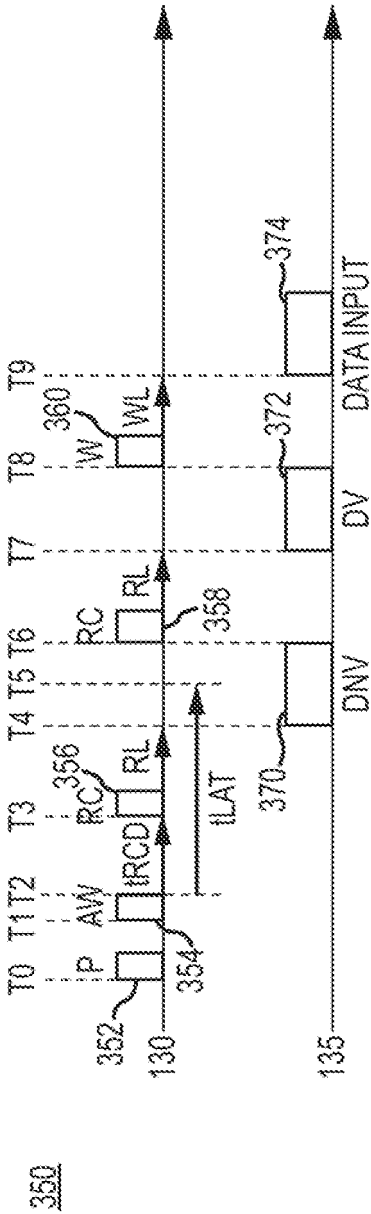


FIGURE 3B

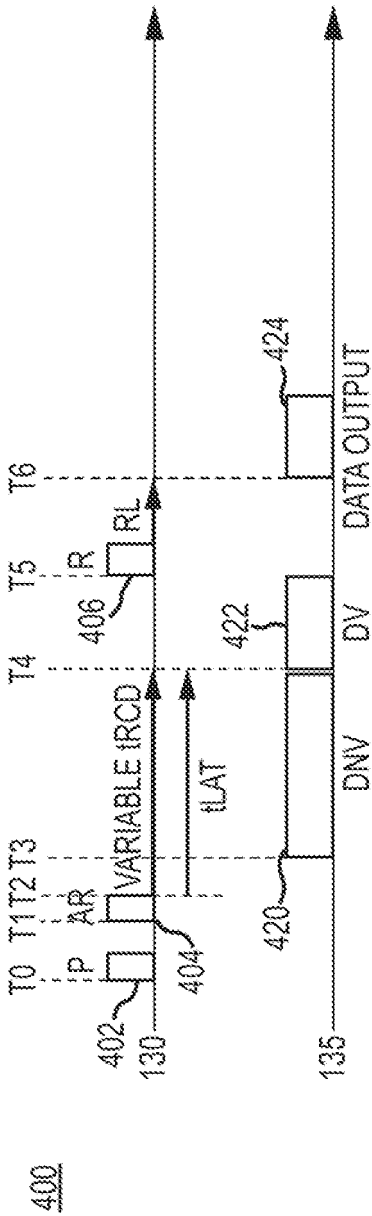


FIGURE 4A

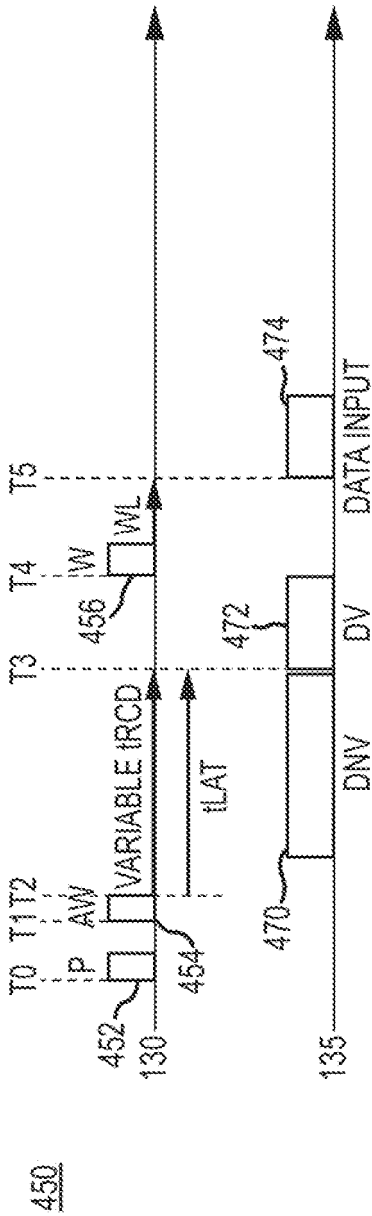


FIGURE 4B

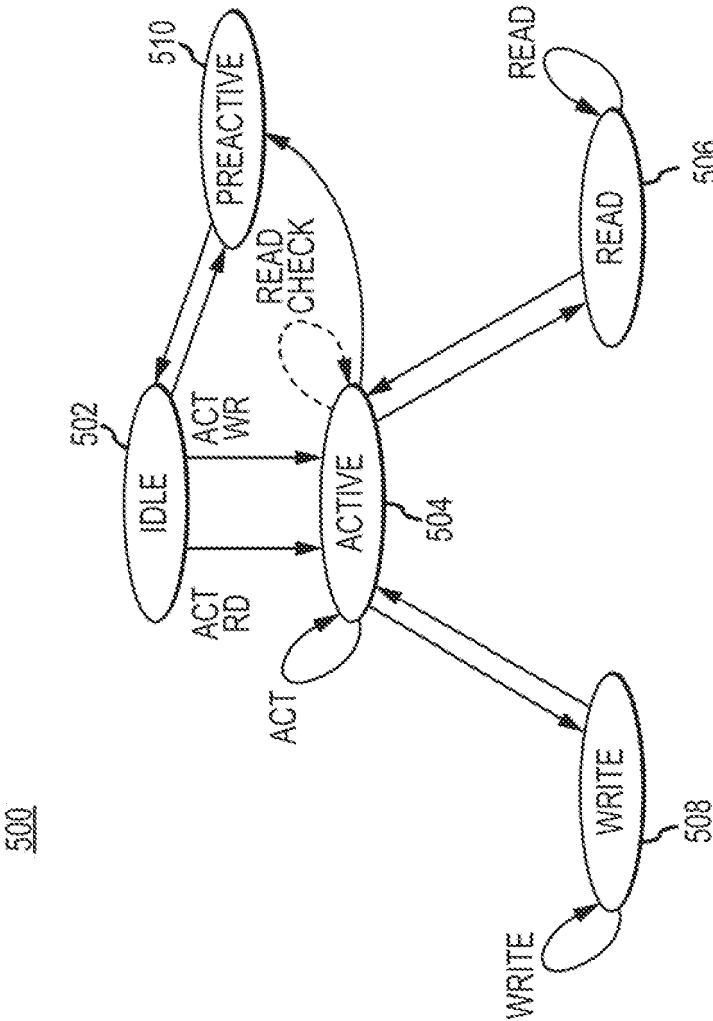


FIGURE 5

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600

TOR[5:3]	TRANSPARENT WRITE MAX LATENCY TIME (tMAXLATW)	000	UNLIMITED	tBUSY=LATENCY CONFIGURATION GRANULARITY (I.E. 25us) UNLIMITED ALLOWS MEMORY TO COMPLETE ANY INTERNAL OPERATION
		001	1*tBUSY	
		010	2*tBUSY	
		011	3*tBUSY	
		100	4*tBUSY	
		101	5*tBUSY	
		110	6*tBUSY	
		111	7*tBUSY	
TOR[2:0]	TRANSPARENT READ MAX LATENCY TIME (tMAXLATR)	000	UNLIMITED	tBUSY=LATENCY CONFIGURATION GRANULARITY (I.E. 25us) UNLIMITED ALLOWS MEMORY TO COMPLETE ANY INTERNAL OPERATION
		001	1*tBUSY	
		010	2*tBUSY	
		011	3*tBUSY	
		100	4*tBUSY	
		101	5*tBUSY	
		110	6*tBUSY	
		111	7*tBUSY	

FIGURE 6

700

BIT 3	BIT 2	BIT 1	WAIT STATE
0	0	0	"SHORT" → WAIT ASSERTED, ≤ 1us (E.G.)
0	1	0	"NORMAL" → WAIT ASSERTED, ≤ tBUSY
1	0	0	"LONG" → WAIT ASSERTED, > tBUSY
1	1	0	RESERVED

FIGURE 7

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800

BIT3	BIT2	BIT1	ACKNOWLEDGE EVENT	CURRENT READ COMMAND	CURRENT WRITE COMMAND
0	0	1	WAIT DE-ASSERTED	ACCEPTED	
0	1	1		ACCEPTED BUT A HOUSE KEEPING OPERATION IS SUGGESTED ACTION: ISSUE HOUSE KEEPING COMMAND	
1	0	1		ACCEPTED BUT THERE HAS BEEN AN ERROR DURING A PREVIOUS OPERATION (E.G. LOCK ERROR) ACTION: CHECK STATUS REGISTER	
1	1	1		NA	REJECTED DUE TO AN ERROR (E.G. LOCK ERROR) ACTION: CHECK STATUS REGISTER

FIGURE 8