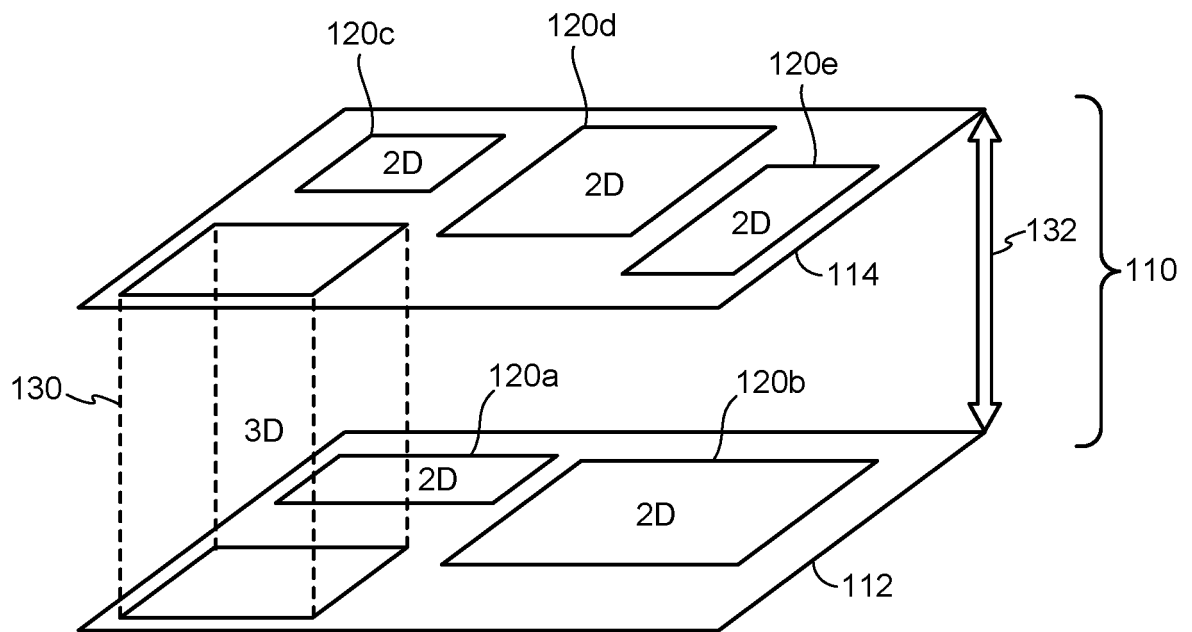
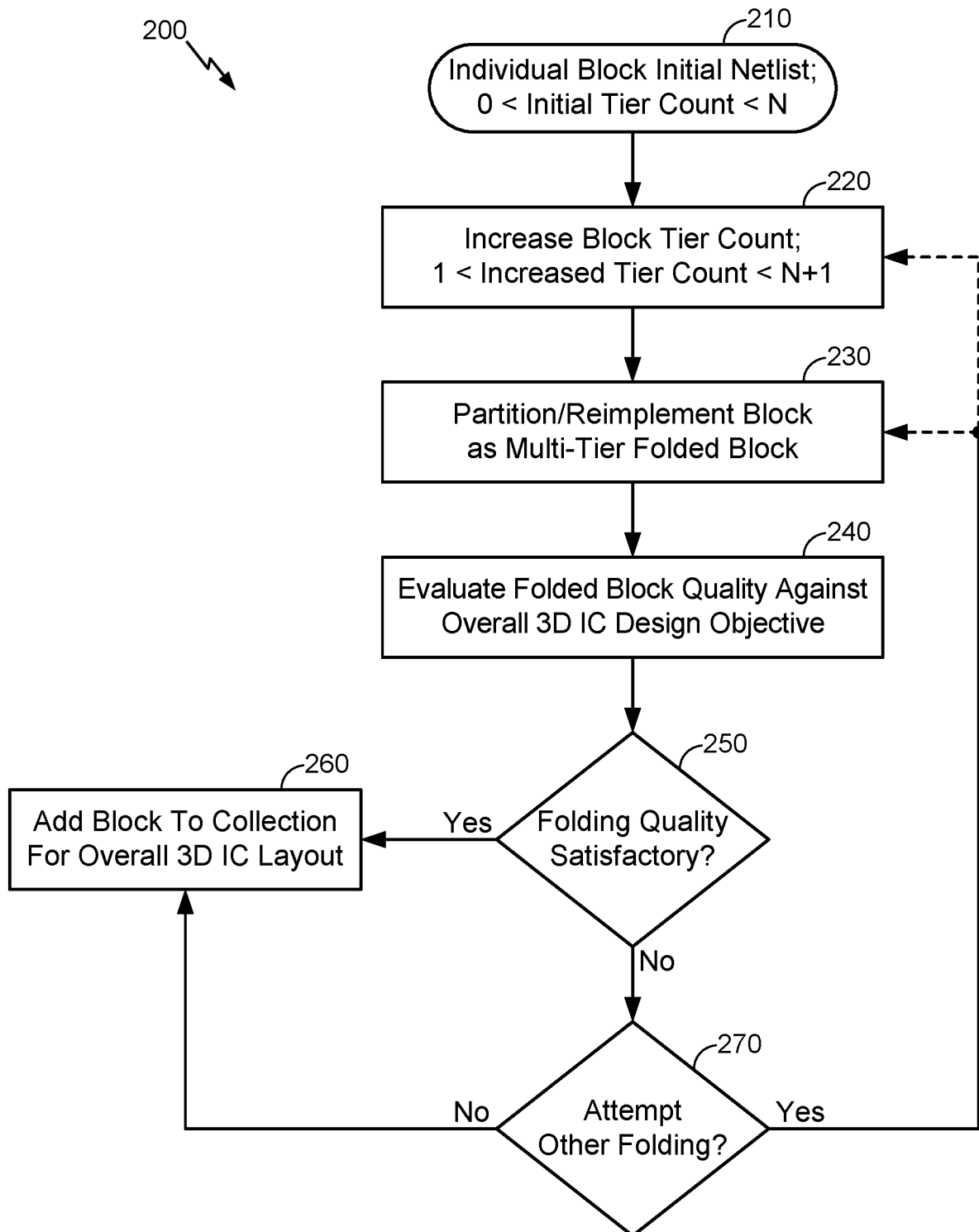
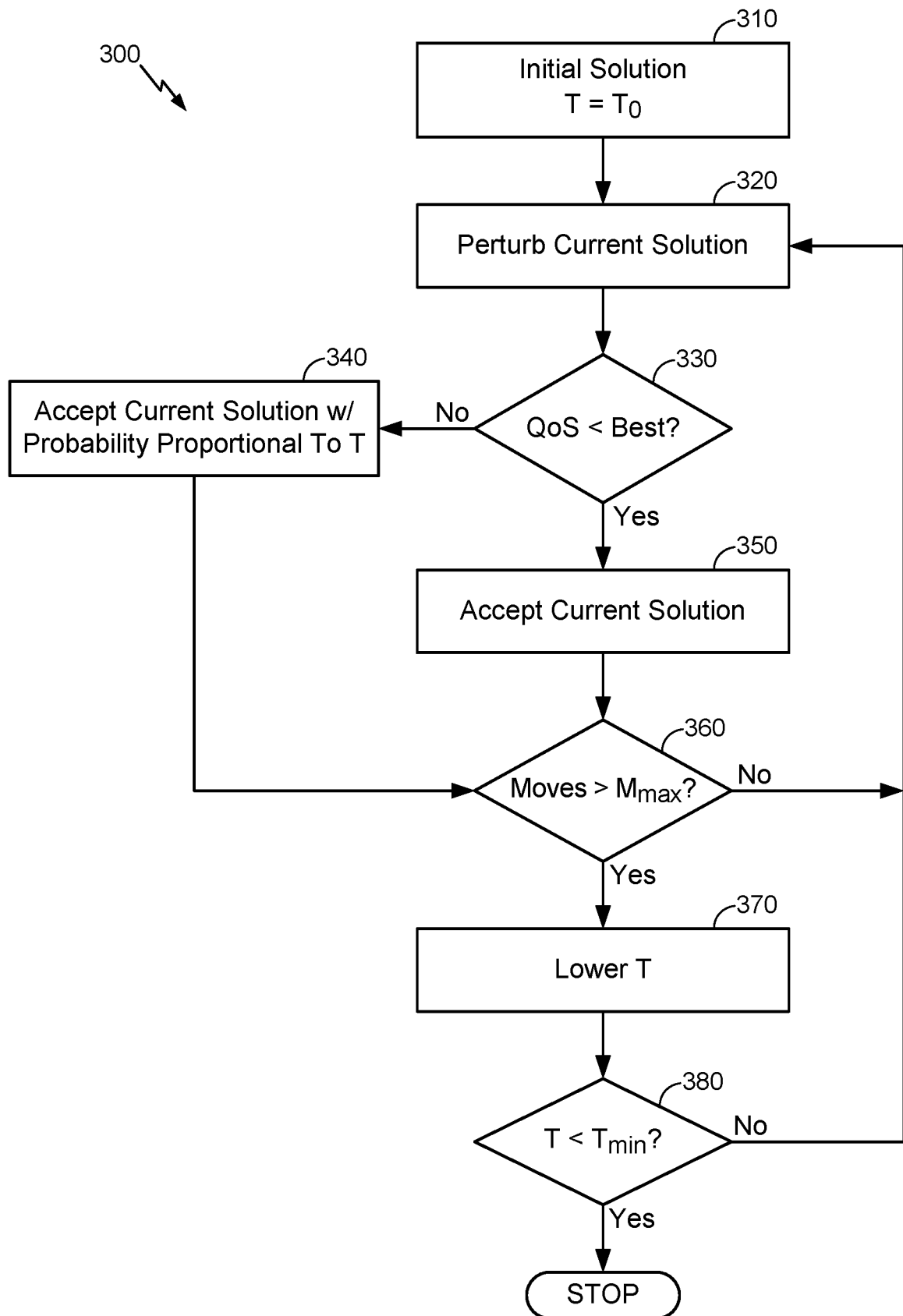


**FIG. 1**

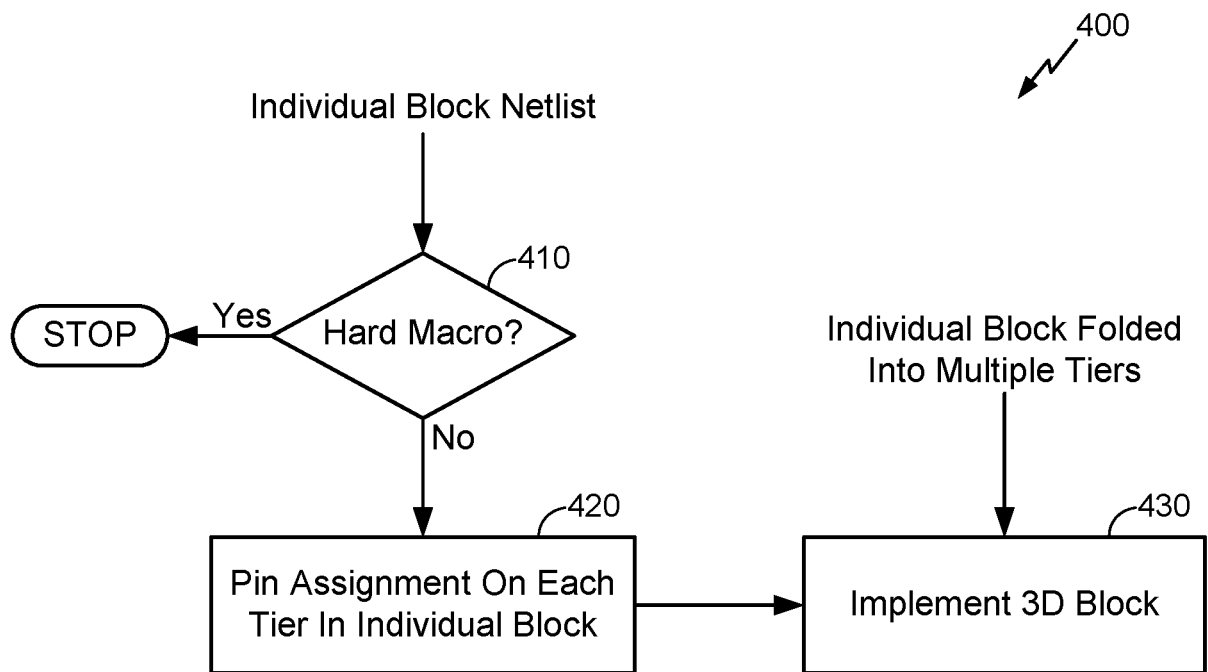
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**FIG. 2**

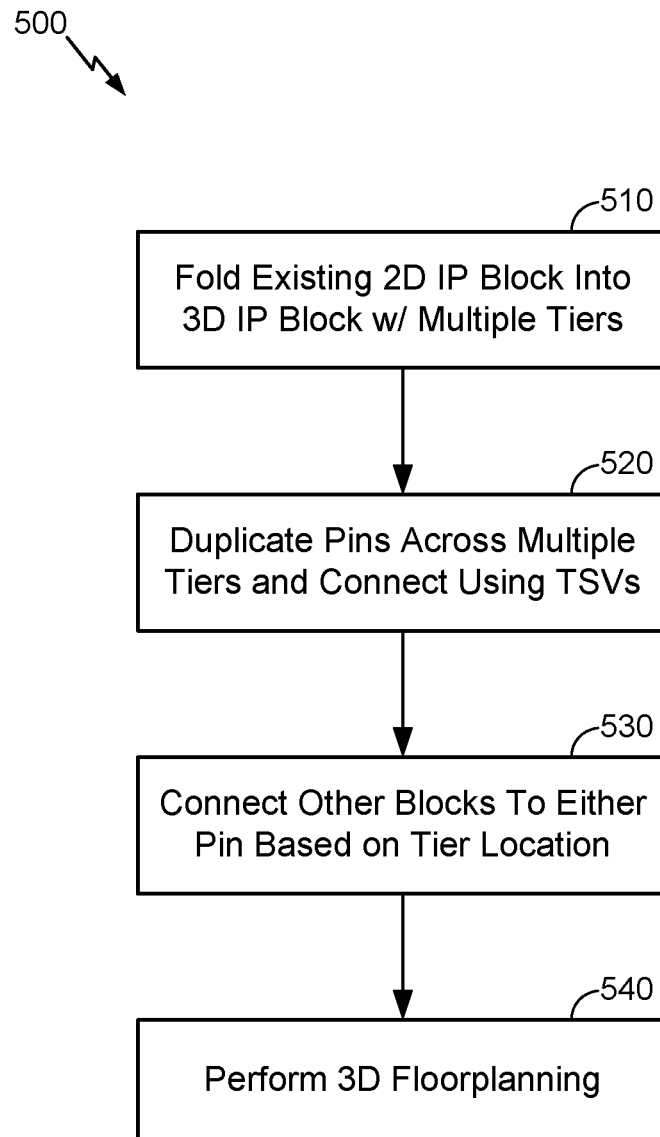
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**FIG. 3**

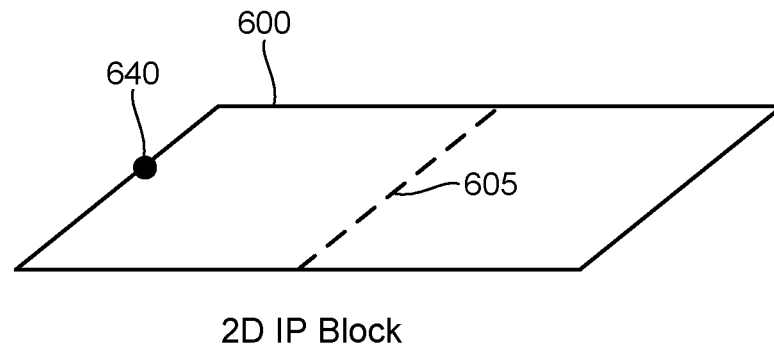
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**FIG. 4**

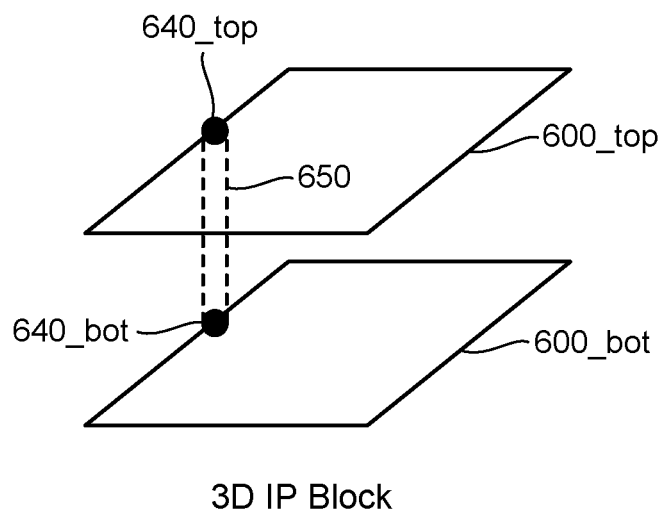
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**FIG. 5**

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Folding and Pin
Duplication

**FIG. 6**

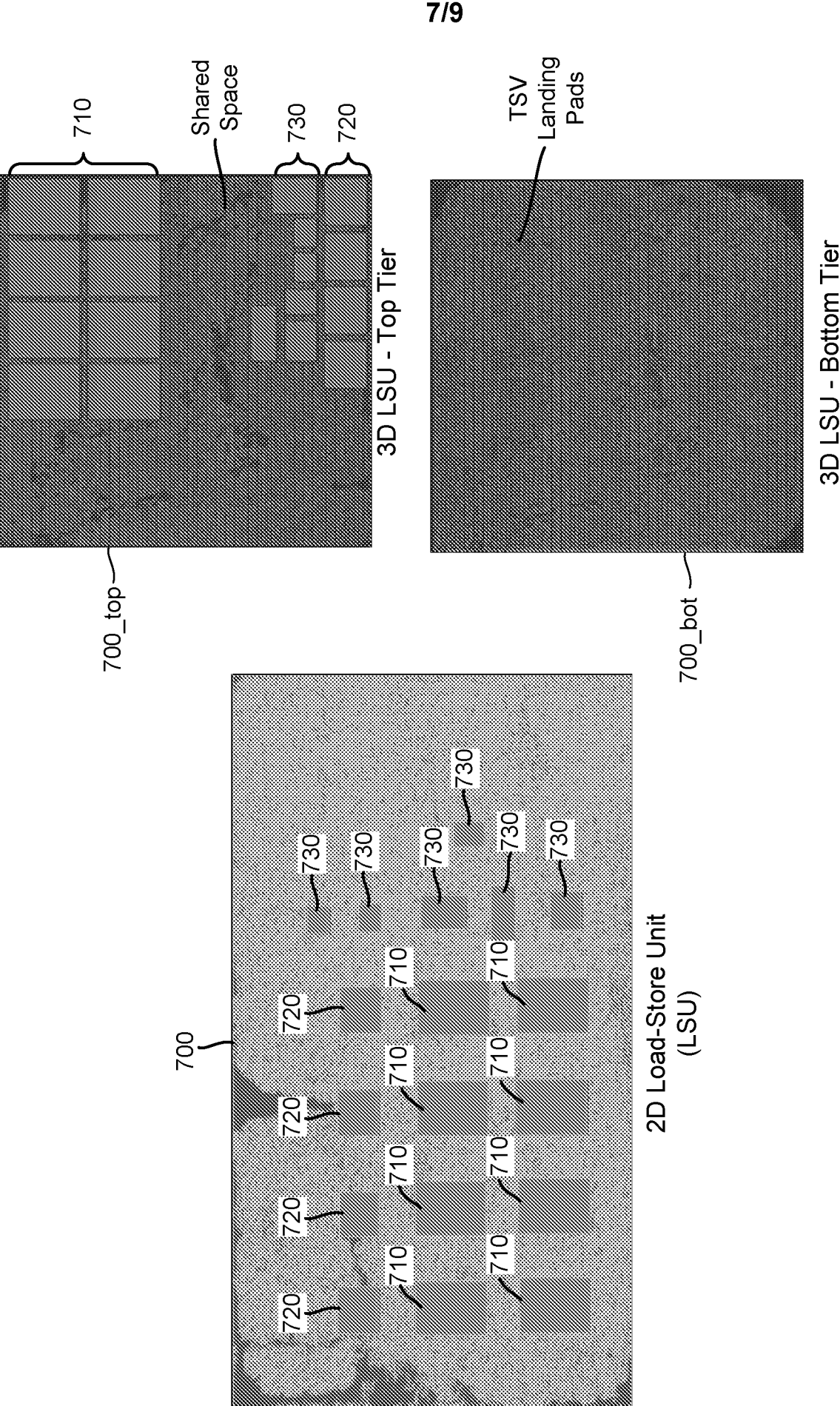
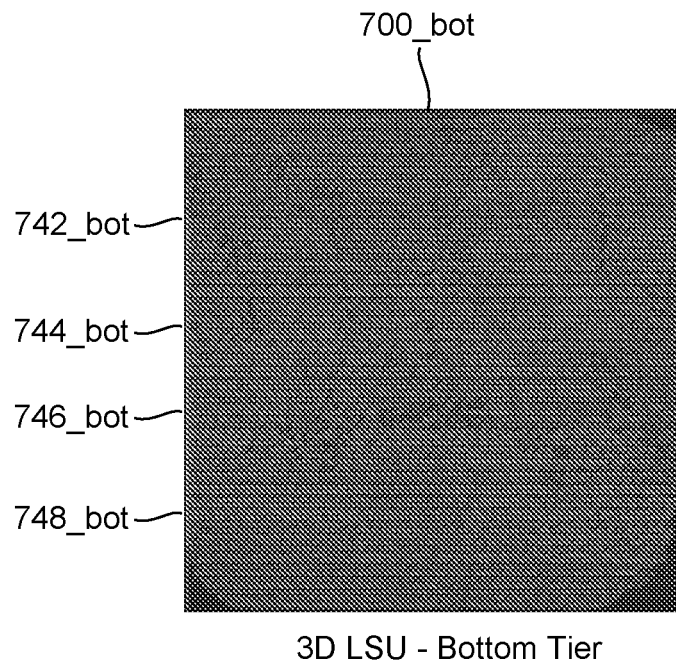
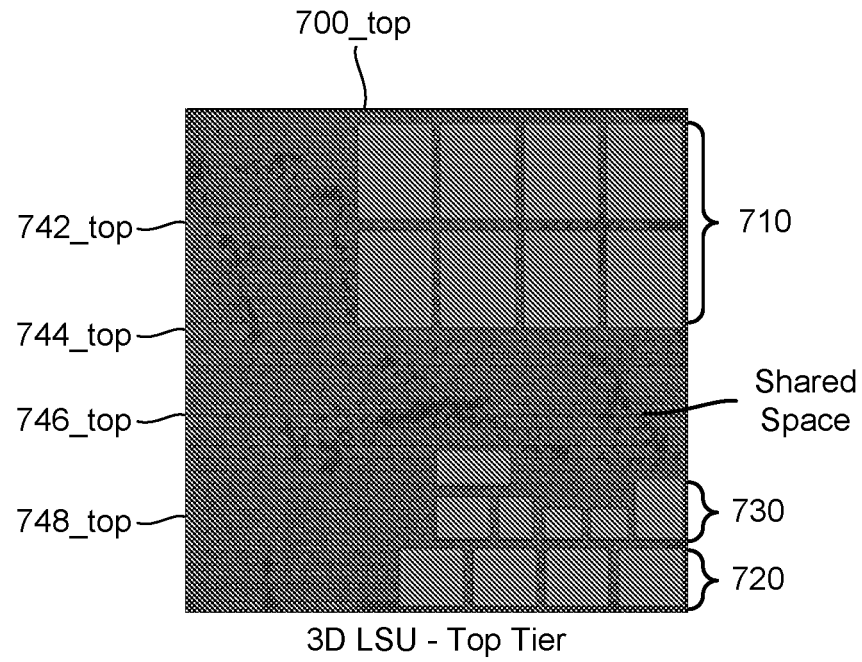


FIG. 7A

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**FIG. 7B**



T2 Core with 3D LSU

T2 Core with 2D LSU

FIG. 7C