ABSTRACT

SYSTEM AND METHOD FOR ESTABLISHING A KNOWN TIMING RELATIONSHIP BETWEEN TWO CLOCK SIGNALS

A system and method for establishing a known timing relationship between two clock signals, wherein a first clock signal (222) is operable to clock data transfer operations from a transmitter domain (202) to a receiver domain (204) and a second clock signal (334A or 334B) is operable to be transported to the receiver domain (204). Circuitry is provided for detecting an edge in a global framework clock (GFC) signal (214) that is supplied to the transmitter domain (202). A common alignment signal (320) is manufactured that is based at least in part upon the GFC signal (214). A multiplexer and register arrangement is operable to output the second clock signal (334A or 334B) in response to the common alignment signal (320) which is also used for gating the data transfer operations clocked by the first clock signal (222).

(FIG. 5)