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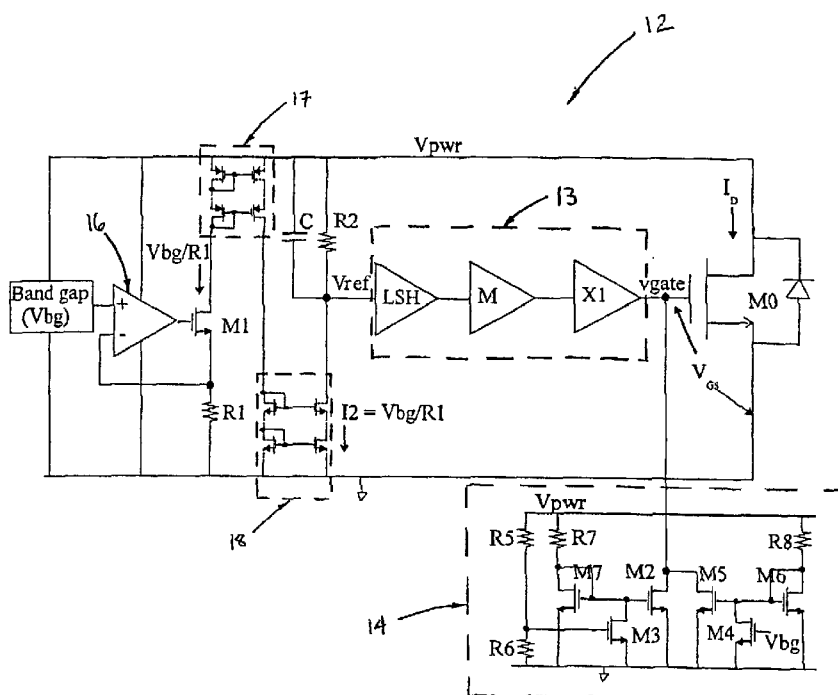
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(54) Title: TRANSIENT VOLTAGE CLAMPING CIRCUIT



(57) Abstract: A circuit (12) for transient voltage clamping, the circuit being internal to a motor driver ASIC (10) for a hard drive and comprising: a power transistor (M0) for sinking a power supply voltage (V_{pwr}) subjected to transient variation; a reference circuit for deriving a first reference voltage (V_{ref}) from a second reference voltage (V_{bg}) and the power supply voltage (V_{pwr}); and an amplifier circuit (13) for receiving the first reference voltage (V_{ref}) as input and for driving the power transistor (M0).