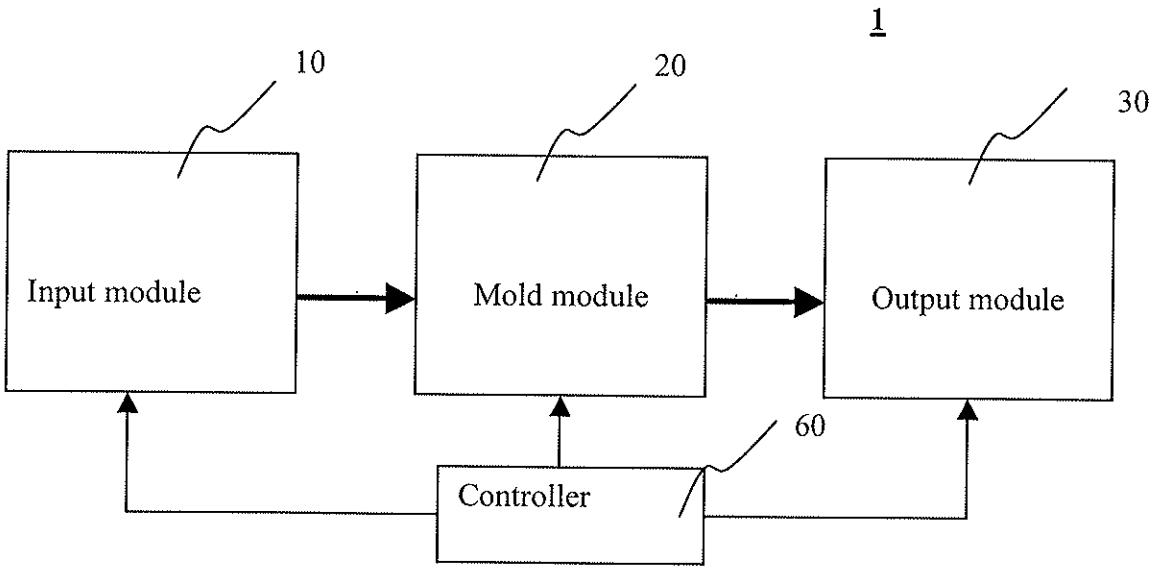

1
10
20
30
Input module
Mold module
Output module
60
Controller

FIG 1

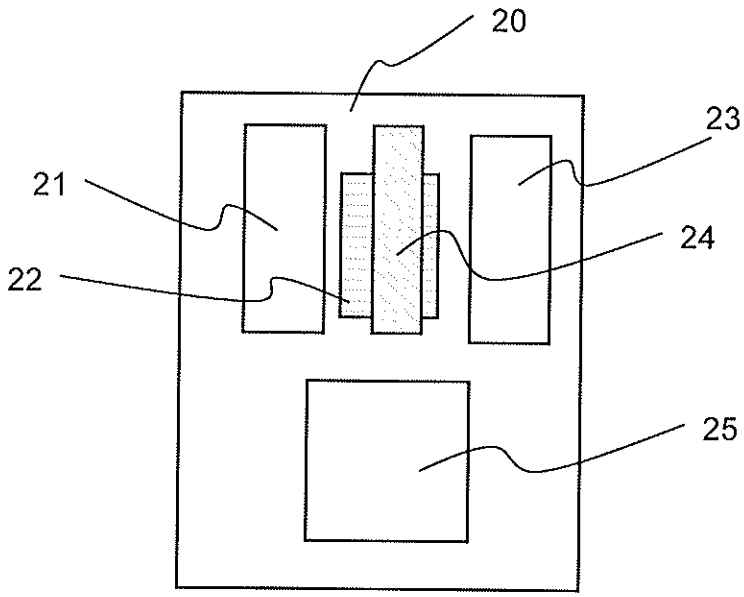

20
23
21
24
22
25

FIG 2

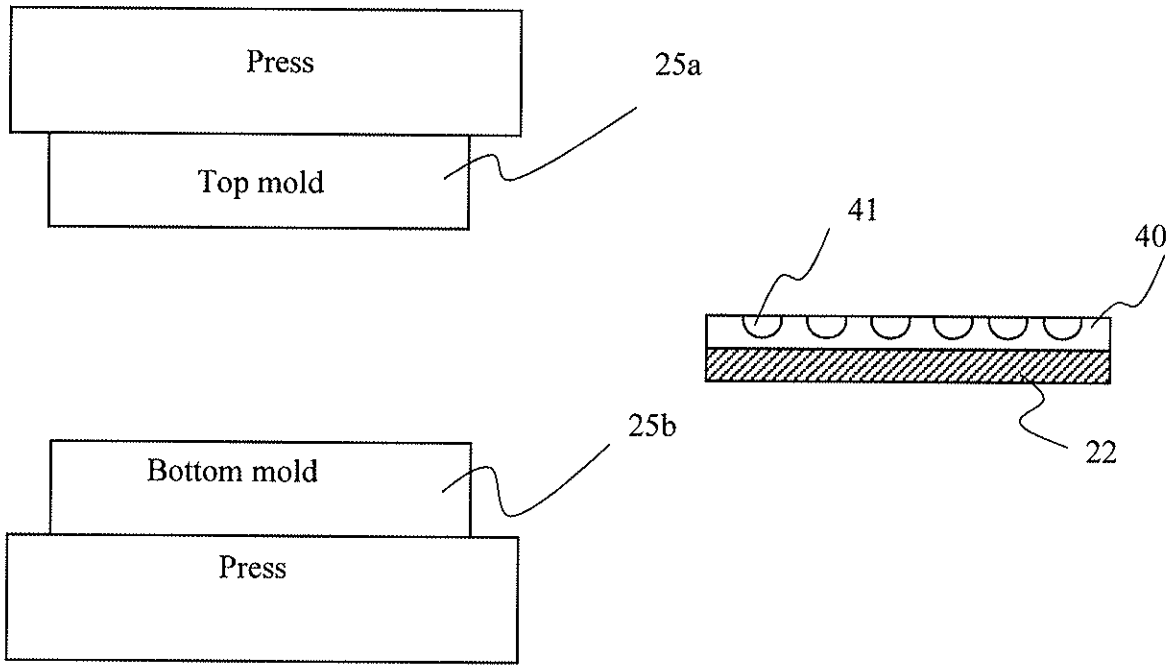


FIG 3(a)

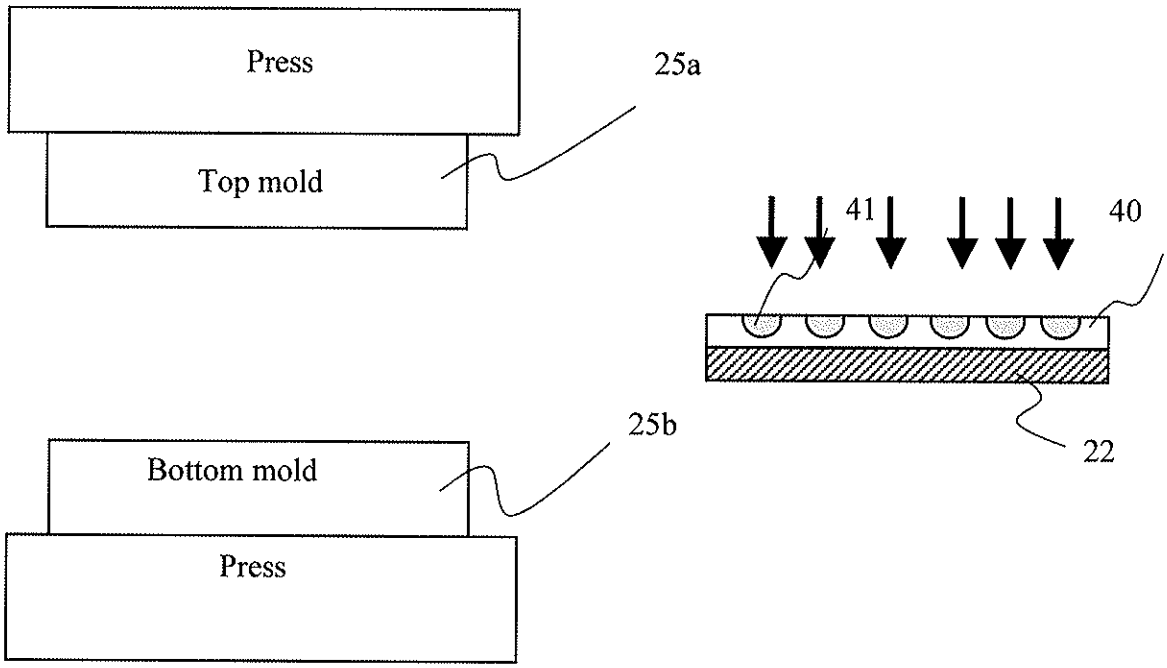


FIG 3(b)

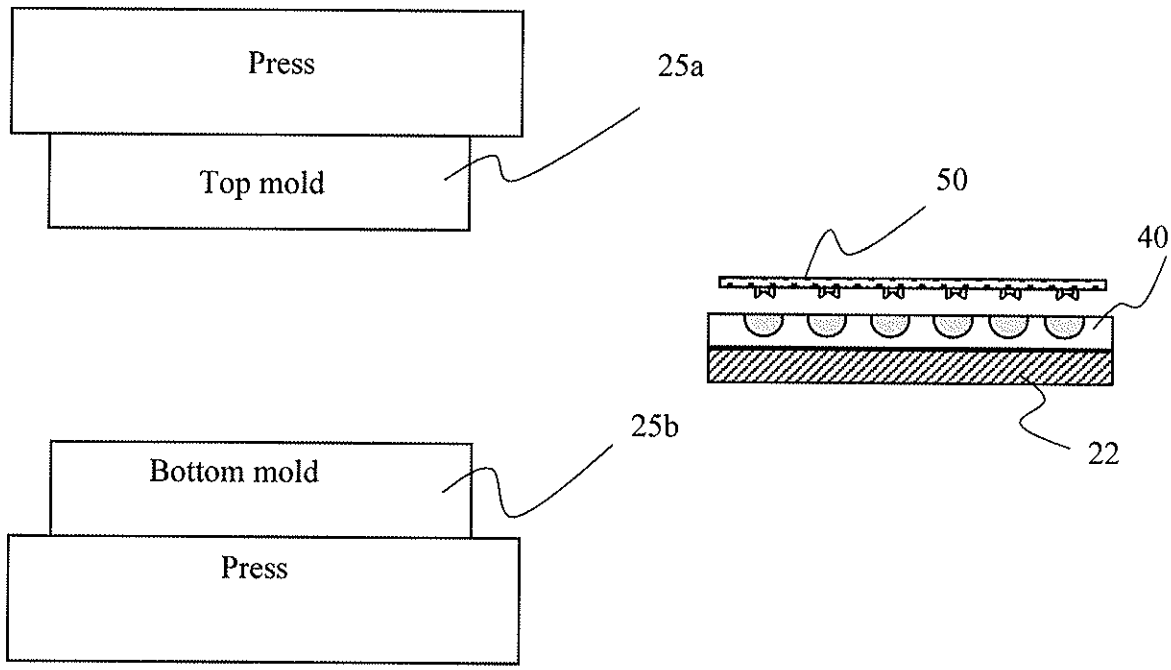


FIG 3(c)

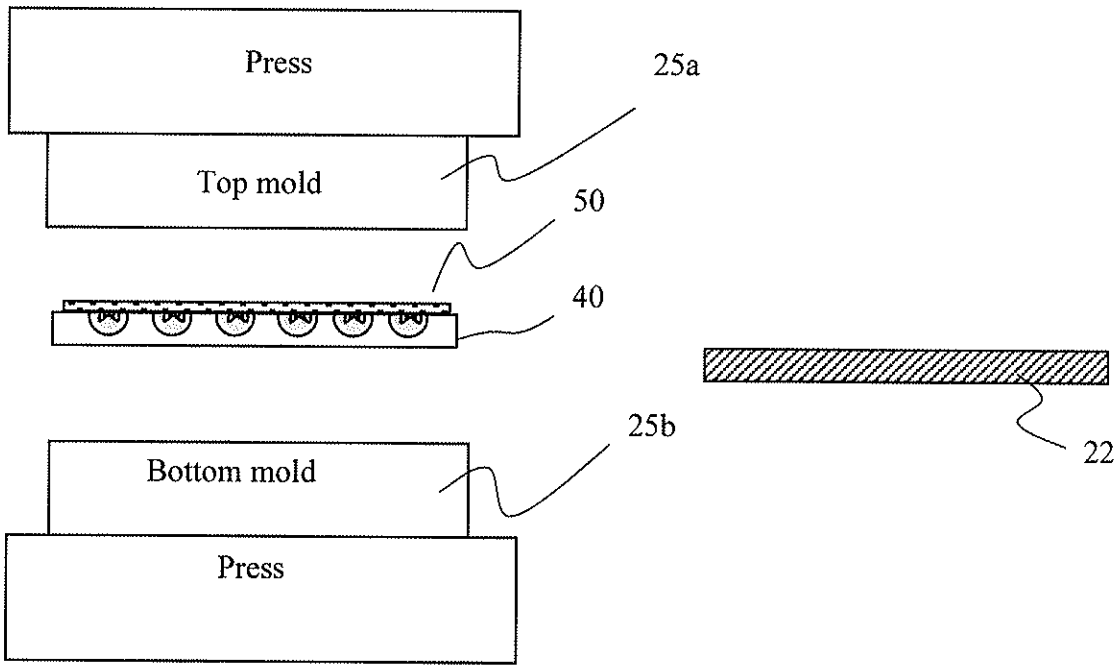


FIG 3(d)

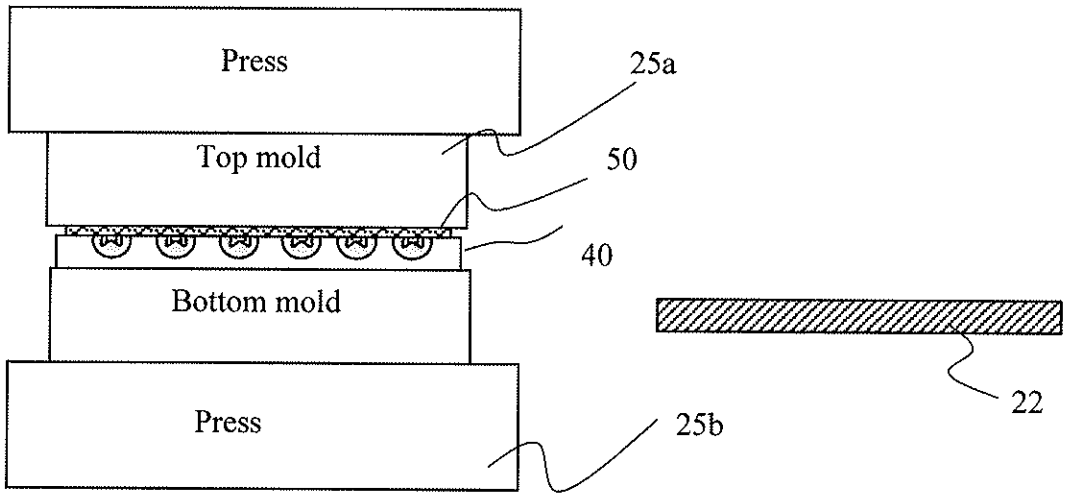


FIG 3(e)

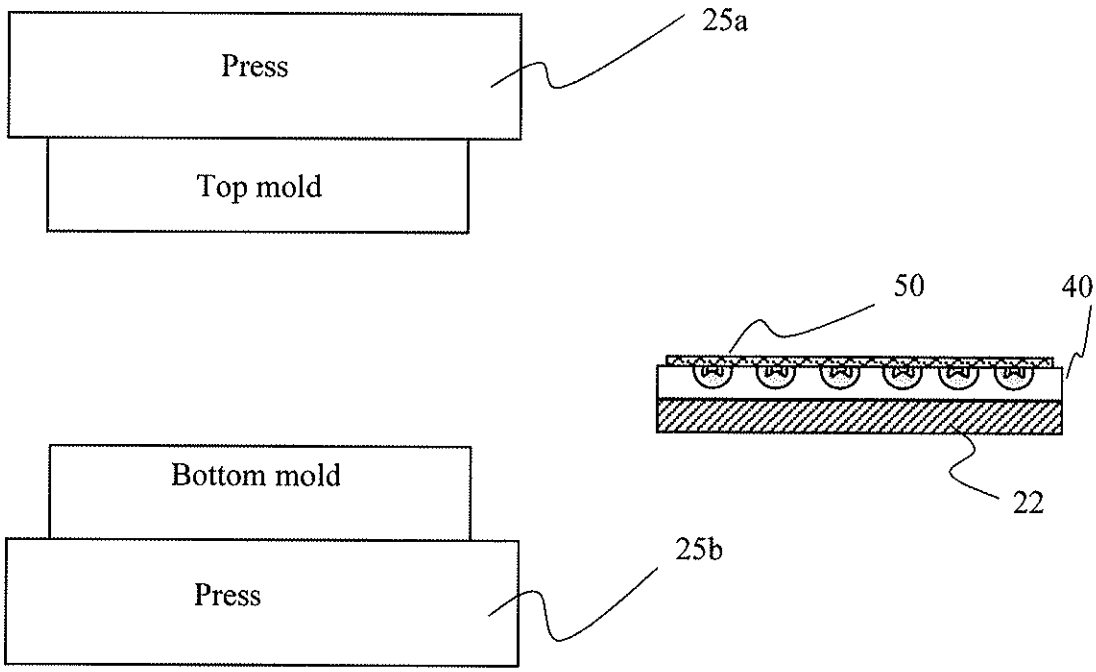


FIG 3(f)

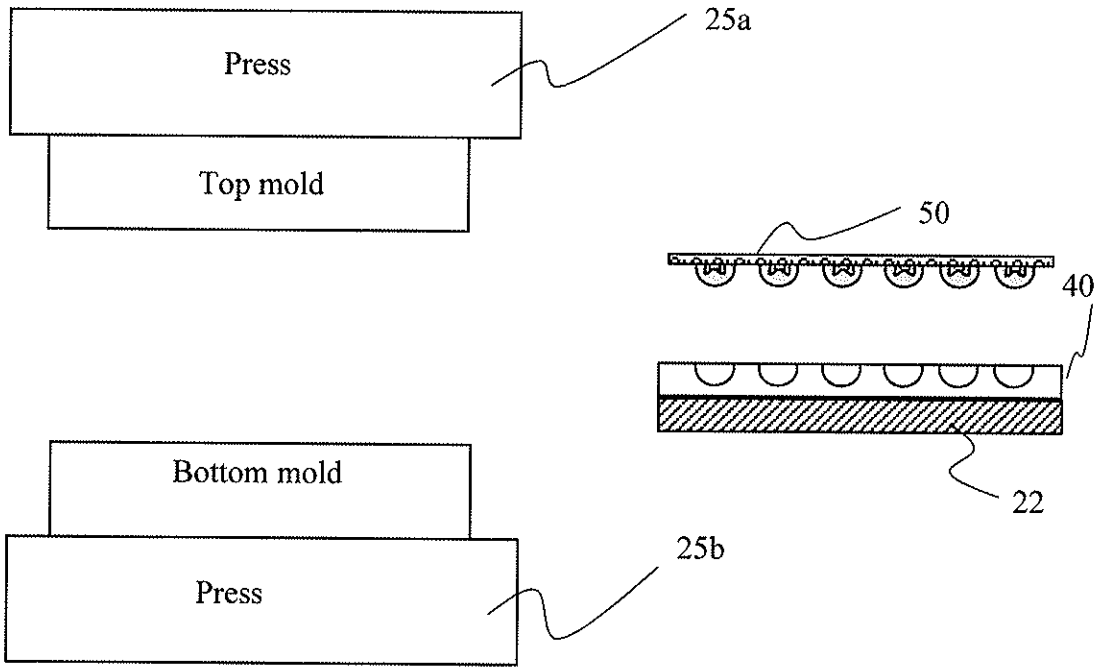


FIG 3(g)

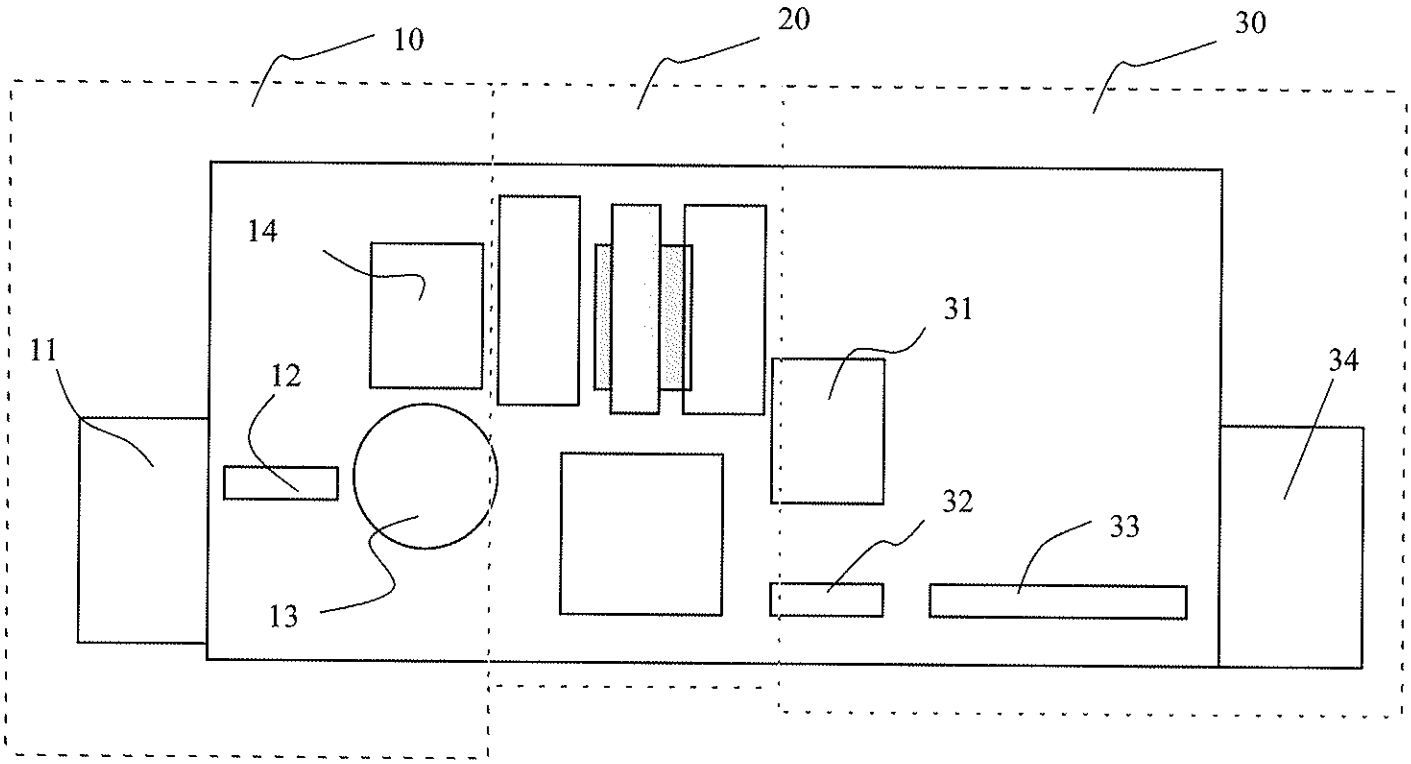


FIG 4

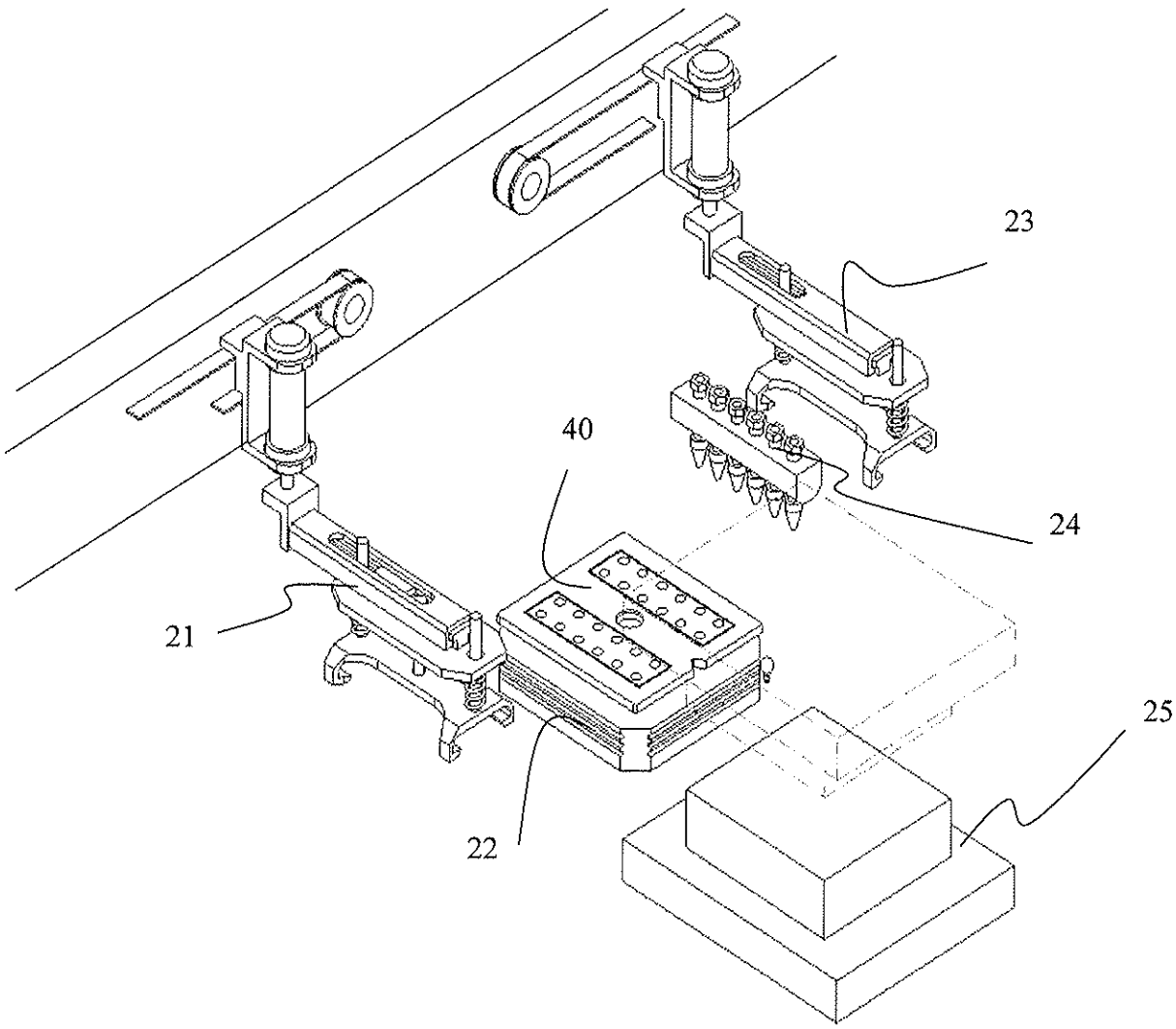


FIG 5